



40V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C
40V	8.8mΩ @ V _{GS} = 10V	64.8A
	$13m\Omega @ V_{GS} = 5V$	53.3A

Features

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching, Test in Production Ensures More Reliable And Robust End Application
- Low R_{DS(ON)} Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Data Sheet (DMTH4008LPSQ)

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- BLDC Motors
- DC-DC Converters
- Load Switch

Mechanical Data

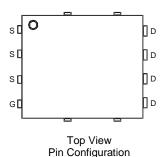
- Case: PowerDI[®]5060-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.097 grams (Approximate)



Top View



G Internal Schematic



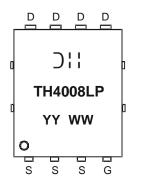
Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH4008LPS-13	PowerDI5060-8	2,500/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



PowerDI5060-8

☐ Hamufacturer's Marking

TH4008LP = Product Type Marking Code

YYWW = Date Code Marking

YY = Last Two Digits of Year (ex: 18 = 2018)

WW = Week Code (01 to 53)

PowerDI is a registered trademark of Diodes Incorporated.



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V _{DSS}	40	V
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current, V _{GS} = 10V (Note 5)	$T_A = +25^{\circ}C$ $T_A = +100^{\circ}C$	I _D	14.4 10.2	А
Continuous Drain Current, V _{GS} = 10V (Note 6)	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	I _D	64.8 45.8	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	110	Α
Maximum Continuous Body Diode Forward Current (Note 6)		Is	55.5	Α
Avalanche Current, L = 0.1mH		I _{AS}	22.7	Α
Avalanche Energy, L = 0.1mH		E _{AS}	25.7	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	P_{D}	2.99	W
Thermal Resistance, Junction to Ambient (Note 5)		$R_{ heta JA}$	50.4	°C/W
Total Power Dissipation (Note 6)	T _C = +25°C	P_{D}	55.5	W
Thermal Resistance, Junction to Case (Note 6)		$R_{ heta JC}$	2.7	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +175	°C

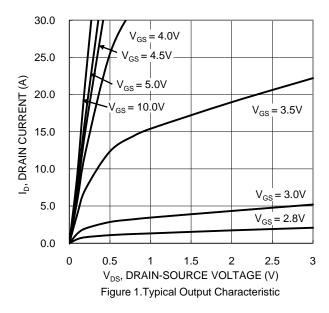
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

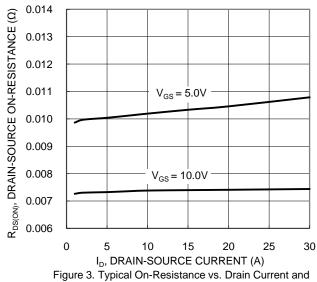
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	40	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 32V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	1	1.6	3	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance		_	7.3	8.8	_	$V_{GS} = 10V, I_D = 10A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	10	13	mΩ	$V_{GS} = 5V, I_{D} = 10A$	
Diode Forward Voltage	V_{SD}	_	0.8	1.0	V	$V_{GS} = 0V, I_{S} = 10A$	
DYNAMIC CHARACTERISTICS (Note 8)	•	•	•	•	•		
Input Capacitance	C _{iss}	_	1,088	_		V _{DS} = 20V, V _{GS} = 0V, f = 1MHz	
Output Capacitance	Coss		322	_	pF		
Reverse Transfer Capacitance	Crss	_	27	_			
Gate Resistance	Rg	_	2.6	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	7.4	_			
Total Gate Charge (V _{GS} = 10V)	Qg	_	15.3	_	nC	V 20V I 40A	
Gate-Source Charge	Q _{qs}	_	2.4	_	IIC IIC	$V_{DS} = 20V, I_{D} = 10A$	
Gate-Drain Charge	Q_{gd}	_	3.4	_			
Turn-On Delay Time	t _{D(ON)}	_	4.3	_		$V_{DD} = 20V, V_{GS} = 10V,$ $I_{D} = 10A, R_{G} = 6\Omega$	
Turn-On Rise Time	t _R	_	7.5	_			
Turn-Off Delay Time	t _{D(OFF)}	_	16.7	_	ns		
Turn-Off Fall Time	t _F	_	5.8	_	1		
Body Diode Reverse Recovery Time	t _{RR}	_	20.2	_	ns , , , , , , , , , , , , , , , , , , ,		
Body Diode Reverse Recovery Charge	Q _{RR}	_	8.9	_	nC	$I_F = 10A$, di/dt = 100A/ μ s	

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
- S. Device invalided of a standard of sociality 25 separation of the exposed drain pad).
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.









Gate Voltage

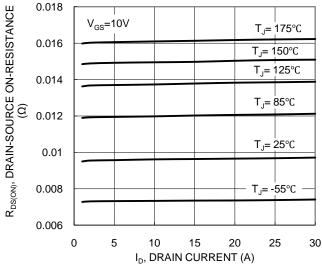
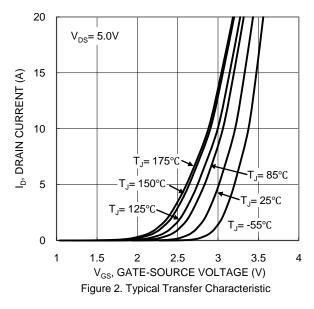
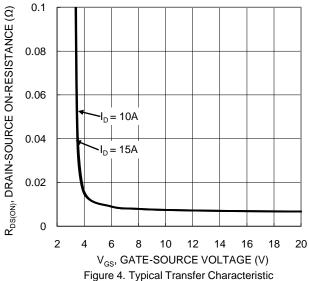


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





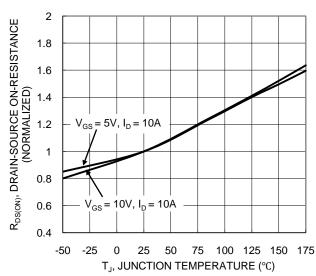


Figure 6. On-Resistance Variation with Temperature





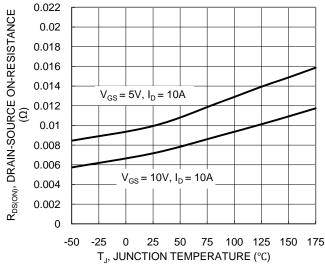
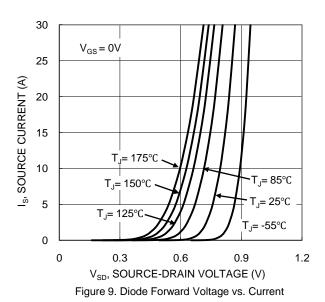
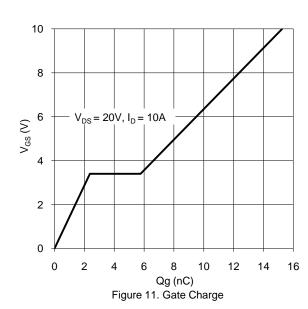
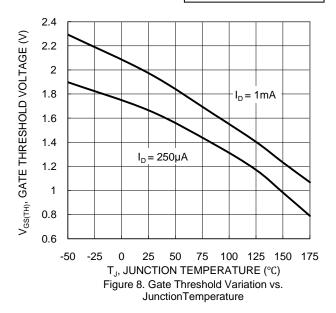
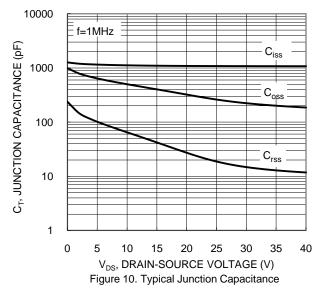


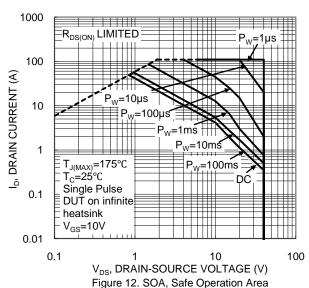
Figure 7. On-Resistance Variation with Temperature













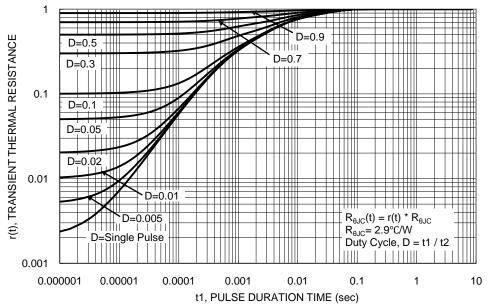


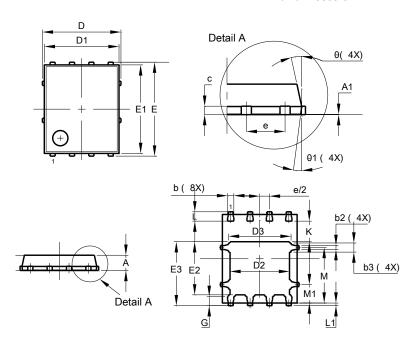
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8

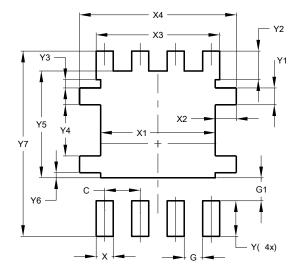


PowerDI5060-8					
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A1	0.00	0.05	-		
b	0.33	0.51	0.41		
b2	0.200	0.350	0.273		
b3	0.40	0.80	0.60		
С	0.230	0.330	0.277		
D		5.15 BSC	;		
D1	4.70	5.10	4.90		
D2	3.70	4.10	3.90		
D3	3.90	4.30	4.10		
Е	(6.15 BSC	;		
E1	5.60	6.00	5.80		
E2	3.28	3.68	3.48		
E3	3.99	4.39	4.19		
е	1.27 BSC				
G	0.51	0.71	0.61		
K	0.51	-	-		
L	0.51	0.71	0.61		
L1	0.100	0.200	0.175		
M	3.235	4.035	3.635		
M1	1.00	1.40	1.21		
Θ	10°	12°	11°		
Θ1	6°	8°	7°		
All	All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8



Dimensions	Value (in mm)			
С	1.270			
G	0.660			
G1	0.820			
Х	0.610			
X1	4.100			
X2	0.755			
Х3	4.420			
X4	5.610			
Y	1.270			
Y1	0.600			
Y2	1.020			
Y3	0.295			
Y4	1.825			
Y5	3.810			
Y6	0.180			
Y7	6.610			



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