

Features

- Trench Power LV MOSFET Technology
- · Excellent Package for Heat Dissipation
- High Density Cell Design for Low R_{DS(on)}
- · Epoxy Meets UL 94 V-0 Flammability Rating
- · Moisture Sensitivity Level 1
- Halogen Free Available Upon Request By Adding Suffix "-HF"
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

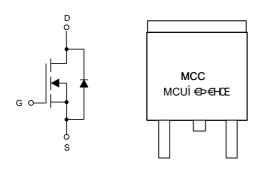
- Operating Junction Temperature Range : -55°C to +175°C
- Storage Temperature Range: -55°C to +175°C
- Thermal Resistance: 3.3°C/W Junction to Case (Note 1)

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage		V _{DS}	30	V
Gate-Source Volltage		V _{GS}	±20	V
Continuous Drain Current	T _C =25°C	I _D	80	Α
	T _C =100°C		56	Α
Pulsed Drain Current (Note 2)		I _{DM}	190	Α
Single Pulse Avalanche Energy ^(Note 3)		E _{AS}	230	mJ
Total Power Dissipation	T _C =25°C	P _D	45	W
	T _C =100°C		22.5	W

Note:

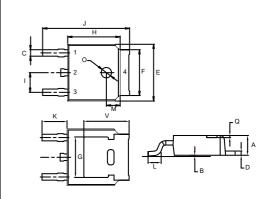
- $1.R_{\theta JA}$ is the Sum of the Junction-to-Case and Case-to-Ambient Thermal Resistance, Where the Case Thermal Reference is Defined as the Solder Mounting Surface of the Drain Pins. $R_{\theta JC}$ is Guaranteed by Design, While $R_{\theta JA}$ is Determined by the Board Design. The Maximum Rating Presented Here is Based on Mounting on a 1 in Pad of 2oz Copper.
- 2.Pulse Test: Pulse Width≤300µs,Duty Cycle ≤2%.
- $3.T_J=25$ °C, $V_{DS}=30V$, $V_{DD}=25V$, $V_{GS}=10V$, L=1mH.

Internal Structure and Marking Code



N-CHANNEL MOSFET

DPAK(TO-252)



- Gate
- 2,4. Drain
 - 3. Source

DIMENSIONS					
DIM INCHES		MM		NOTE	
DIIVI	MIN	MAX	MIN	MAX	NOTE
Α	0.087	0.094	2.20	2.40	
В	0.000	0.005	0.00	0.13	
С	0.026	0.034	0.66	0.86	
D	0.018	0.023	0.46	0.58	
E	0.256	0.264	6.50	6.70	
F	0.201	0.215	5.10	5.46	
G	0.190		4.83		TYP.
Н	0.236	0.244	6.00	6.20	
ı	0.086	0.094	2.18	2.39	
J	0.386	0.409	9.80	10.40	
K	0.1	14	2.9	90	TYP.
L	0.055	0.067	1.40	1.70	
M	0.0	63	1.6	30	TYP.
0	0.043	0.051	1.10	1.30	
Q	0.000	0.012	0.00	0.30	
V	0.2	11	5.3	35	TYP.

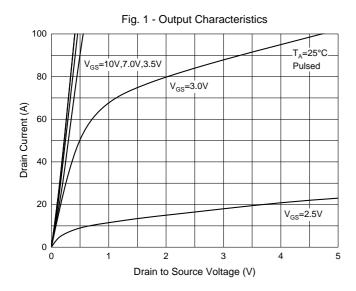


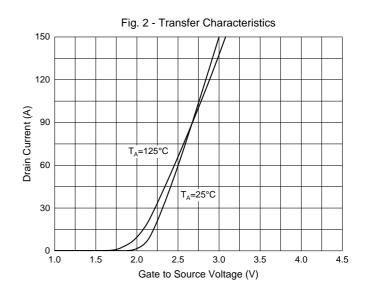
Electrical Characteristics @ 25°C (Unless Otherwise Specified)

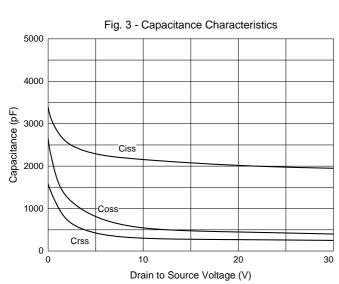
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static Characteristics			I	1		
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	30			V
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V			1	μA
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1	1.5	2.5	V
Drain-Source On-Resistance		V _{GS} =10V, I _D =20A		4.2 5.5		
	$R_{DS(on)}$	V _{GS} =4.5V, I _D =15A		5.7	8	- mΩ
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =20A			1.2	V
Continuous Body Diode Current	Is				80	Α
Dynamic Characteristics						
Input Capacitance	C _{iss}			2150		pF
Output Capacitance	C _{oss}	V _{DS} =15V,V _{GS} =0V,f=1MHz		435		
Reverse Transfer Capacitance	C _{rss}			252		
Total Gate Charge	Qg			52.8		
Gate-Source Charge	Q _{gs}	V _{DS} =15V,V _{GS} =10V,I _D =20A		12.3		nC
Gate-Drain Charge	Q_{gd}			10.8		
Turn-On Delay Time	t _{d(on)}			9		
Turn-On Rise Time	t _r	V_{GS} =10V, V_{DD} =20V, I_{D} =2A, R_{L} =1 Ω		15.5		ns
Turn-Off Delay Time	t _{d(off)}	R_{GEN} =3 Ω		29		
Turn-Off Fall Time	t _f			9		
Reverse Recovery Time	t _{RR}	L =20A di/dt=100A/up		27		ns
Reverse Recovery Charge	Q_{RR}	I _F =20A, di/dt=100A/μs		28		nC

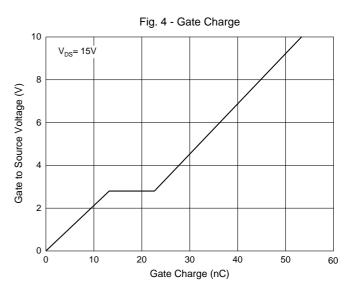


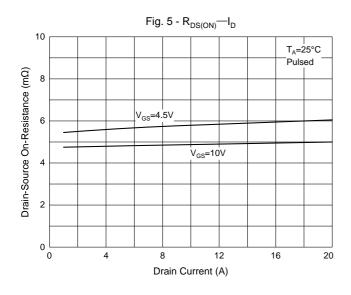
Curve Characteristics

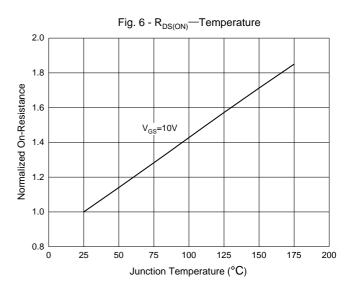














Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 2.5Kpcs/Reel

Note: Adding "-HF" Suffix for Halogen Free, eg. Part Number-TP-HF

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