



DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
	150mΩ @ V _{GS} = -4.5V	-1.8A
-20V	200mΩ @ V _{GS} = -2.5V	-1.6A
	240mΩ @ V _{GS} = -1.8V	-1.4A

Description

This new generation MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$, and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- General Purpose Interfacing Switch
- Power Management Functions

D₁ S₂ D₂ G₁ S₂ G₂ Top View Pin-Out

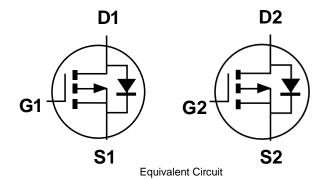


Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Tin Finish Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.013 grams (Approximate)



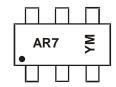
Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2110UVT-7	TSOT26	3,000/Tape & Reel
DMP2110UVT-13	TSOT26	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



AR7 = Product Type Marking Code YM = Date Code Marking $Y or \overline{Y} = Year (ex: F = 2018)$ M = Month (ex: 9 = September)

Date Code Key

Year	2018	2019	20	020	2021	2022	2	2023	2024	202	25	2026
Code	F	G		Н		J		K	L	N	1	N
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V_{DSS}	-20	V	
Gate-Source Voltage	V_{GSS}	±10	V	
Drain Current (Note 5) Continuous	$T_A = +25$ °C $T_A = +70$ °C	I _D	-1.8 -1.4	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	I _{DM}	-15	А	
Body-Diode Continuous Current (Note 5)	I _S	-0.7	Α	

Thermal Characteristics (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

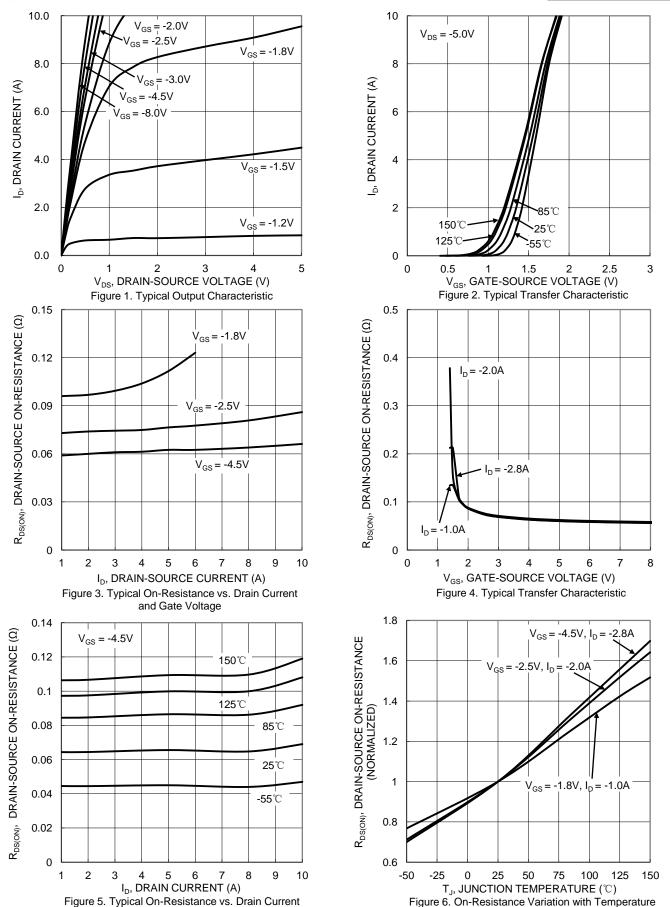
Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		P _D	0.74	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	168	°C/W
Total Power Dissipation (Note 6)		P _D	0.74	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{0JA}	1.01	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	_	-1.0	μA	V _{DS} = -16V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	-0.45	_	-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
			_	150		$V_{GS} = -4.5V$, $I_{D} = -2.8A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	_	200	mΩ	$V_{GS} = -2.5V$, $I_D = -2.0A$	
	, ,			240		$V_{GS} = -1.8V, I_{D} = -1.0A$	
Diode Forward Voltage	V_{SD}	_	_	-1.0	V	$V_{GS} = 0V, I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	_	443	—	pF	N	
Output Capacitance	Coss	_	59	_	pF	$V_{DS} = -6V, V_{GS} = 0V$ - f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	47	_	pF	1 - 1.000112	
Gate Resistance	R_{G}	_	8.5	_	Ω	$V_{GS} = 0V, V_{DS} = 0V, f = 1.0MHz$	
Total Gate Charge	Qg	_	6.0	_	nC		
Gate-Source Charge	Q _{gs}	_	0.6	_	nC	$V_{GS} = -4.5V, V_{DS} = -10V, I_{D} = -3A$	
Gate-Drain Charge	Q_{qd}	_	1.8	_	nC		
Turn-On Delay Time	t _{D(ON)}	_	4.0	_	ns		
Turn-On Rise Time	t _R	_	3.7	_	ns	$V_{DS} = -10V, V_{GS} = -4.5V,$	
Turn-Off Delay Time	t _{D(OFF)}	_	24.5	_	ns	$R_L = 10\Omega, R_G = 1.0\Omega, I_D = -1A$	
Turn-Off Fall Time	t _F	_	9.5	_	ns	7	
Reverse Recovery Time	t _{RR}	_	8.3	_	ns	I _F = -1.0A, di/dt = 100A/µs	
Reverse Recovery Charge	Q _{RR}	_	2.0	_	nC	I _F = -1.0A, di/dt = 100A/µs	

Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.





and Temperature



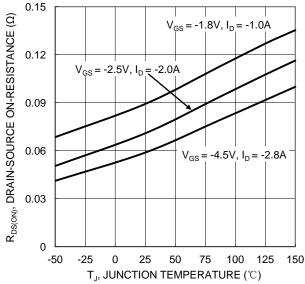


Figure 7. On-Resistance Variation with Temperature

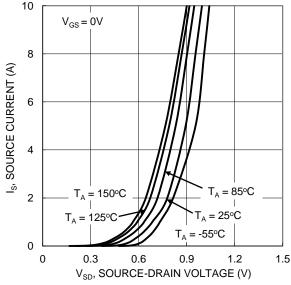
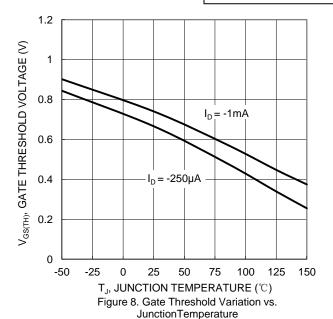
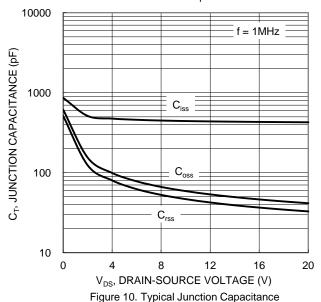
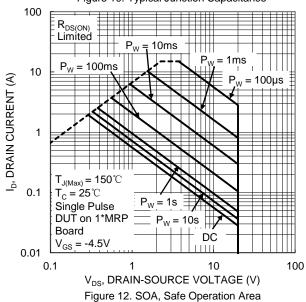


Figure 9. Diode Forward Voltage vs. Current 10 8 6 $V_{GS}(V)$ 4 $V_{DS} = -10V, I_{D} = -3.0A$ 2 0 2 0 6 8 10 12 14 Q_q (nC) Figure 11. Gate Charge









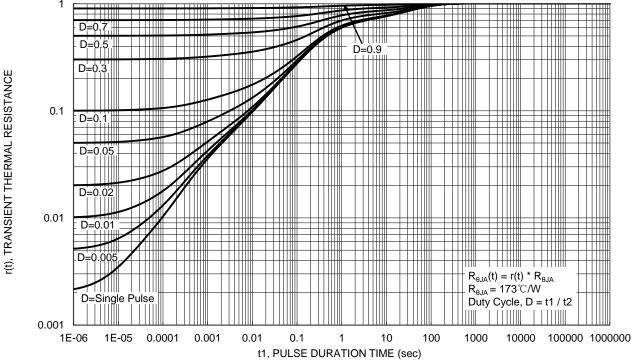


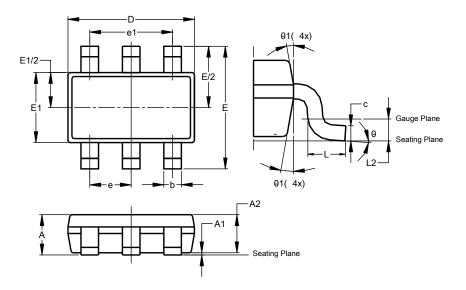
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TSOT26

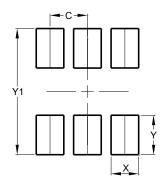


TSOT26							
Dim	Min	Max	Тур				
Α	-	1.00	-				
A1	0.010	0.100	_				
A2	0.840	0.900	-				
D	2.800	3.000	2.900				
Е	2	2.800 BS	C				
E1	1.500	1.700	1.600				
b	0.300	0.450	-				
C	0.120	0.200	-				
е	0.950 BSC						
e1	1	1.900 BSC					
Г	0.30	0.50	1				
L2	0.250 BSC						
θ	0°	8°	4°				
θ1	4°	12°	-				
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

TSOT26



Dimensions	Value (in mm)
С	0.950
Х	0.700
Y	1.000
Y1	3.199



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