



**Quad Channel Digital PWM/PFM
Programmable Power Management System**

REVISION HISTORY

Revision	Date	Description
1.0.0	10/04/2012	Initial Release of Data Sheet
1.0.1	10/04/2012	Eliminated "Native GH, GL Rise and Fall Time" typical specification.
1.0.2	02/06/19	Updated to Maxlinear logo. Updated format and Ordering Information. Clarified VOUT Regulation Accuracy High Output Range conditions. Added missing units for ENABLE pin Leakage Current and Current Sense ADC Range. Corrected 607ns timing resolution in PWM Loop section to 607ps. Corrected pin names in LDOs section. Updated Power OK description in Configuring GPIO / PSIOs section. Updated PGOOD and OVP equation format. Added "PA5" naming to PowerArchitect™ 5.0.



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