Evaluation Board and Kit

Getting Started

TLE985x EvalBoard TLE9855 EvalKit



April 2019

Agenda





TLE985x Overview



Infineon Embedded Power ICs Product Portfolio based on Arm[®] Cortex[®]-M processor



Evaluation Board and Kit Overview



TLE985x – Evaluation Board

- > 2-Phase N-MOS Bridge
- > Single Shunt in GND path
- > Integrated LIN transceiver
- > 4 push buttons
- > Debug LEDs
- Onboard Segger J-Link Debugger
- > EvalBoard-PC connector: USB
- > uIO stick connector

TLE9855 – Evaluation Kit

- > 2-Phase N-MOS Bridge
- Single Shunt in GND path
- Integrated LIN transceiver
- > Debug LEDs
- Onboard Segger J-Link Debugger
- EvalKit-PC connector: USB





TLE985x EvalBoard Overview





TLE9855 EvalKit Overview



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TLE985x: Documentation

Collaterals and Brochures	 Product Brief Selection Guides Motor Drive eBook 	- Link to family page
Technical Material	 Datasheet Application Notes Getting Started PCB Design Data User Manual 	- Link to Documents
Evaluation Boards	 Evaluation Board Evaluation Kit 	 Link to Boards
Software & Tools	 Config Wizard Keil µVision5 Software Examples Infineon Toolbox 	 Link to Software & Tools

Support Online tools and services



> infineon.com/embeddedpower



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Toolchain installation: General Overview



Infineon Embedded Power ICs are supported by a complete development tool chain provided by Infineon and third party vendors. The tool chain includes compilers, debuggers, evaluation boards, LIN low level drivers and configuration tools as well as a variety of example software code.



Development Kit (SDK) is a low level driver library which can be downloaded within "Keil µVision" via the "Pack Installer".

debugger.

environment which consists of

code editor, compiler and

stand-alone debuggers.

designed to install and use

Infineon plugins and tools.

Toolchain installation: 1/4





Toolchain installation: 2/4



Infineon Config Wizard

Configuration of chip modules

Device description for TLE985x included

Installation from Infineon Toolbox

TLE985x supported by Keil µVision 5

Boot Strap Loader SCL	J PMU	MON	Interrupt	PORT	ADC1	ADC2	HS	BDRV	CCL
Structure			S	ettings					
✓ VDDEXT									
Turned on]					
Cyclic Sense enab	le		L						
Fail Interrupt Wake Up Sources									
 Cyclic Wake enab 	le		\sim	1					
> Cyclic Sense enab	le]					
> LIN wake up]					
> MON wake up > GPIO wake up									
 Wakeup Interrupt 			Г	1					
> Reset Settings				-					
> Power Saving Modes									
Supply Interrupt									
								_	
<									,
<									
<									
< Log Script Loaded configuration: cc	onfia.icwp. N	lo headerf	îles written, T i	o update	configura	ition plea	se save	e this file	1
< Log Script Loaded configuration: cc	nfig.icwp. N	lo headerf	îles written. T e	o update	configura	ition plea	se save	e this file	!
< Log Script Loaded configuration: cc	nfig.icwp. N	lo headerf	îles written. T o	o update	configura	ition plea	se save	e this file	!
< Log Script Loaded configuration: cc	nfig.icwp. N	io headerf	îles written. To	o update	configura	ition plea	se save	e this file	!
< Log Script Loaded configuration: cc	nfig.icwp. N	lo headerf	îles written. T i	o update	configura	ation plea	se save	e this file	!
< Log Script Loaded configuration: cc	nfig.icwp. N	lo headerf	iles written. To	o update	configura	ition plea	se save	e this file	!





^(*) For more details about the Infineon Toolbox installation, please read the Installation Manual.







Segger J-LINK-Lite driver:

- Driver for 'on-board' or 'stand-alone' debugger
- Install driver from: <u>https://www.segger.com/jlink-</u> <u>software.html</u>

J-Link / J-Trace Adap	ter	SW Devi	ce			
SN: 591073990	-		IDCODE	Device Name		Move
Device: J-Link Lite-X	KMC4200 Rev.1	SWD	Ox0BB11477	ARM CoreSight SW	I-DP	Up
HW : V1.00	dll : V6.00a					Down
FW : J-Link Lite-XI	MC4200 Rev.1 ci	-	1			
Port:	Max Clock:	C Auto	matic Detection	ID CODE:		
ISW 💌	5 MHz 💌	C Man	ual Configuration	Device Name:		
	Auto Clk	Add	Distante I I Inc.	101		
- Connect & Reset On	tions			a Options Do	woload Optic	
Connect & Reset Op Connect: with Pre-re	tions Reset: Nor nect	mal	Cach	e Options Do ache Code ache Memory	wnload Optic Verify Code Download to	ons Download o Flash
Connect & Reset Op Connect: with Pre-re Reset after Com	tions esc Reset: Nor nect TCP/IP TCP/IP	mal	Cach	e Options Do ache Code ache Memory	wnload Optic Verify Code Download tr Mis	ons Download o Flash c
Connect & Reset Op Connect: with Pre-re Reset after Conn Interface	tions see ▼ Reset: Nor nect P TCP/IP Network Se IP-Address	mal	Paete Up Cach ♥ C ♥ C Port (Au	e Options Do ache Code ache Memory L to: 0) Autodete	wnload Optic Verify Code Download tr Ct	ons Download o Flash c JLink Info
Connect & Reset Op Connect: with Pre-re Reset after Conn Interface USB © TCP/II Scan	rtions see ▼ Reset: Nor nect P TCP/IP Network Se IP-Address 127	mal ettings	Petere Up Cach ♥ C Port (Au . 1 : 0	e Options Do ache Code ache Memory Code	wnload Optic Verify Code Download tr Mis ct	ons Download o Flash c JLink Info
Connect & Reset Op Connect: with Pre-re Reset after Cont Interface USB C TCP/II Scan State: ready	rtions sse ▼ Reset: Nor nect P TCP/IP Network Se IP-Address 127 .	mal ettings	- Cach . 1 : 0	e Options Do ache Code ache Memory Code to: 0) Autodete	wnload Optic Verify Code Download tr ct	ons Download o Flash c JLink Info ILink Cmd



Toolchain installation: 4/4

PACK-file TLE985x for μ Vision5:

- Device database for all TLE985x variants
- Device support for flashing/erasing TLE985x
- Device description for TLE985x for Config Wizard
- Includes SDK (Software Development Kit)
- If the TLE985x SDK is not available in the pack installer, you can load it from a local file using the "File->Import" menu.



Agenda







1) Create a new Project

- > Open Keil mdk
- Go to → Project
 → new µVision Project
- > Name project: "TLE9855QX_test"
- > Select Device:
 - Infineon
 - TLE98xx Series
 - TLE985x Series
 - TLE9855QX



Device	Software Packs	×	
Vendor:	Infineon		
Device:	TLE9855QX		
Toolset:	ARM		
Search:			
	,	Description:	
	** TLE98xx Series ** TLE98xx Series ** TLE985S Series ** TLE985S10XW ** TLE985S0X ** TLE985S0X ** TLE985S50X ** TLE985S50X ** TLE987x Series	The TLE985x series is designed for motor drive applications with LIN/PWM interface - 32bit ARM Contex-M0 Core - Unra low power consumption - Nested Vectored Interrupt Controller (NVIC) - Integrated LIN transerver (selected derivates) - Integrated IOSFET Driver and Charge Pump for H-Bridge - Current Sense Amplifier	~
		OK Cancel	Help



2) Configuration of Run-Time Environment

- > Expand: "Device"
 - Check: Startup
 - Check: ConfigWizard
- "Sel." window background is orange
- > Press: "Resolve"
- "Sel." window
 background is now green
- > Expand "SDK"
 - Check "TIMER2x"
- Continue with "OK"

Software Component	Sel.	Variant		Version	Description
🗉 🚸 CMSIS					Cortex Microcontroller Software Interface Components
🖶 💠 CMSIS Driver					Unified Device Drivers compliant to CMSIS-Driver Specifications
🗉 💠 Compiler		ARM Compiler		1.4.0	Compiler Extensions for ARM Compiler 5 and ARM Compiler 6
😑 💠 Device					Startup, System Setup
Startup				1.0.1	System Startup for Infineon TLE984x device series
ConfigWizard	V			1.8.7	Infineon ConfigWizard Configuration File
⊞ SDK					
🗉 💠 File System		MDK-Plus	\sim	6.10.0	File Access on various storage devices
🗄 💠 Graphics		MDK-Plus	\sim	5.46.5	User Interface on graphical LCD displays
🗈 💠 Network		MDK-Plus	\sim	7.8.0	IPv4 Networking using Ethernet or Serial protocols
🖬 💠 USB		MDK-Plus	~	6.12.4	USB Communication with various device classes
Validation Output Infineon::Device:Startup -require Device:SDK:SCU		Descriptio Additional Select com	n softw npone	are compo nt from list	nents required
Infineon::Device:SDK:S0	cu	System Co	ntrol l	Jnit (SCU) d	driver for TLE984x
Manage Run-Time Environment					
Manage Run-Time Environment					
Manage Run-Time Environment		Variant		Version	Description
Manage Run-Time Environment Software Component		Variant		Version	Description Cortex Microcontroller Software Interface Components
Manage Run-Time Environment Software Component		Variant		Version	Description Cortex Microcontroller Software Interface Components Unified Device Drivers compliant to CMSIS-Driver Specifications
Manage Run-Time Environment Software Component		Variant ARM Compiler	9	Version 1.4.0	Description Cortex Microcontroller Software Interface Components Unified Device Drivers compliant to CMSIS-Driver Specifications Compiler Extensions for ARM Compiler 5 and ARM Compiler 6
Manage Run-Time Environment Software Component		Variant ARM Compiler	9	Version 1.4.0	Description Cortex Microcontroller Software Interface Components Unified Device Drivers compliant to CMSIS-Driver Specifications Compiler Extensions for ARM Compiler 5 and ARM Compiler 6 Startup. System Setup Cortex Orthon SetuPF cont J 2007; davies ordine
Manage Run-Time Environment Software Component CMSIS Driver CMSIS Driver CMSIS Driver CMSIS Driver Device Startup ConfoiMaged		Variant ARM Compiler	9	Version 1.4.0 1.0.0	Description Cortex Microcontroller Software Interface Components Unified Device Drivers compilant to CMSIS-Driver Specifications Compiler Extensions for ARM Compiler 5 and ARM Compiler 6 Startup. System Setup System Startup for Infineon TLE987x device series Driverson Conscipational Configurations File
Manage Run-Time Environment Software Component CMSIS CMSIS Driver Compiler Compiler ConfigWizard ConfigWizard Extension	<u> 1</u>	Variant ARM Compiler	9	Version 1.4.0 1.0.0 1.8.6	Description Cortex Microcontroller Software Interface Components Unified Device Drivers compilant to CMSIS-Driver Specifications Compiler Extensions for ARM Compiler 5 and ARM Compiler 6 Startup, System Startup for Infineon TLE987x device series Infineon ConfigWizard Configuration File Colora if two device in Standard
Manage Run-Time Environment Software Component ⊕ ◆ CMSIS ⊕ ◆ CMSIS Driver ⊕ ◆ Compiler ⊖ ◆ Device ↓ ◆ Startup ↓ ◆ ConfigWizard ↓ ♥ F-Stepping ♥ ♥ ♥	2 4	Variant ARM Compiler	9	Version 1.4.0 1.0.0 1.8.6 0.0.1	Description Cortex Microcontroller Software Interface Components Unified Device Drivers compliant to CMSIS-Driver Specifications Compiler Extensions for ARM Compiler 5 and ARM Compiler 6 Startup. System Setup System Startup for Infineon TLE987x device series Infineon ConfigWizard Configuration File Select if target device is BF-Stepping
Manage Run-Time Environment Software Component	2	Variant ARM Compiler	9	Version 1.4.0 1.0.0 1.8.6 0.0.1	Description <u>Cortex Microcontroller Software Interface Components</u> <u>Unified Device Drivers compliant to CMSIS-Driver Specifications</u> <u>Compiler Extensions for ARM Compiler 5 and ARM Compiler 6</u> <u>Startup. System Setup</u> <u>System Startup for Infineon TLE907x device series</u> <u>Infineon ConfigUrard Configuration File</u> <u>Select if target device is BF-Stepping</u> <u>Els. Access on unions theorem divises</u>
Manage Run-Time Environment Software Component Image Run-Strike Image CMSIS Image Run-Strike	<u>.</u>	Variant ARM Compiler MDK-Plus MDK-Plus	7	Version 1.4.0 1.0.0 1.8.6 0.0.1 6.10.0 5.46.5	Description <u>Cortex Microcontroller Software Interface Components</u> <u>Unified Device Drivers compliant to CMSIS-Driver Specifications</u> <u>Compiler Extensions for ARM Compiler 5 and ARM Compiler 5</u> <u>Startup. System Setup</u> <u>System Statup for Infineon TLE987x device series</u> <u>Infineon ConfigWizard Configuration File</u> <u>Select if target device is BF-Stepping</u> <u>File Access on various storage devices</u> <u>Low textrofrace on examinal LCD divelopment</u>
Manage Run-Time Environment Software Component CMSIS CMSIS Driver Configurat ConfigWizard Soft File System Caraphics Carap	× ×	Variant ARM Compiler MDK-Plus MDK-Plus		Version 1.4.0 1.0.0 1.8.6 0.0.1 6.10.0 5.46.5 7.9.0	Description <u>Cortex Microcontroller Software Interface Components</u> <u>Unified Device Drivers compliant to CMSIS-Driver Specifications</u> <u>Compiler Extensions for ARM Compiler 5 and ARM Compiler 6</u> <u>Startup, System Starup</u> <u>System Starup for Infineon TLE987x</u> device series <u>Infineon ConfigWizard Configuration File</u> <u>Select if target device is BF-Stepping</u> <u>File Access on various storage devices</u> <u>User Interface on graphical LCD displays</u> <u>Devi Metworken to 5 Swil toratorale</u>
Manage Run-Time Environment Software Component CMSIS CMSIS Driver CMSIS Driver Compiler ConfigWizard Startup ConfigWizard BF-Stepping Soft BF-Stepping Soft BF-Stepping Soft BF-Stepping Network Network CMSIS	V	Variant ARM Compiler MDK-Plus MDK-Plus MDK-Plus		Version 1.4.0 1.0.0 1.8.6 0.0.1 6.10.0 5.46.5 7.8.0 6.13.4	Description Cortex Microcontroller Software Interface Components Unified Device Drivers compliant to CMSIS-Driver Specifications Compiler Extensions for ARM Compiler 5 and ARM Compiler 6 Startup, System Setup System Statup for Infineon TLE987x device series Infineon ConfigWzard Configuration File Select if target device is BF-Stepping File Access on various storage devices User Interface on graphical LCD displays IPVA Networking using Ethernet or Serial protocols IUSP Computations with interest
Manage Run-Time Environment Software Component CMSIS CMSIS Driver Consigner Startup ConfigWizard BF-Stepping Startup ConfigWizard BF-Stepping Startup ConfigWizard BF-Stepping Startup Network Startup Sta	× ×	Variant ARM Compiler MDK-Plus MDK-Plus MDK-Plus MDK-Plus		Version 1.4.0 1.0.0 1.8.6 0.0.1 6.10.0 5.46.5 7.8.0 6.12.4	Description <u>Cortex Microcontroller Software Interface Components</u> <u>Unified Device Drivers compliant to CMSIS-Driver Specifications</u> <u>Compiler Extensions for ARM Compiler 5 and ARM Compiler 6</u> <u>Startup, System Setup</u> <u>System Startup for Infineon TLE907x device series</u> <u>Infineon ConfigUrard Configuration File</u> <u>Select if target device is BF-Stepping</u> <u>File Access on various storage devices</u> <u>User Interface on graphical LCD displays</u> <u>IPv4 Networking using Ethernet or Serial protocols</u> <u>USB Communication with various device classes</u>
Manage Run-Time Environment Software Component CMSIS CMSIS Driver ConfigUizard ConfigUizard E-Stepping SDK Graphics ConfigUizard Config		Variant ARM Compiler MDK-Plus MDK-Plus MDK-Plus MDK-Plus		Version 1.4.0 1.0.0 1.8.6 0.0.1 6.10.0 5.46.5 7.8.0 6.12.4	Description Cortex Microcontroller Software Interface Components Unified Device Drivers compliant to CMSIS-Driver Specifications Compiler Extensions for ARM Compiler 5 and ARM Compiler 6 Startup. System Setup System Starup for Infineon TLE987x device series Infineon ConfigWizard Configuration File Select if target device is BF-Stepping File Access on various storage devices User Interface on graphical LCD displays JUSE Communication with various device classes USB Communication with various device classes
Manage Run-Time Environment Software Component CMSIS CMSIS Driver CMSIS Driver ConfigWizard ConfigWizard ConfigWizard BF-Stepping ConfigWizard File System Software Network Software USB		Variant ARM Compiler MDK-Plus MDK-Plus MDK-Plus MDK-Plus		Version 1.4.0 1.0.0 1.8.6 0.0.1 6.10.0 5.46.5 7.8.0 6.12.4	Description Cortex Microcontroller Software Interface Components Unified Device Drivers compliant to CMSIS-Driver Specifications Compiler Extensions for ARM Compiler 5 and ARM Compiler 6 Startup, System Setup System Startup for Infineon TLE987x device series Infineon Confylikard Configuration File Select if target device is BF-Stepping File Access on various storage devices User Interface on graphical LCD displays IPv4 Networking using Ethernet or Serial protocols USB Communication with various device classes
Image Run-Time Environment Software Component Image Run-Time Environment Image Run		Variant ARM Compiler MDK-Plus MDK-Plus MDK-Plus MDK-Plus Descriptio		Version 1.4.0 1.8.6 0.0.1 6.10.0 5.46.5 7.8.0 6.12.4	Description Cortex Microcontroller Software Interface Components Unified Device Drivers compliant to CMSIS-Driver Specifications Compiler Extensions for ARM Compiler 5 and ARM Compiler 6 Startup, System Statup for Infineon TLE987x device series Infineon ConfigWizard Configuration File Select if target device is BF-Stepping File Access on various storage devices User Interface on graphical LCD displays IPv4 Networking using Ethernet or Serial protocols USB Communication with various device classes



3) Using 'Main' template

- > Expand: "Target 1"
- Right click on:
 "Source Group 1"
- Choose: Add New Item to Group "Source Group 1"
- Choose "User Code Template"
- > Expand "Device"
- Choose:"Startup"
- > Continue with "Add"



	.c)		
	Component	t Name	
C++ Fi	e (.cpp)	ce	
A Asm Fi	ie (.s)	up with full SDK Complete Main	
h Heade	r File (.h)		
Text Fi	le (.bd)		
🖳 Image	File (.*)		
User C	ode Template		
Type:	User Code Template		
Name:	main.c		
realized and a			



4) Using Config Wizard V2

- Open Config Wizard by choosing Tools > Config Wizard V2
- Config Wizard will open in a separate window







5) Using Config Wizard V2: Timer2 Configuration

- > Open "Timer 2x" section
- Enable "Configure Timer" checkbox
- Go to:
 "Reload / Capture Register"
 - Enter "1000" µs
- > Go to: "Interrupt"
 - Enable Overflow Interrupt
 - Type "task_1ms" in "Call Back" line

boot strap toader SCO Philo Philo	Interrupt PORT ADC1 ADC2 HS BDRV C
Structure	Settings
 Configure Timer2 	
Timer2 Clock	3.33MHz
Max. Period	19660.5us
Clock Setting	
> Mode Select	_
External Capture/Reload Event Enable	
External Start Enable	
 Timer Register Delend (Contract Denister) 	
 Reload / Capture Register 	0
 as time [us] 	
Time	1000 00 us
Reload Value	OvE2EAticks
	UNI ZI AUCIS
External Interrupt	
 Overflow / Underflow Interrupt 	
Enable Interrupt	
Call Back	task_1ms
Con Duck	_
> Pin Select	
Enable Interrupt Call Back	task_1ms



6) Using Config Wizard V2: Port Configuration

- > Select "PORT" section
- Go to the "P0.2" section
- Configure pin to "Output" mode

P0.0		P0.1	•	P0.2	
Structure Input > Output Pull mode	Settings	Structure Input > Output Pull mode	Settings None	Structure Input > Output Pull mode	Settings None
P1.0		P1.1		P1.2	
Structure Input > Output Pull mode	Settings O None V	Structure Input > Output Pull mode	Settings O None	Structure Input > Output Pull mode	Settings O None
P2.0		P2.1		P2.2	
Structure Input Disabled Pull mode	Settings O None T	Structure Input Disabled Pull mode	Settings O None	Structure Input Disabled Pull mode	Settings O None V







7) Finish Code in "main.c"

- > Go to Keil MDK
- Start Timer2 before the "for(;;)" loop
- Write function definition of interrupt callback
- Use API function "PORT_ChangePin()"
- > "Save" and "Build" project







8) Power up Evaluation board

- > Connect micro USB cable
- > Supply board via banana jacks (VBAT, GND)
- > Debug LED lights up

n 100	

9)	Connect Debugger	Cortex JLink/JTrace Target Driver Setup Debug Trace Flash Download J-Link / J-Trace Adapter SW Device SN FSU723991 Upcope	
>	Go to	Junk Lite XMC4200 Rev.1 HW : V1.00 FW : Unik Lite XMC4200 Rev.1 FW : V1.00 FW : Unik Lite XMC4200 Rev.1	eSight SW-DP Up Down
>	Choose:	SW V SHADDARE DEPENDENT DE	IR ler:
	Debug->use: J-Link Uinker Debug Utilities Use: J-LINK/J-TRACE Contex	Connect & Reset Options Connect: [with Pre-rese ▼ Reset: [Normal ▼] ♥ Cache Options ♥ Cache Code ♥ Cache Memo	Download Options
	 Go to "Settings" 	Interface TCP/IP IP-Address Port (Auto: 0) Scan 127.0.0.1	Autodetect JLink Info
>	SWD connection established when "IDCODE" is visible	State: ready	







11) Use Runtime Debug

- > Enter "Debug Session"
- Left click at the dark grey area left of the code, to place a "breakpoint"
- > Hit "Run" or press "F5" to start execution
- Code execution stops at breakpoint
- > In this example:
 - Every time "Run" is pressed:
 "P0.2" toggles



Getting Started: Infineon Embedded Power SDK Example Code



Infineon Example Code available in "Pack Installer"

Pack Installer - C:\Keil_v5\ARM\PACK					– 🗆 ×
ile Packs Window Help					
Device: Infineon - TLE9855QX					
Devices Boards		•	Packs Examples		
Search: 🗸 🗸	E		Show examples from installed Packs only		
Device	🖉 Summary		Example	Action	Description
🖃 🍄 All Devices	5696 Devices		ADC1_POTI_EIM (TLE985x Eval.Board V1.01)	💠 Сору	ADC1 with Exceptional Interrupt Mode reading in the Poti at Channel 4
ABOV Semiconductor	20 Devices		ADC1_POTI_ESM (TLE985x Eval.Board V1.01) 🚸 Сору	ADC1 with Exceptional Sequencer Mode reading in the Poti at Channel 4
🗉 🔗 Active-Semi	4 Devices		ADC1_POTI_SEQ (TLE985x Eval.Board V1.01)	🔶 Сору	ADC1 in Sequencer Mode reading in the Poti at Channel 4
🗉 🔗 Ambiq Micro	8 Devices		ADC2_VS (TLE985x Eval.Board V1.01)	🚸 Сору	ADC2 VS Measurement
🗉 🔗 Analog Devices	14 Devices		BDRV_CCU6_ADC1_MOTOR (TLE985x Eval.E	во 🚸 Сору	Runs a motor with speed and direction control
🕀 🔗 ARM	51 Devices		BLINKY (TLE985x Eval.Board V1.01)	🚸 Сору	Blinky - blinks LED1
🕀 🔗 AutoChips	1 Device		CCU6_ADC1 (TLE985x Eval.Board V1.01)	🚸 Сору	PWM synchroneous ADC measurement, the Poti at Channel 4 adjusts the PW
🕀 🔗 Cypress	425 Devices		CCU6_PWM (TLE985x Eval.Board V1.01)	💠 Сору	CCU6 generates PWM signals
🕀 🔗 GigaDevice	100 Devices		CCU6_SINUSOIDAL_PWM (TLE985x Eval.Bo	ar 🚸 Copy	CCU6 generates sinusoidal PWM signal
🕀 🔗 HDSC	26 Devices		GPT12E_T3_CONCAT (TLE985x Eval.Board \	/1 🚸 Copy	Timer2 and Timer3 as 32bit Timer
🗉 🔗 Holtek	145 Devices		GPT12E_T3_RELOAD (TLE985x Eval.Board V	1.01) 🚸 Copy	GPT12E Timer3 reloaded by Timer2
🖃 🔗 Infineon	174 Devices		MATH (TLE985x Eval.Board V1.01)	🚸 Сору	Shows the calculation speed of the MATH unit
🖃 🎌 TLE98xx Series	24 Devices		NVM_PROGRAMMING (TLE985x Eval.Board	IV 🚸 Copy	NVM Page Programming
🕀 🔧 TLE984x Series	7 Devices		NVM_PROTECTION (TLE985x Eval.Board V1	.01) 🚸 Copy	Flash Page Write Protection
🖃 😤 TLE985x Series	4 Devices		NVM_RAM_BRANCH (TLE985x Eval.Board \	/1 🚸 Copy	Code execution in RAM
TLE9851QXW	ARM Cortex-M0, 40 MHz, 4 kB RAM, 64 kB ROM		PMU_POWER_SAVING_SLEEP_MODE (TLE9	35x 🚸 Copy	Power Saving Modes - Sleep Mode
	ARM Cortex-M0, 40 MHz, 4 kB RAM, 48 kB ROM		PMU_POWER_SAVING_SLEEP_MODE_CYCL	IC 🚸 Copy	Power Saving Modes - Sleep Mode with cyclic wake
TLE9854QX	ARM Cortex-M0, 40 MHz, 4 kB RAM, 64 kB ROM		PMU_POWER_SAVING_STOP_MODE (TLE98	5x 🚸 Copy	Power Saving Modes - Stop Mode
TLE9855QX	ARM Cortex-M0, 40 MHz, 4 kB RAM, 96 kB ROM		PMU_RESET_STATUS (TLE985x Eval.Board V	1.0 🚸 Copy	Reset Status Evaluation
🕀 🔧 TLE986x Series	5 Devices		PMU_VDDEXT (TLE985x Eval.Board V1.01)	🚸 Сору	VDDEXT handling
🕀 🔧 TLE987x Series	8 Devices		SCU_PLL (TLE985x Eval.Board V1.01)	🚸 Сору	PLL adjustment
	110 Devices		SCU_PLL_XTAL (TLE985x Eval.Board V1.01)	🚸 Сору	PLL with external XTAL
	40 Devices		SSC1_2_SSC2 (TLE985x Eval.Board V1.01)	🚸 Сору	SSC1 sends to SSC2
🗉 🍳 Lapis Semiconductor	2 Devices		SSC1_SEND (TLE985x Eval.Board V1.01)	🚸 Сору	SSC1 sends data as SPI Master
🕂 🏈 Maxim	14 Devices		TIMER2_BLINK (TLE985x Eval.Board V1.01)	🔶 Сору	TIMER2 Blinking
🗉 🔗 MediaTek	2 Devices		TIMER2_PWM_CAPTURE (TLE985x Eval.Boa	rd 🚸 Copy	TIMER2 PWM Duty Cycle and Period Measurement
🗉 🏈 Microchip	400 Devices		UART1_SEND (TLE985x Eval.Board V1.01)	🚸 Сору	UART1 sends data over LIN Transceiver
🗉 🏈 Microsemi	6 Devices		UART2_TTY (TLE985x Eval.Board V1.01)	🚸 Сору	UART2 sends printf messages
MindMotion	89 Devices				
Nordic Semiconductor	13 Devices	-			



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