Power MOSFET 60 V, 15.0 mΩ, 36 A, Single N–Channel

Features

- Small Footprint (5x6 mm) for Compact Design
- Low R_{DS(on)} to Minimize Conduction Losses
- Low Q_G and Capacitance to Minimize Driver Losses
- These Devices are Pb-Free and are RoHS Compliant

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

			,	Malara	11
Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V _{DSS}	60	V
Gate-to-Source Voltage			V _{GS}	±20	V
Continuous Drain Current R _{ፁIC}	Steady State	T _C = 25°C	۱ _D	36	А
(Notes 1, 3)		T _C = 100°C		25	
Power Dissipation		$T_{C} = 25^{\circ}C$	PD	37	W
R _{θJC} (Note 1)		$T_{C} = 100^{\circ}C$		18	
Continuous Drain	Steady State	T _A = 25°C	Ι _D	11	А
Current R _{θJA} (Notes 1, 2, 3)		T _A = 100°C		7.8	
Power Dissipation		T _A = 25°C	PD	3.5	W
$R_{\theta JA}$ (Notes 1 & 2)		$T_A = 100^{\circ}C$		1.8	
Pulsed Drain Current	T _A = 25	°C, t _p = 10 μs	I _{DM}	166	А
Operating Junction and Storage Temperature			T _J , T _{stg}	–55 to + 175	°C
Source Current (Body Diode)			I _S	31	А
Single Pulse Drain-to-Source Avalanche Energy ($I_{L(pk)} = 2.87 \text{ A}$)			E _{AS}	65	mJ
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			ΤL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Junction-to-Case - Steady State	$R_{\theta JC}$	4.1	°C/W
Junction-to-Ambient - Steady State (Note 2)	$R_{\theta JA}$	43	

1. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.

2. Surface-mounted on FR4 board using a 650 mm², 2 oz. Cu pad.

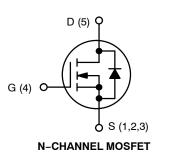
3. Maximum current for pulses as long as 1 second is higher but is dependent on pulse duration and duty cycle.

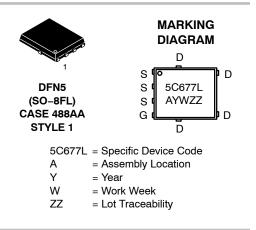


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V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
60 V	15.0 m Ω @ 10 V	36 A
00 V	21.5 mΩ @ 4.5 V	007





ORDERING INFORMATION

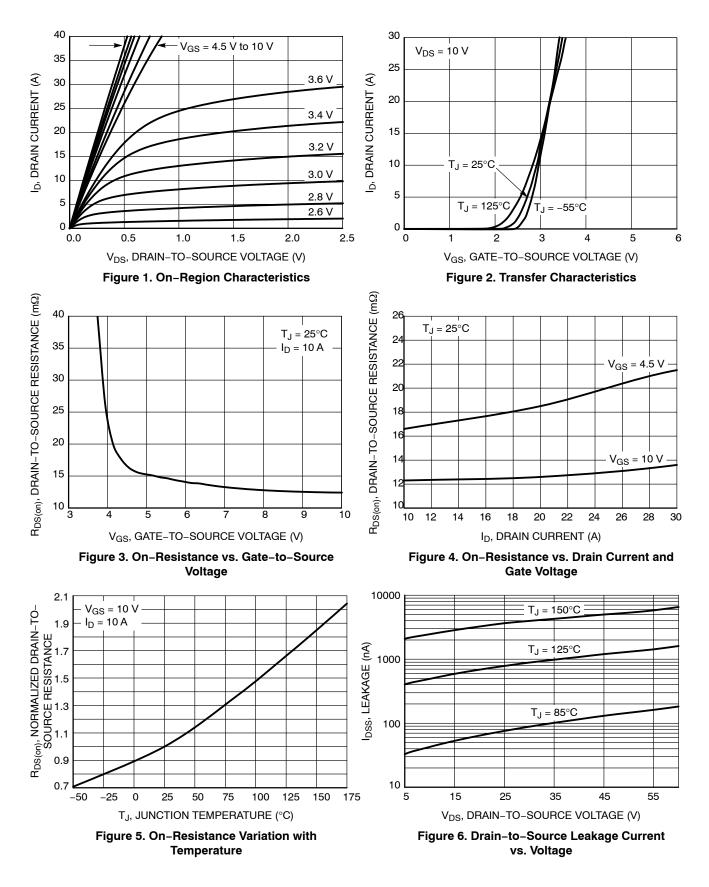
See detailed ordering, marking and shipping information on page 5 of this data sheet.

ELECTRICAL CHARACTERISTICS (T_J = $25^{\circ}C$ unless otherwise specified)

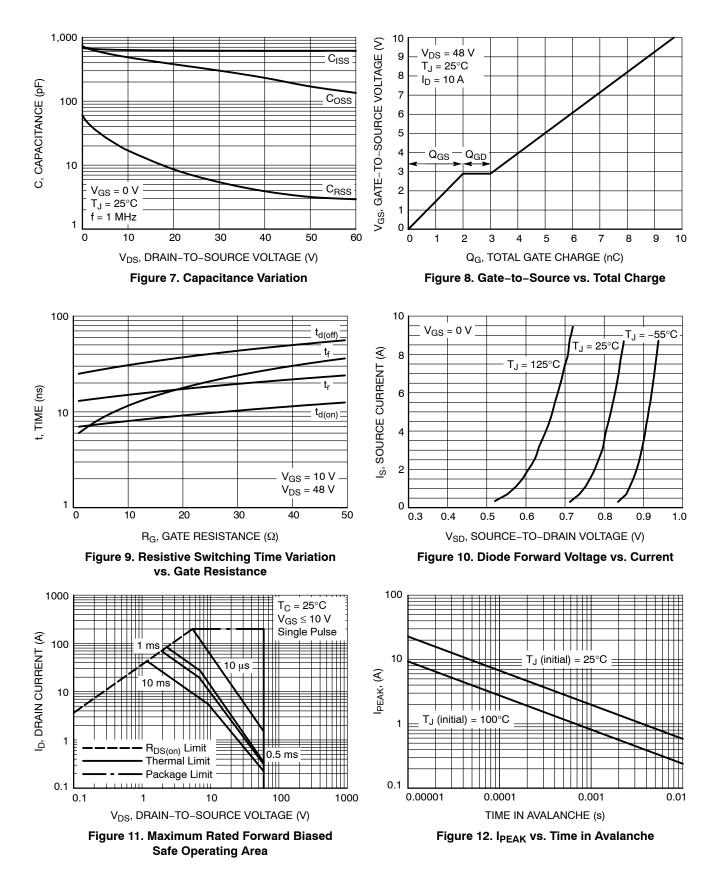
	Symbol	Test Condition		Min	Тур	Max	Unit	
OFF CHARACTERISTICS	<u> </u>							
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I _D = 250 µA		60			V	
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} / T _J				26		mV/°C	
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V,	T _J = 25 °C			10		
		V _{DS} = 60 V	T _J = 125°C			250	μΑ	
Gate-to-Source Leakage Current	I _{GSS}	V _{DS} = 0 V, V _{GS}	= 20 V			100	nA	
ON CHARACTERISTICS (Note 4)								
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_{D}$	= 25 μA	1.2		2.0	V	
Threshold Temperature Coefficient	V _{GS(TH)} /T _J				-5.0		mV/°C	
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 10 A		12.5	15.0	_	
		V _{GS} = 4.5 V	I _D = 10 A		17.9	21.5	mΩ	
Forward Transconductance	9 _{FS}	V _{DS} =15 V, I _D	= 15 A		27.5		S	
CHARGES AND CAPACITANCES								
Input Capacitance	C _{ISS}	$V_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}, V_{DS} = 25 \text{ V}$ $V_{GS} = 4.5 \text{ V}, V_{DS} = 48 \text{ V}; \text{ I}_{D} = 10 \text{ A}$			620		pF	
Output Capacitance	C _{OSS}				340			
Reverse Transfer Capacitance	C _{RSS}				7			
Total Gate Charge	Q _{G(TOT)}				4.5		nC	
Total Gate Charge	Q _{G(TOT)}	V _{GS} = 10 V, V _{DS} = 48 V; I _D = 10 A			9.7		nC	
Threshold Gate Charge	Q _{G(TH)}	V _{GS} = 10 V, V _{DS} = 48 V; I _D = 10 A			1.3		nC V	
Gate-to-Source Charge	Q _{GS}				2.1			
Gate-to-Drain Charge	Q _{GD}				1			
Plateau Voltage	V _{GP}				3.0			
SWITCHING CHARACTERISTICS (Note 5)								
Turn–On Delay Time	t _{d(ON)}	V_{GS} = 10 V, V_{DS} = 48 V, I _D = 10 A, R _G = 1 Ω			7			
Rise Time	t _r				13		ns	
Turn-Off Delay Time	t _{d(OFF)}				25			
Fall Time	t _f				6			
DRAIN-SOURCE DIODE CHARACTERIST	CS							
Forward Diode Voltage	V _{SD}	V _{GS} = 0 V,	$T_J = 25^{\circ}C$		0.85	1.2		
i ormala Diode Voltage		$I_{\rm S} = 10 \rm{A}$	T _J = 125°C		0.72		- V	
I SIMAR DIOLE VOILAge	1 1							
Reverse Recovery Time	t _{RR}				23.8			
	t _{RR}	V _{CS} = 0 V. dls/dt =	= 100 A/µs.		23.8 11.9		ns	
Reverse Recovery Time		V _{GS} = 0 V, dls/dt = I _S = 10 A					ns	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 4. Pulse Test: pulse width $\leq 300 \ \mu$ s, duty cycle $\leq 2\%$. 5. Switching characteristics are independent of operating junction temperatures.

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS

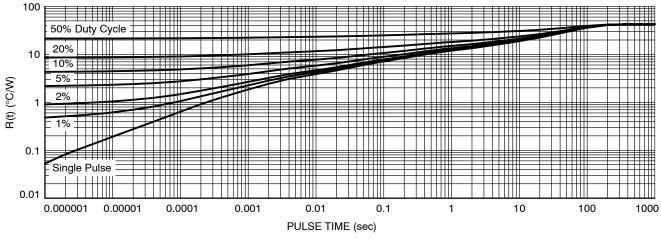


Figure 13. Thermal Characteristics

DEVICE ORDERING INFORMATION

Device	Marking	Package	Shipping [†]
NTMFS5C677NLT1G	5C677L	DFN5 (Pb–Free)	1500 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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