



### 80V N-CHANNEL ENHANCEMENT MODE MOSFET

## **Product Summary**

BVDSS	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
80V	$8m\Omega$ @ $V_{GS} = 10V$	13A
	$9.5 \text{m}\Omega$ @ $V_{GS} = 6V$	12A
	12mΩ @ V <sub>GS</sub> = 4.5V	11A

## **Features and Benefits**

- High Conversion Efficiency
- Low R<sub>DS(ON)</sub> Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- 100% Unclamped Inductive Switching (UIS) Test in Production –
   Ensures More Reliable and Robust End Application
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control
  (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and
  manufactured in IATF 16949 certified facilities), please
   contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

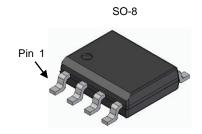
## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (RDS(ON)), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

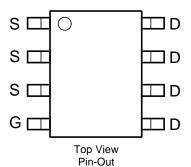
- High Frequency Switching
- Synchronous Rectification
- DC-DC Converters

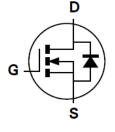
## **Mechanical Data**

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.074 grams (Approximate)



Top View





**Equivalent Circuit** 

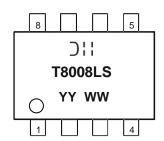
## **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMT8008LSS-13	SO-8	2,500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



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# **Maximum Ratings** (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		$V_{DSS}$	80	V
Gate-Source Voltage		Vgss	±20	V
Continuous Prais Current (Note 6) Ves 40V	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	l <sub>D</sub>	13 10	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	$T_C = +25$ °C $T_C = +70$ °C	lo	32 26	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I <sub>DM</sub>	110	Α
Maximum Continuous Body Diode Forward Current (Note 6)		Is	10	Α
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		lsм	110	Α
Avalanche Current, L = 0.3mH (Note 9)		las	27	Α
Avalanche Energy, L = 0.3mH (Note 9)		Eas	109	mJ

## Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P <sub>D</sub>	1.3	W
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θ</sub> ЈА	94	°C/W
Total Power Dissipation (Note 6)	PD	2.2	W
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>θ</sub> ЈА	58	°C/W
Thermal Resistance, Junction to Case (Note 6)	Rejc	10	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

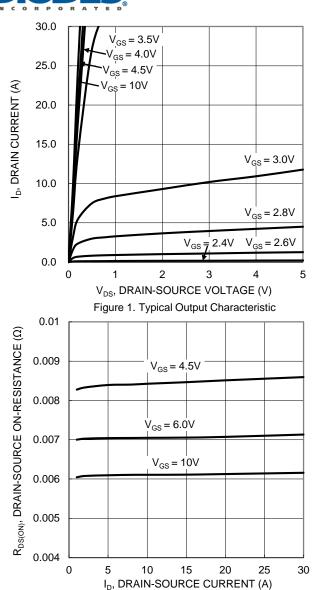
## **Electrical Characteristics** (T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	80	_	_	V	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current	IDSS		_	1	μΑ	V <sub>DS</sub> = 64V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	I <sub>GSS</sub>	1	_	±1	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	Vgs(TH)	1.3	_	2.8	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
	R <sub>DS(ON)</sub>	1	6	8	mΩ	$V_{GS} = 10V, I_D = 10A$	
Static Drain-Source On-Resistance			7	9.5		$V_{GS} = 6V, I_D = 10A$	
			8.3	12		$V_{GS} = 4.5V, I_{D} = 6A$	
Diode Forward Voltage	VsD	_	8.0	1.2	V	Vgs = 0V, Is = 20A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C <sub>iss</sub>	_	2840	_		V <sub>DS</sub> = 40V, V <sub>GS</sub> = 0V, f = 1MHz	
Output Capacitance	Coss	_	797	_	pF		
Reverse Transfer Capacitance	$C_{rss}$		42	_			
Gate Resistance	Rg	1	1.7	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (VGS = 4.5V)	Qg		24	_		V <sub>DD</sub> = 40V, I <sub>D</sub> = 2A	
Total Gate Charge (V <sub>GS</sub> = 10V)	$Q_g$	1	47	_	nC		
Gate-Source Charge	Qgs	1	7	_	IIC		
Gate-Drain Charge	$Q_{gd}$	1	11	_			
Turn-On Delay Time	td(on)	1	6	_		$V_{DD} = 40V, V_{GS} = 10V,$ $I_D = 2A, R_g = 1.6\Omega$	
Turn-On Rise Time	t <sub>R</sub>	_	6	_			
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	27	_	ns		
Turn-Off Fall Time	tF	_	44	_			
Body Diode Reverse Recovery Time	t <sub>RR</sub>	-	43	_	ns L OA divite 400A/es-		
Body Diode Reverse Recovery Charge	Qrr	_	59	_	nC	-I <sub>F</sub> = 2A, di/dt = 100A/μs	

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
  6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
  7. Short duration pulse test used to minimize self-heating effect.
  8. Guaranteed by design. Not subject to product testing.

- 9.  $I_{AS}$  and  $E_{AS}$  ratings are based on low frequency and duty cycles to keep  $T_J = +25^{\circ}C$ .





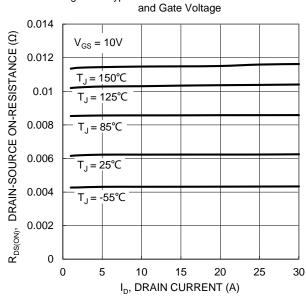
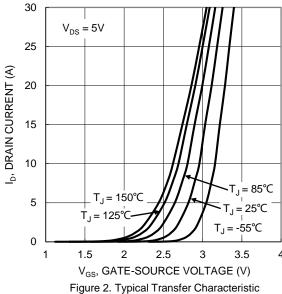
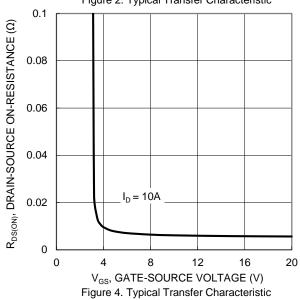


Figure 3. Typical On-Resistance vs. Drain Current

Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature





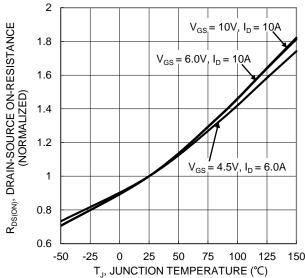
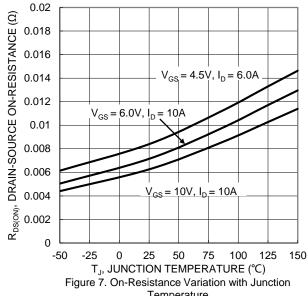


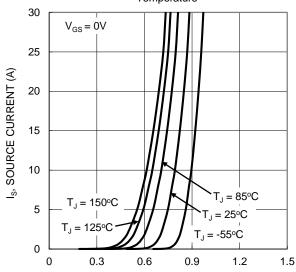
Figure 6. On-Resistance Variation with Junction Temperature







**Temperature** 



 $V_{SD}$ , SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs. Current

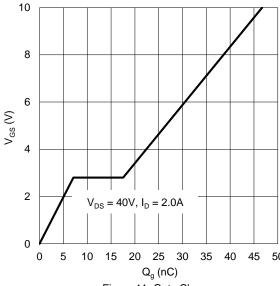
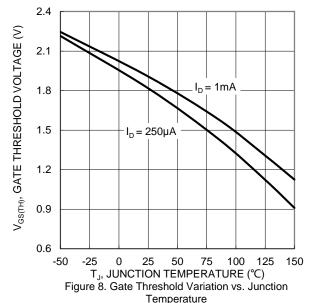


Figure 11. Gate Charge



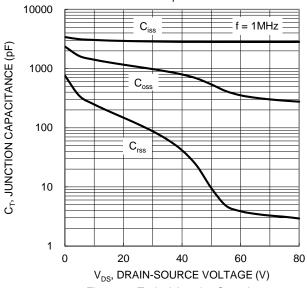
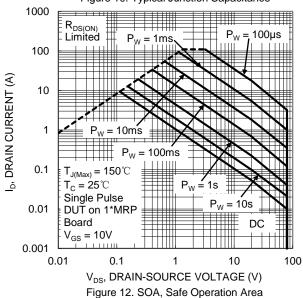


Figure 10. Typical Junction Capacitance





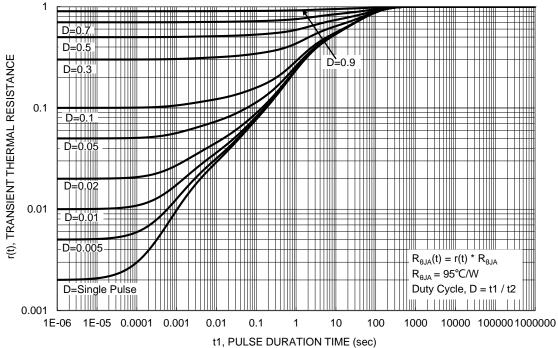


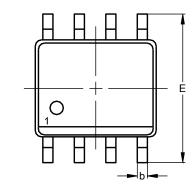
Figure 13. Transient Thermal Resistance

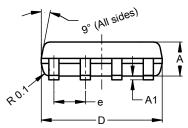


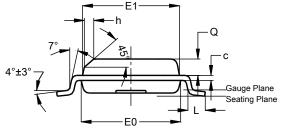
## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8





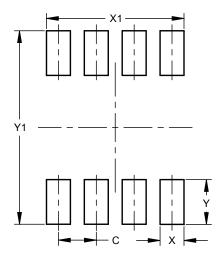


SO-8				
Dim	Min	Max	Тур	
Α	1.40	1.50	1.45	
A1	0.10	0.20	0.15	
b	0.30	0.50	0.40	
С	0.15	0.25	0.20	
D	4.85	4.95	4.90	
Е	5.90	6.10	6.00	
E1	3.80	3.90	3.85	
E0	3.85	3.95	3.90	
е			1.27	
h	-		0.35	
L	0.62	0.82	0.72	
ø	0.60	0.70	0.65	
All Dimensions in mm				

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8



Dimensions	Value (in mm)
С	1.27
Х	0.802
X1	4.612
Υ	1.505
Y1	6.50



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