

Features

- Split Gate Trench MOSFET Technology
- High Density Cell Design for Low $R_{DS(ON)}$
- Excellent Package for Heat Dissipation
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

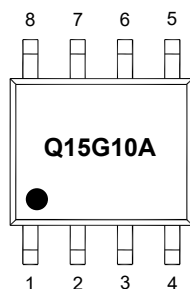
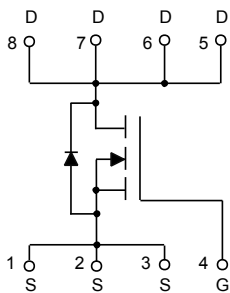
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 60°C/W Junction to Ambient(Steady-State)^(Note 2)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	I_D	$T_A=25^\circ\text{C}$	15
		$T_A=100^\circ\text{C}$	9.5
Pulsed Drain Current ^(Note 3)	I_{DM}	60	A
Total Power Dissipation ^(Note 4)	P_D	2.1	W
Single Pulsed Avalanche Energy ^(Note 5)	E_{AS}	156	mJ

Note:

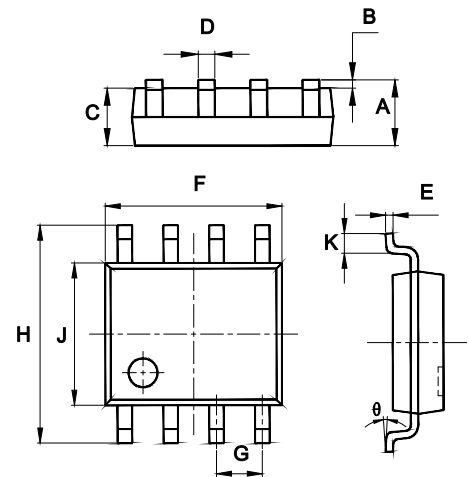
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$. The value in any given application depends on the user's specific board design.
3. Repetitive rating; pulse width limited by max. junction temperature.
4. P_D is based on max. junction temperature, using Steady-State junction-ambient thermal resistance.
5. $T_J=25$, $V_{DD}=25\text{V}$, $V_{GS}=10\text{V}$, $L=0.5\text{mH}$.

Internal Structure and Marking Code



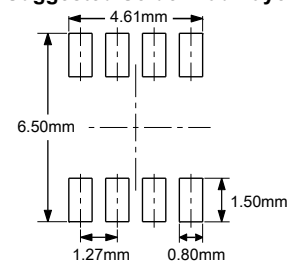
N-CHANNEL MOSFET

SOP-8



DIM	DIMENSIONS				NOTE
	INCHES		MM		
	MIN	MAX	MIN	MAX	
A	0.053	0.069	1.35	1.75	
B	0.004	0.010	0.10	0.25	
C	0.053	0.061	1.35	1.55	
D	0.013	0.020	0.33	0.51	
E	0.007	0.010	0.17	0.25	
F	0.185	0.200	4.70	5.10	
G	0.050		1.270		TYP.
H	0.228	0.244	5.80	6.20	
J	0.150	0.157	3.80	4.00	
K	0.016	0.050	0.40	1.27	
θ	0°	8°	0°	8°	

Suggested Solder Pad Layout



Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	100			V
Gate-Source Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=100V, V_{GS}=0V$			1	μA
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1	1.8	3	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=10A$		8	9.5	m Ω
		$V_{GS}=4.5V, I_D=5A$		9.7	12.5	
Gate Resistance	R_g	f=1MHz, Open drain		0.68		Ω
Diode Characteristics						
Continuous Body Diode Current	I_S				15	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=15A$			1.3	V
Reverse Recovery Time	t_{rr}	$I_S=10A, di/dt=100A/\mu s$		50		ns
Reverse Recovery Charge	Q_{rr}			62		nC
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=50V, V_{GS}=0V, f=1MHz$		2350		pF
Output Capacitance	C_{oss}			797		
Reverse Transfer Capacitance	C_{rss}			13		
Total Gate Charge	Q_g	$V_{DS}=50V, V_{GS}=10V, I_D=10A$		36		nC
Gate-Source Charge	Q_{gs}			5.8		
Gate-Drain Charge	Q_{gd}			5.6		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=50V, V_{GS}=10V, R_{GEN}=2.2\Omega, I_D=10A$		10.8		ns
Turn-On Rise Time	t_r			4.4		
Turn-Off Delay Time	$t_{d(off)}$			27		
Turn-Off Fall Time	t_f			7		

Curve Characteristics

Fig. 1 - Typical Output Characteristics

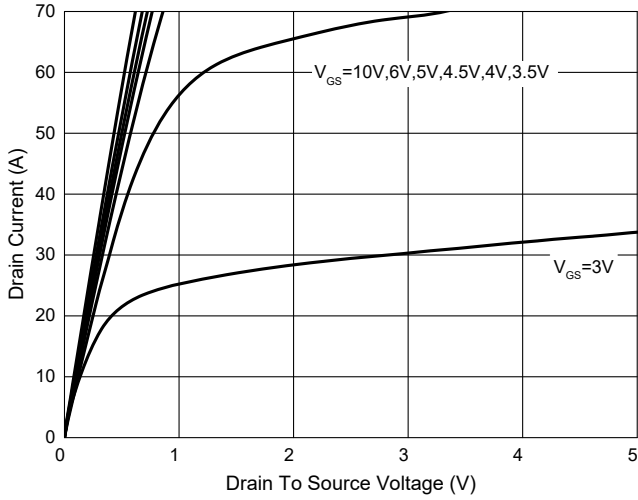


Fig. 2 - Transfer Characteristics

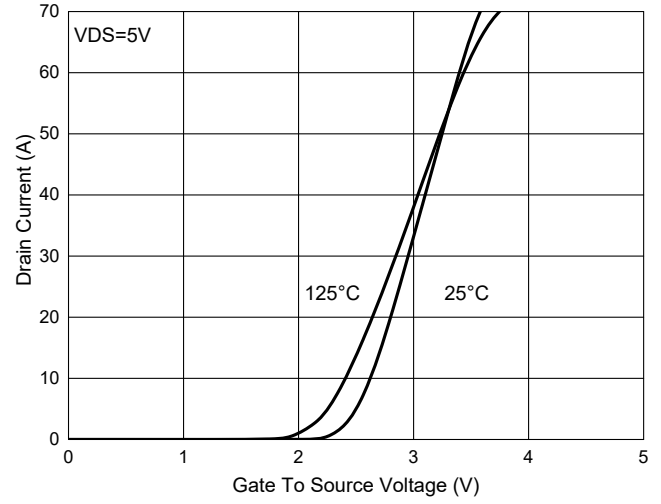


Fig. 3 - $R_{DS(ON)} - V_{GS}$

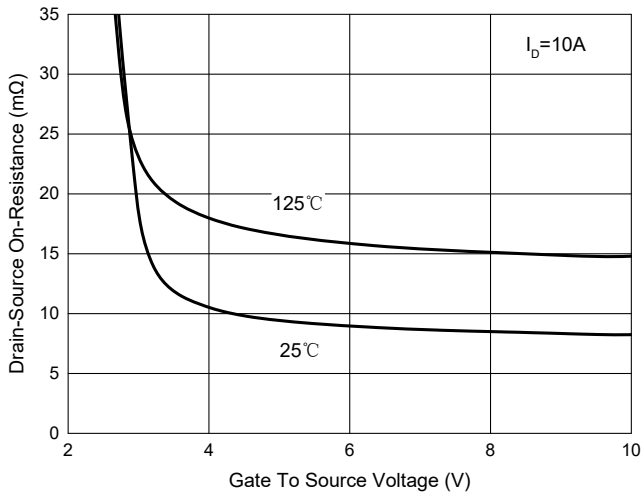


Fig. 4 - $R_{DS(ON)} - I_D$

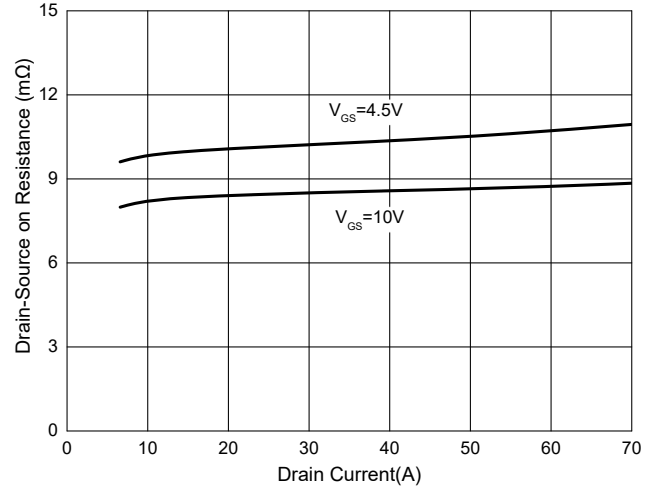


Fig. 5 - Capacitance Characteristics

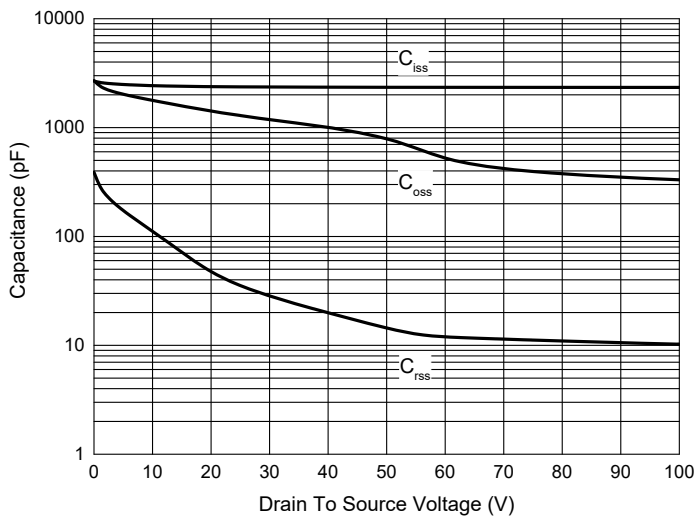
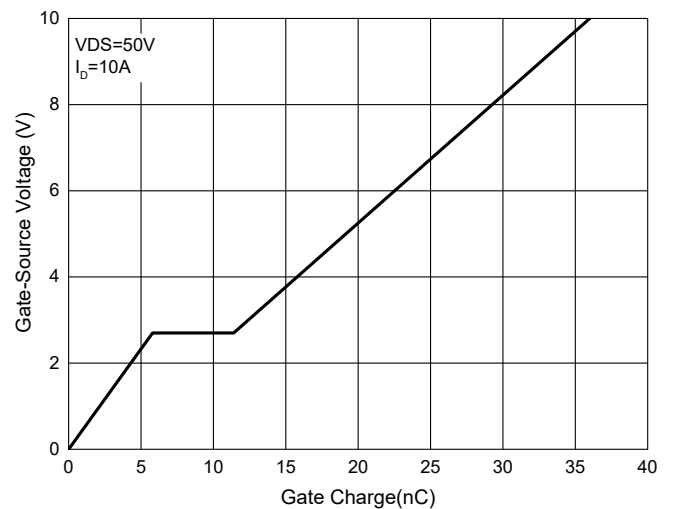


Fig. 6 - Gate Charge



Curve Characteristics

Fig. 7 - Normalized Threshold voltage

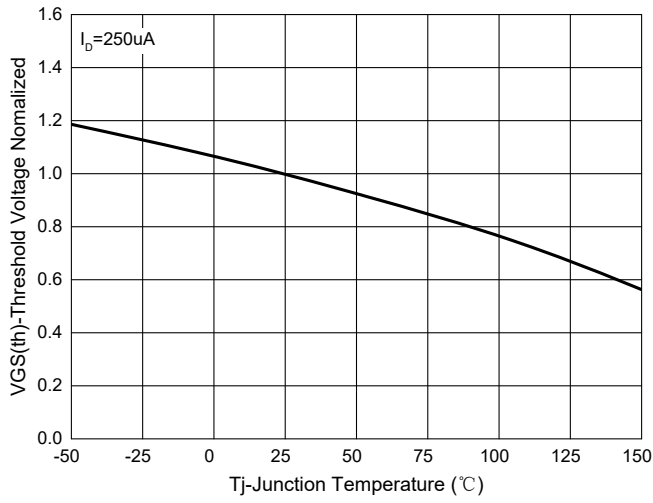


Fig. 8 - Normalized On Resistance Characteristics

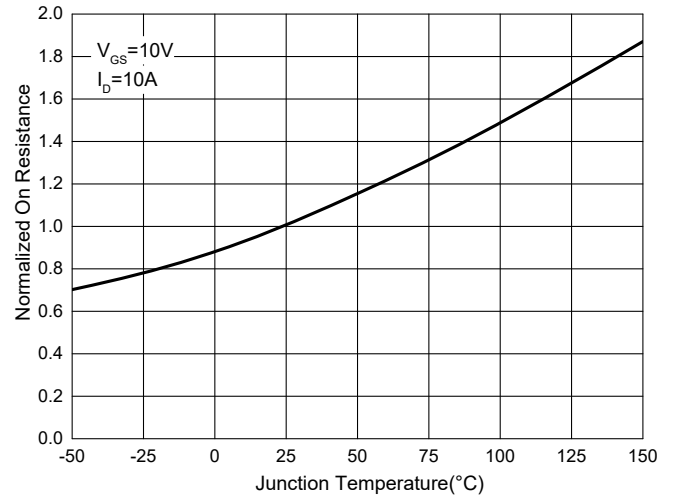


Fig. 9 - $I_s - V_{SD}$

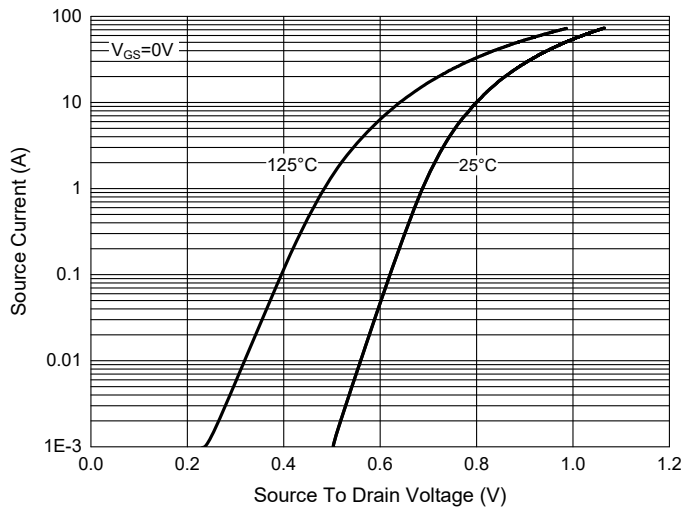


Fig. 10 - Drain Current

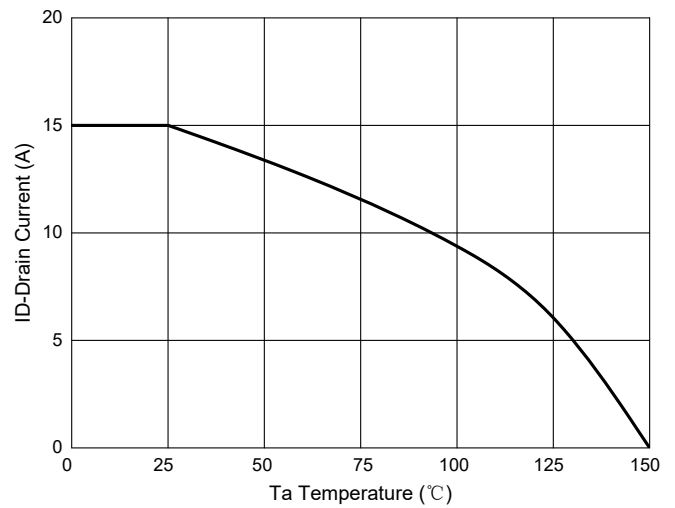
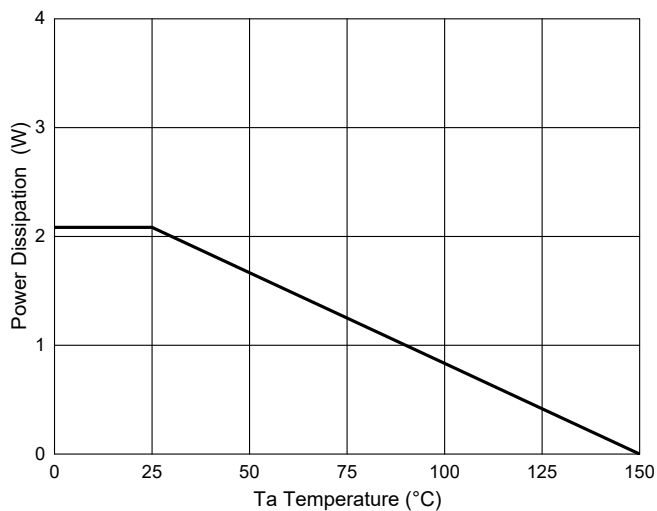


Fig. 11-PD Dissipation



Curve Characteristics

Fig. 12 - Safe Operation Area

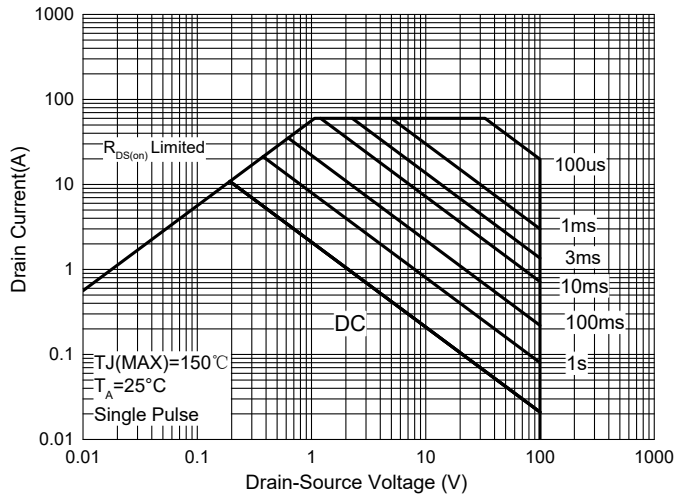
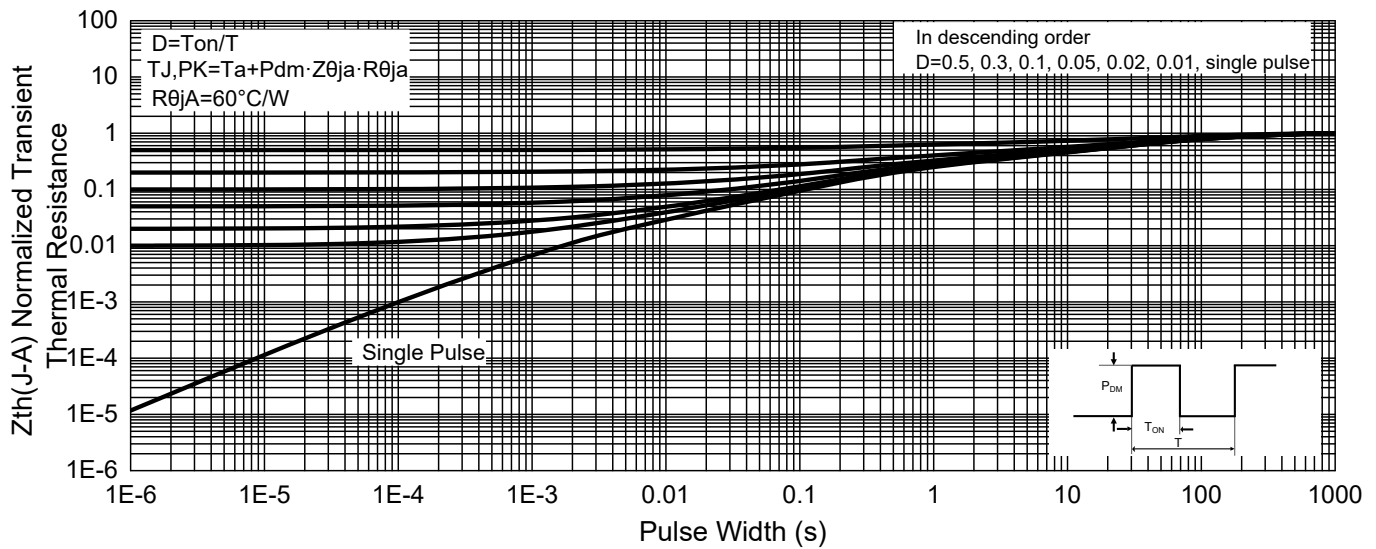


Fig. 13 -Normalized Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 4Kpcs/Reel

*****IMPORTANT NOTICE*****

Micro Commercial Components Corp. reserves the right to make changes without further notice to any product herein to make corrections, modifications, enhancements, improvements, or other changes. **Micro Commercial Components Corp.** does not assume any liability arising out of the application or use of any product described herein; neither does it convey any license under its patent rights, nor the rights of others. The user of products in such applications shall assume all risks of such use and will agree to hold **Micro Commercial Components Corp.** and all the companies whose products are represented on our website, harmless against all damages. **Micro Commercial Components Corp.** products are sold subject to the general terms and conditions of commercial sale, as published at <https://www.mccsemi.com/Home/TermsAndConditions>.

*****LIFE SUPPORT*****

MCC's products are not authorized for use as critical components in life support devices or systems without the express written approval of Micro Commercial Components Corporation.

*****CUSTOMER AWARENESS*****

Counterfeiting of semiconductor parts is a growing problem in the industry. Micro Commercial Components (MCC) is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. MCC strongly encourages customers to purchase MCC parts either directly from MCC or from Authorized MCC Distributors who are listed by country on our web page cited below. Products customers buy either from MCC directly or from Authorized MCC Distributors are genuine parts, have full traceability, meet MCC's quality standards for handling and storage. **MCC will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources.** MCC is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.