

# LVDS CLOCK OSCILLATOR

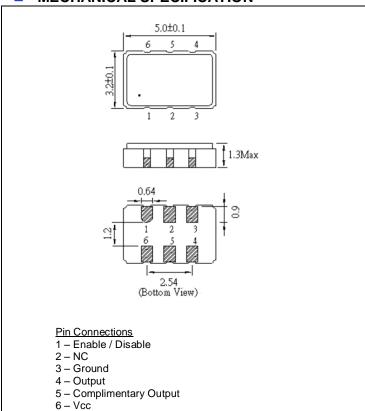
Page 1 of 3

### CL5032-150.000-3.3-50-T-TR-NS1

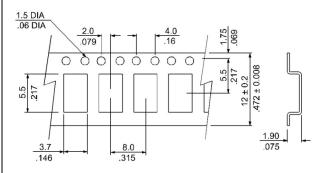
### **ELECTRICAL SPECIFICATION**

PARAMETER	SYMBOL	CONDITIONS	VALUE	UNIT
Nominal Frequency	fo	Ta=25°C	150.000	MHz
Supply Voltage	Vcc	Vcc ±5%	3.3	VDC
Supply current, max	Is	Vcc; Ta=+25°C;	100	mA
Operating Temperature Range	Ta		-20 to +70	°C
Storage temperature	T <sub>(stg)</sub>	Absolute max	-55 to +125	°C
Output Logic Type			LVDS	
Overall Freq. Stability, Max.	Δf/fo	Inclusive of 25°C Tolerance and Changes due to Operating Temperature, Supply Voltage, Load, Aging, Shock and Vibration		
Output Voltage	$V_{OL}$	Logic "0" Level	0.9 Min	VDC
Output voltage	V <sub>OH</sub>	Logic "1" Level	1.6 Max	VDC
Output Load		Connected between Out and Complementary Out	100 Ω	
E de Breite E de	E/D	Pin 1: N.C. (Open) or High (0.7 * Vcc)	Pin 4 & 5 – Oscillation (Enabled)	
Enable / Disable Function	E/D	Pin 1: Low (0.3 * Vcc)	Pin 4 & 5 – High Impedance (Disabled)	
Symmetry (Duty Cycle)	DC	@50% Vdd	45 to 55	%
Rise Time and Fall Time	tr / tf	@ 20% to 80% Vdd 1.0		ns
Jitter, RMS, max.	J	$1\sigma$ , $12kHz < F_i < 20MHz$ 1.0		ps

### MECHANICAL SPECIFICATION



### CARRIER TAPE DIMENSIONS



NOTE: REFER TO EIA-481 FOR DIMENSIONS NOT LISTED

#### PACKAGING

178 mm REEL DIAMETER
12 mm TAPE WIDTH, 8 mm PITCH
QUANTITY: 1000 PIECES PER REEL

NOTE: A capacitor of 0.01  $\mu F$  between Vcc and Ground is recommended

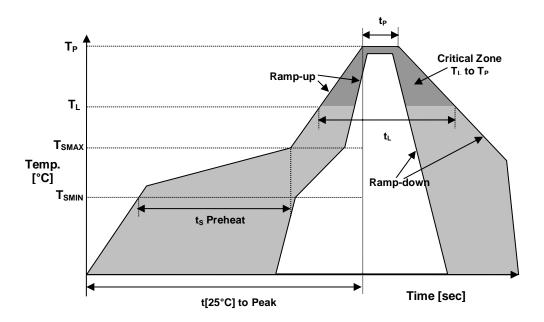


# LVDS CLOCK OSCILLATOR

Page 2 of 3

CL5032-150.000-3.3-50-T-TR-NS1

### REFLOW PROFILE



	Reflow profile	
Temperature Min Preheat	T <sub>SMIN</sub>	150°C
Temperature Max Preheat	T <sub>SMAX</sub>	200°C
Time (T <sub>SMIN</sub> to T <sub>SMAX</sub> )	t <sub>S</sub>	60-180 sec.
Temperature	TL	217°C
Peak Temperature	$T_P$	260°C
Ramp-up rate	R <sub>UP</sub>	3°C/sec max.
Ramp-down rate	R <sub>DOWN</sub>	6°C/sec max.
Time within 5°C of Peak Temperature	t <sub>P</sub>	10 sec.
Time t[25°C] to Peak Temperature	t[25°C] to Peak	480 sec.
Time	t <sub>L</sub>	60-150 sec.

#### **■** ENVIRONMENTAL

PARAMETER	VALUE
MOISTURE SENSITIVITY LEVEL	1
RoHS	COMPLIANT
REACH-SVHC	COMPLIANT
HALOGEN-FREE	COMPLIANT
TERMINATION FINISH	Au





# LVDS CLOCK OSCILLATOR

Page 3 of 3

CL5032-150.000-3.3-50-T-TR-NS1

### MARKING

Rx150 •3Byw

x – Internal Production ID code

y – Year code

w - Week code

YEAR CODE		
Year	Code	
2015	5	
2016	6	
2017	7	
2018	8	
2019	9	
2020	0	
2021	1	
2022	2	
2023	3	
2024	4	
2025	5	

ALPHA WEEK CODE TABLE					
Week	Code	Week	Code	Week	Code
1	а	19	S	37	K
2	b	20	t	38	L
3	С	21	u	39	M
4	d	22	V	40	N
5	е	23	W	41	0
6	f	24	х	42	Р
7	g	25	У	43	Q
8	h	26	Z	44	R
9	i	27	Α	45	S
10	j	28	В	46	Т
11	k	29	С	47	U
12	I	30	D	48	V
13	m	31	E	49	W
14	n	32	F	50	X
15	0	33	G	51	Υ
16	р	34	Н	52	Z
17	q	35	I		
18	r	36	J		

#### APPROVAL

RALTRON		
DRAWN BY:	LP, June 02, 2017	
APPROVED BY:	JI, June 02, 2017	
REVISION:	A, Initial Release	

Raltron Electronics / RAMI Technology USA, LLC, including its affiliates, employees, agents and other persons acting on its behalf (collectively Raltron/RAMI Tech), disclaim any and all liability for any errors or inaccuracies contained in this data sheet. While Raltron/RAMI Tech has made every reasonable effort ensure the accuracy of all product information, specifications and data contained herein, Raltron/RAMI Tech does not guarantee that the information is provided only for reference purposes only and is subject to enge, correction or revision, at any time without notice. Raltron/RAMI ech does not assume any liability arising out of an application or use of any product described herein and disclaims any warranties expressed or implied. The user of products in such applications shall assume all risks of such use and will agree to hold Raltron/RAMI Tech, harmless against all damages.

Copyright © 2016, Raltron Electronics / RAMI Technology USA, LLC. All rights reserved. No part of this document may be reproduced in any form without the prior written permission of Raltron Electronics / RAMI Technology USA, LLC.