

Ultrafast Power Rectifier

6 A, 600 V

RURD660S9A-F085

The RURD660S9A-F085 is an ultrafast diode with soft recovery characteristics ($t_{rr} < 83$ ns). It has a low forward voltage drop and is of silicon nitride passivated ion-implanted epitaxial planar construction. This device is intended for use as a freewheeling/clamping diode and rectifier in a variety of switching power supplies and other power switching applications. Its low stored charge and ultrafast soft recovery minimize ringing and electrical noise in many power switching circuits, thus reducing powerloss in the switching transistors.

Features

- High Speed Switching ($t_{rr} = 63$ ns (Typ.) @ $I_F = 6$ A)
- Low Forward Voltage ($V_F = 1.26$ V (Typ.) @ $I_F = 6$ A)
- Avalanche Energy Rated
- AEC-Q101 Qualified and PPAP Capable
- This is a Pb-Free Device

Applications

- General Purpose
- Switching Mode Power Supply
- Power Switching Circuits

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
V_{RRM}	Peak Repetitive Reverse Voltage	600	V
V_{RWM}	Working Peak Reverse Voltage	600	V
V_R	DC Blocking Voltage	600	V
$I_{F(AV)}$	Average Rectified Forward Current @ $T_C = 25^\circ\text{C}$	6	A
I_{FSM}	Non-repetitive Peak Surge Current	60	A
T_J, T_{STG}	Operating Junction and Storage Temperature	- 55 to +175	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

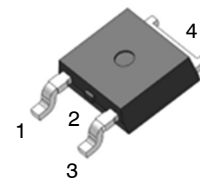
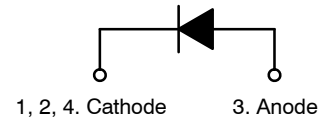
Symbol	Parameter	Max	Unit
$R_{\theta JC}$	Maximum Thermal Resistance, Junction to Case	3	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$ (Note 1)	Maximum Thermal Resistance, Junction to Ambient	140	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$ (Note 2)	Maximum Thermal Resistance, Junction to Ambient	50	$^\circ\text{C}/\text{W}$

1. Mounted on a minimum pad follow by JEDEC standard.
2. Mounted on a 1 in² pad of 2 oz copper follow by JEDEC standard.



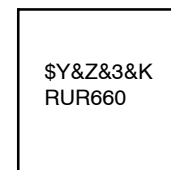
ON Semiconductor®

www.onsemi.com



DPAK3 (TO-252 3 LD)
CASE 369AS

MARKING DIAGRAM



RUR660 = Specific Device Code
\$Y = ON Semiconductor Logo
&Z = Assembly Plant Code
&3 = 3-Digit Date Code
&K = 2-Digits Lot Run Traceability Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 4 of this data sheet.

RURD660S9A-F085

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit	
I _R	Instantaneous Reverse Current	V _R = 600 V	T _C = 25°C	-	-	100	μA
			T _C = 175°	-	-	500	μA
V _{FM} (Note 3)	Instantaneous Forward Voltage	I _F = 6 A	T _C = 25°C	-	1.26	1.5	V
			T _C = 175°	-	1.04	-	V
t _{rr} (Note 4)	Reverse Recovery Time	I _F = 1 A, di/dt = 200 A/μs, V _{CC} = 390 V	T _C = 25°C	-	25	33	ns
		I _F = 6 A, di/dt = 200 A/μs, V _{CC} = 390 V	T _C = 25°C	-	63	83	ns
			T _C = 175°	-	119	-	ns
t _a t _b Q _{rr}	Reverse Recovery Time	I _F = 6 A, di/dt = 200 A/μs, V _{CC} = 390 V	T _C = 25°C	-	23	-	ns
	Reverse Recovery Charge		-	40	-	ns	
-		151	-	nC			
W _{AVL}		Avalanche Energy (L = 20 mH)	10	-	-	mJ	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. Pulse: Test Pulse width = 300 μs, Duty Cycle = 2%

4. Guaranteed by design

TEST CIRCUIT AND WAVEFORMS

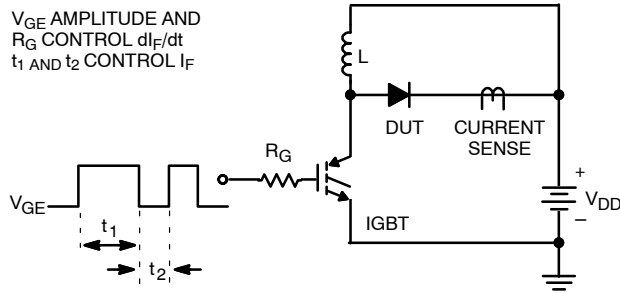


Figure 1. t_{rr} Test Circuit

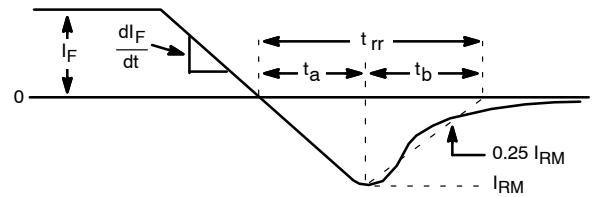


Figure 2. t_{rr} Waveforms and Definitions

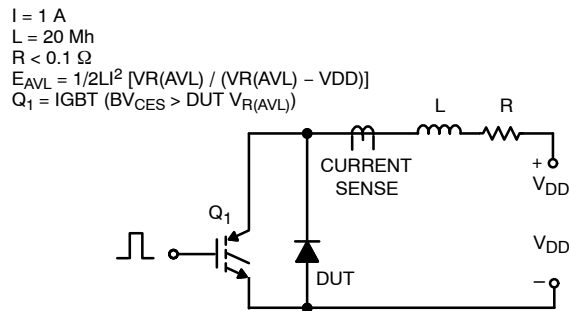


Figure 3. Avalanche Energy Test Circuit

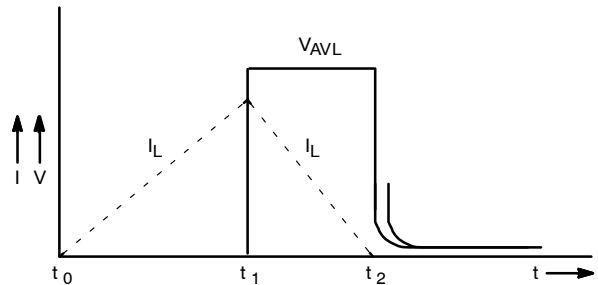


Figure 4. Avalanche Current and Voltage Waveforms

TYPICAL PERFORMANCE CHARACTERISTICS

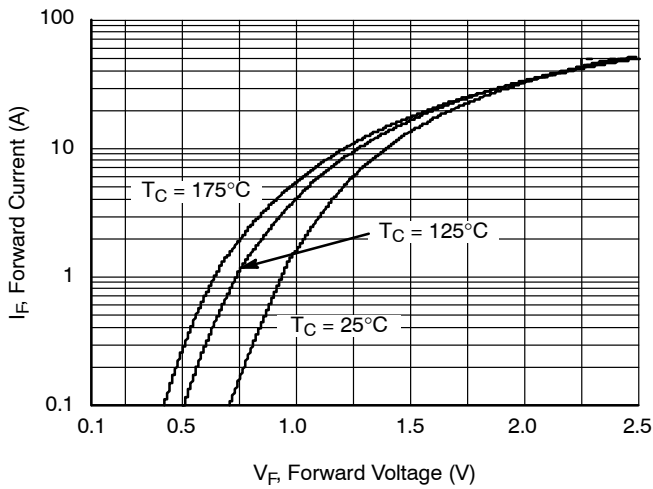


Figure 9. Typical Forward Voltage Drop vs. Forward Current

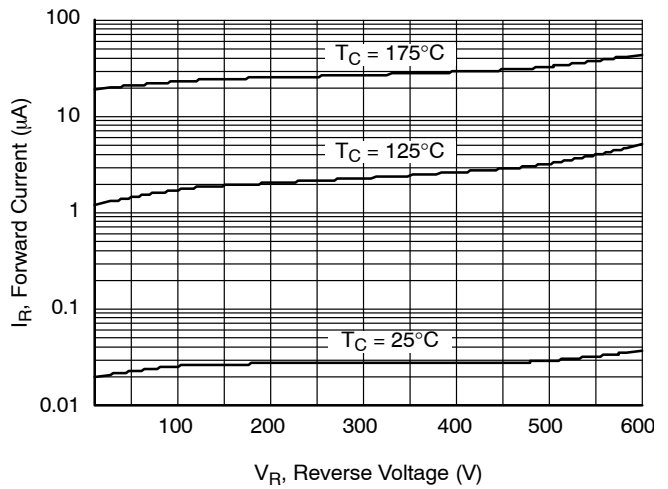


Figure 10. Typical Reverse Current vs. Reverse Voltage

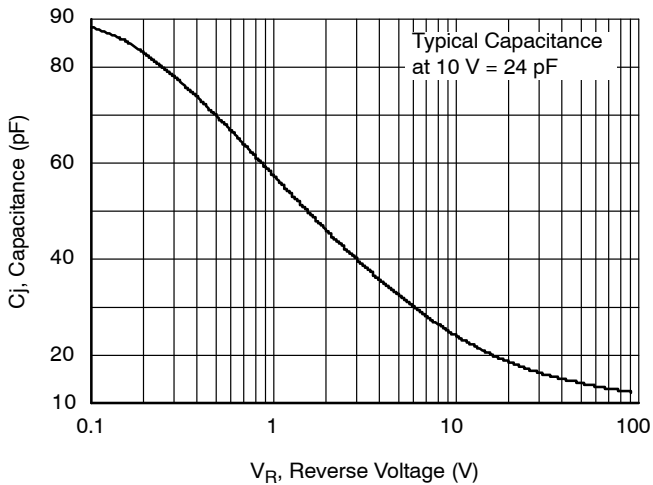


Figure 5. Typical Junction Capacitance

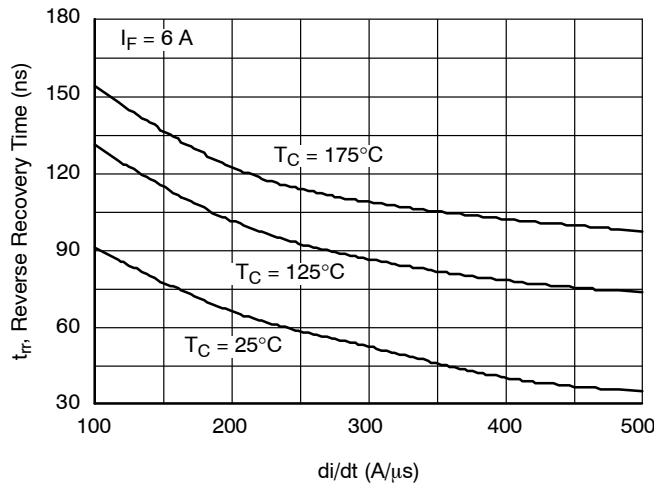


Figure 6. Typical Reverse Recovery Time vs. di/dt

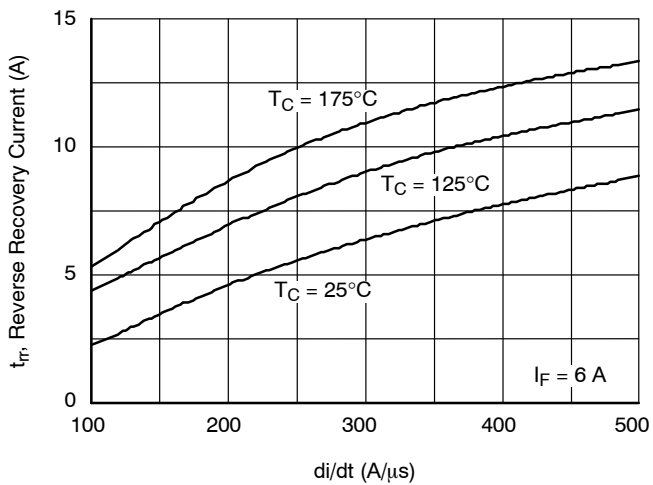


Figure 7. Typical Reverse Recovery Current vs. di/dt

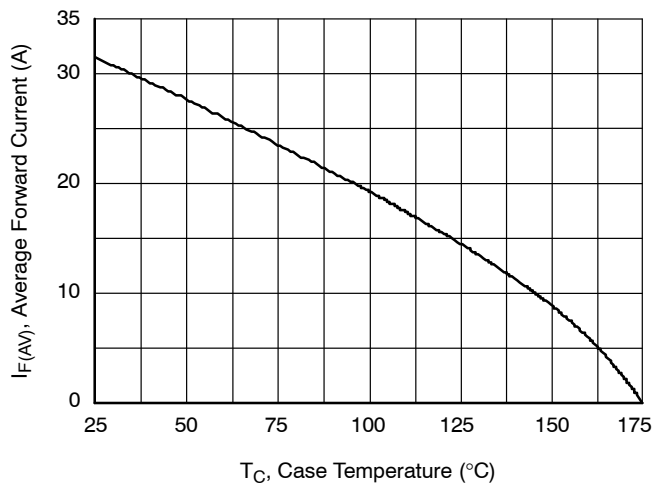


Figure 8. Forward Current Derating Curve

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TYPICAL PERFORMANCE CHARACTERISTICS (continued)

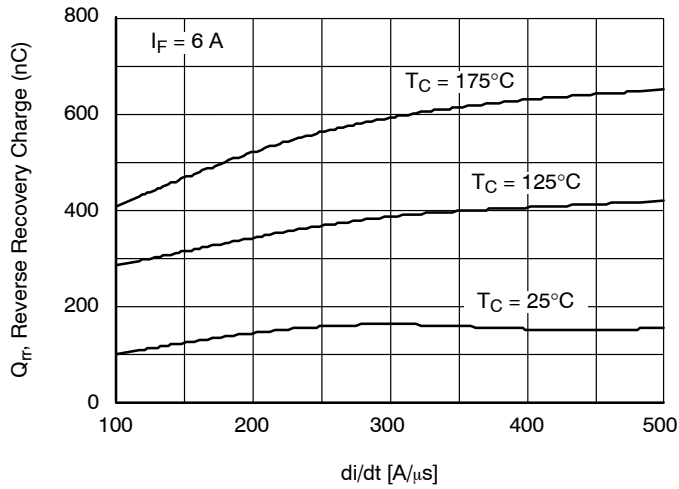


Figure 12. Reverse Recovery Charge

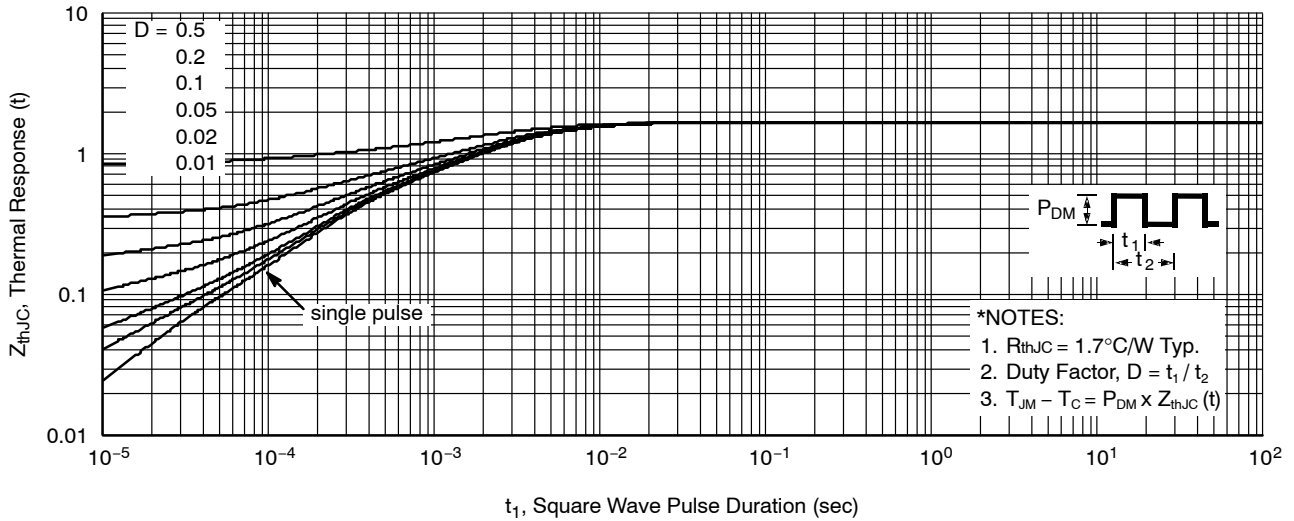


Figure 11. Transient Thermal Response Curve

ORDERING INFORMATION

Device	Device Marking	Package	Shipping†
RURD660S9A-F085	RUR660	TO-252 3 LD (Pb-Free)	2500 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

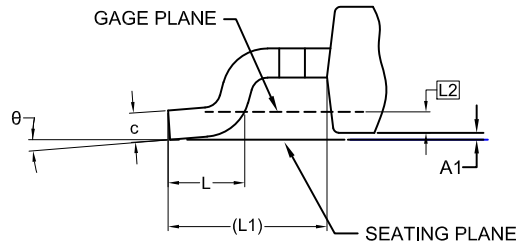


DPAK3 (TO-252 3 LD) CASE 369AS ISSUE A

DATE 28 SEP 2022



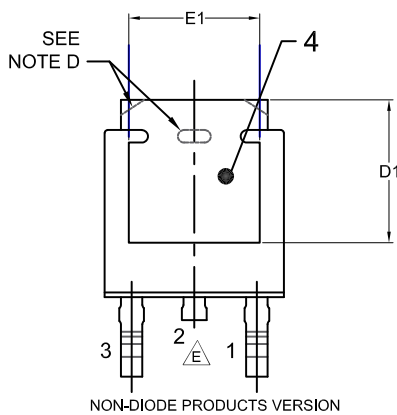
- NOTES: UNLESS OTHERWISE SPECIFIED
 A) THIS PACKAGE CONFORMS TO JEDEC, TO-252, ISSUE C, VARIATION AA.
 B) ALL DIMENSIONS ARE IN MILLIMETERS.
 C) DIMENSIONING AND TOLERANCING PER ASME Y14.5M-2009.
 D) SUPPLIER DEPENDENT MOLD LOCKING HOLES OR CHAMFERED CORNERS OR EDGE PROTRUSION.
 E) FOR DIODE PRODUCTS, L4 IS 0.25 MM MAX.
 F) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR EXTRUSIONS.
 G) LAND PATTERN RECOMMENDATION IS BASED ON IPC7351A STD TO228P991X239-3N.



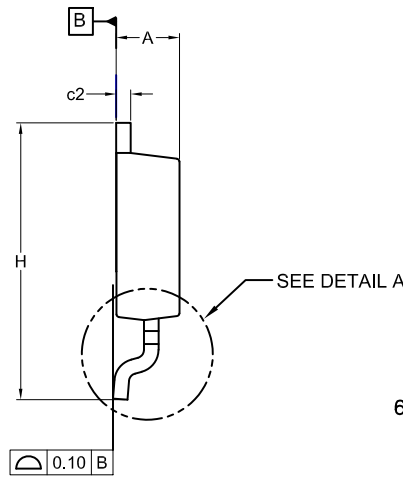
DETAIL A
(ROTATED -90°)
SCALE: 12X

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	2.18	2.29	2.39
A1	0.00	-	0.127
b	0.64	0.77	0.89
b2	0.76	0.95	1.14
b3	5.21	5.34	5.46
c	0.45	0.53	0.61
c2	0.45	0.52	0.58
D	5.97	6.10	6.22
D1	5.21	-	-
E	6.35	6.54	6.73
E1	4.32	-	-
e	2.286 BSC		
e1	4.572 BSC		
H	9.40	9.91	10.41
L	1.40	1.59	1.78
L1	2.90 REF		
L2	0.51 BSC		
L3	0.89	1.08	1.27
L4	-	-	1.02
θ	0°	--	10°

NON-DIODE PRODUCTS VERSION



NON-DIODE PRODUCTS VERSION

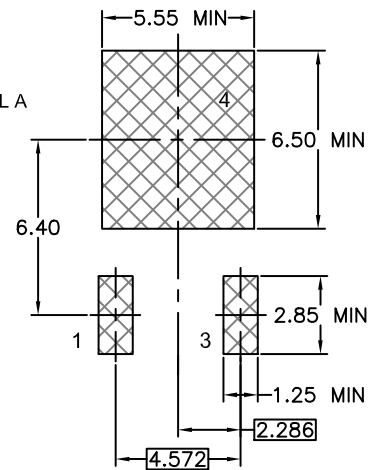


GENERIC MARKING DIAGRAM*



- XXXX = Specific Device Code
- A = Assembly Location
- Y = Year
- WW = Work Week
- ZZ = Assembly Lot Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



LAND PATTERN RECOMMENDATION

*FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

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DESCRIPTION:	DPAK3 (TO-252 3 LD)	PAGE 1 OF 1

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Europe, Middle East and Africa Technical Support:

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