



## 50 mΩ, Slew Rate Controlled Load Switch in WCSP

### DESCRIPTION

The SiP32467 and SiP32468 are slew rate controlled integrated high side load switches that operate in the input voltage range from 1.2 V to 5.5 V.

This series of design features slew rate control, reverse blocking when switch is off, output discharge, and control logic pull up. The devices are logic low enabled.

The SiP32467 and SiP32468 are available in compact wafer level WCSP package, WCSP4 0.76 mm x 0.76 mm with 0.4 mm pitch.

### FEATURES

- Low input voltage, 1.2 V to 5.5 V
- Low  $R_{on}$ , 54 mΩ/typ. at 3 V
- Slew rate control
- Compatible with 1.2 V to 3.3 V logic
- Reverse current blocking when switch is off
- Integrated output discharge switch (SiP32468)
- Integrated pull up resistor at “EN”
- For enable “high” see SiP32460, SiP32461, and SiP32462
- 4-bump WCSP package
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



### APPLICATIONS

- Smart phones
- GPS and portable media players
- Tablet computers
- Medical and healthcare equipment
- Industrial and instrumentation
- Game consoles

### TYPICAL APPLICATION CIRCUIT

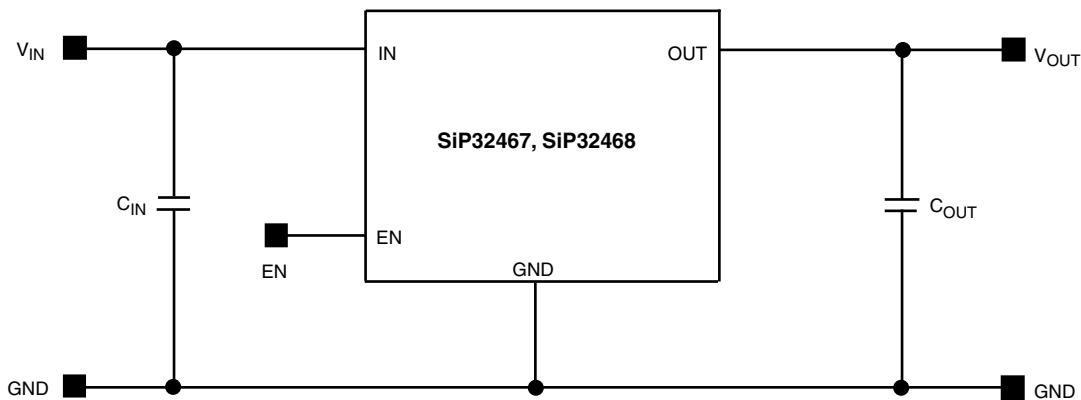


Fig. 1 - Typical Application Circuit

ORDERING INFORMATION					
PART NUMBER	PACKAGE	$t_{on}$ (μs)	$R_{DISCHARGE}$	MARK CODE	TEMPERATURE RANGE
SiP32467DB-T2-GE1	WCSP4 (2 x 2), 0.4 mm pitch	300	No	AJ	-40 °C to +85 °C
SiP32468DB-T2-GE1	WCSP4 (2 x 2), 0.4 mm pitch	300	Yes	AK	-40 °C to +85 °C



ABSOLUTE MAXIMUM RATINGS			
PARAMETER	CONDITIONS	LIMIT	UNIT
Supply input voltage ( $V_{IN}$ )	Reference to GND	-0.3 to 6.5	V
Output voltage ( $V_{OUT}$ )	Reference to GND	-0.3 to 6.5	
Output voltage ( $V_{OUT}$ )	Pulse at 1 ms reference to GND <sup>a</sup>	-1.6	
Enable input voltage EN	Reference to GND	-0.3 to 6.5	
Maximum continuous switch current		1.2	A
Maximum pulse switch current	Pulse at 1 ms, 10 % duty cycle	2	
ESD rating (HBM)		4000	V
Thermal resistance		205	°C/W
Maximum power dissipation	$T_A = 25\text{ °C}$	300	mW
Temperature			
Operating temperature		-40 to 85	°C
Operating junction temperature		125	
Storage temperature		-65 to 150	

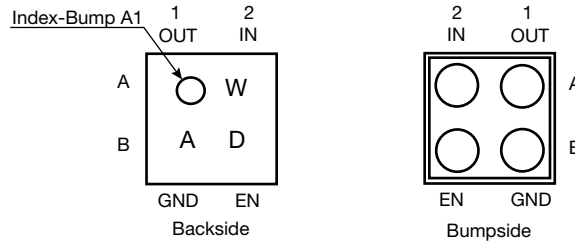
**Note**

a. Negative current injection up to 300 mA

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

RECOMMENDED OPERATING RANGE				
ELECTRICAL PARAMETER	MINIMUM	TYPICAL	MAXIMUM	UNIT
Input voltage ( $V_{IN}$ )	1.2	-	5.5	V
Output voltage ( $V_{OUT}$ )	1.2	-	5.5	

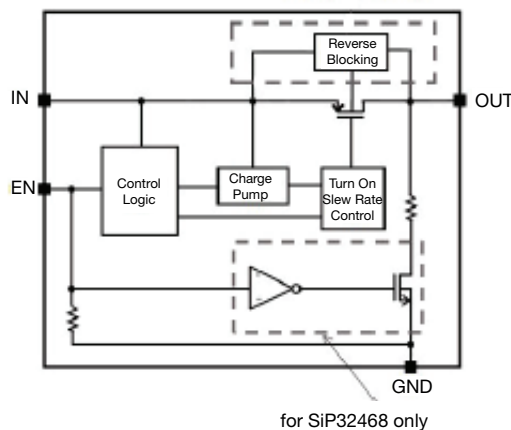
SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITION UNLESS OTHERWISE SPECIFIED $V_{IN} = 1.2\text{ V to }5.5\text{ V}, T_A = -40\text{ °C to }85\text{ °C}$	LIMITS			UNIT
			MIN.	TYP.	MAX.	
<b>Power Supply</b>						
Quiescent current	$I_Q$	$V_{IN} = 3.3\text{ V}, I_{OUT} = 0\text{ mA}$	-	6	8	$\mu\text{A}$
Shutdown current	$I_{SD}$	OUT = GND	-	0.01	2	
Off switch current	$I_{DS(off)}$	EN = $V_{IN}$ , OUT = GND	-	0.01	2	
Reverse blocking current	$I_{(in)RB}$	OUT = 5 V, IN = 1.2 V, EN = 1.2 V, (measured at IN pin)	-	0.01	1	
		OUT = 5 V, IN = 0 V, EN = open, (measured at IN pin)	-	0.01	1	
<b>Switch Resistance</b>						
On resistance	$R_{DS(on)}$	$I_{OUT} = 500\text{ mA}, V_{IN} = 1.2\text{ V}, T_A = 25\text{ °C}$	-	95	150	m $\Omega$
		$I_{OUT} = 500\text{ mA}, V_{IN} = 1.5\text{ V}, T_A = 25\text{ °C}$	-	80	120	
		$I_{OUT} = 500\text{ mA}, V_{IN} = 1.8\text{ V}, T_A = 25\text{ °C}$	-	70	100	
		$I_{OUT} = 500\text{ mA}, V_{IN} = 3\text{ V}, T_A = 25\text{ °C}$	-	54	65	
		$I_{OUT} = 500\text{ mA}, V_{IN} = 5\text{ V}, T_A = 25\text{ °C}$	-	50	65	
Discharge switch on resistance	$R_{PD}$	When $V_{IN} = 3\text{ V}$ at 25 °C	-	80	-	$\Omega$
		When $V_{IN} = 1.8\text{ V}$ at 25 °C	-	< 200	-	
EN pin pull up resistor	$R_{EN}$	EN = 1.2 V	1	2.6	5	M $\Omega$
On resistance temperature coefficient	$TC_{RDS}$		-	2800		ppm/°C
<b>On/off Logic</b>						
EN input low voltage	$V_{IL}$	$V_{IN} = 1.5\text{ V}$	0.4	-	-	V
EN input high voltage	$V_{IH}$	$V_{IN} = 5.5\text{ V}$	-	-	1	
<b>Switching Speed</b>						
Switch turn-on delay time	$t_{on\_DLY}$	$R_{LOAD} = 500\ \Omega, C_L = 0.1\ \mu\text{F}, V_{IN} = 5\text{ V}$	-	130	-	$\mu\text{s}$
Switch turn-on rise time	$t_r$	$R_{LOAD} = 500\ \Omega, C_L = 0.1\ \mu\text{F}, V_{IN} = 5\text{ V}$	-	170	-	
Switch turn-off delay time	$t_{off}$	$R_{LOAD} = 500\ \Omega, C_L = 0.1\ \mu\text{F}, (50\% V_{IN} \text{ to } 90\% V_{OUT})$	-	2	-	

**PIN CONFIGURATION**

**Fig. 2 - WCSP 2 x 2 Package**

DEVICE MARKING		
Row 1	<b>Dot + W</b>	: dot is A1 locator plus week code
Row 2	<b>AB</b>	: mark code for part number
SiP32467 = AJ		
SiP32468 = AK		

PIN DESCRIPTION (WCSP package)		
PIN#	NAME	FUNCTION
A1	OUT	Switch output
A2	IN	Switch input
B1	GND	Ground connection
B2	EN	Switch on/off control. A pull up resistor is integrated

TRUTH TABLE	
EN	SWITCH
1	Off
0	On

**BLOCK DIAGRAM**

**Fig. 3 - Functional Block Diagram**



TYPICAL CHARACTERISTICS (T<sub>J</sub> = 25 °C, unless otherwise noted)

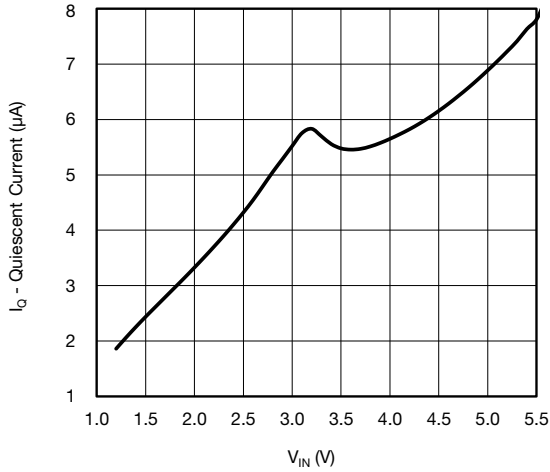


Fig. 4 - Quiescent Current vs. Input Voltage

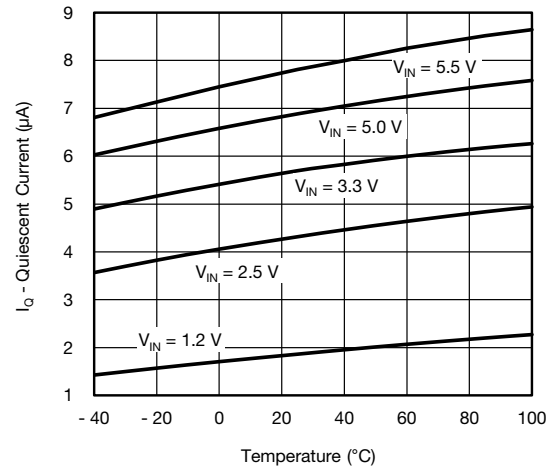


Fig. 7 - Quiescent Current vs. Temperature

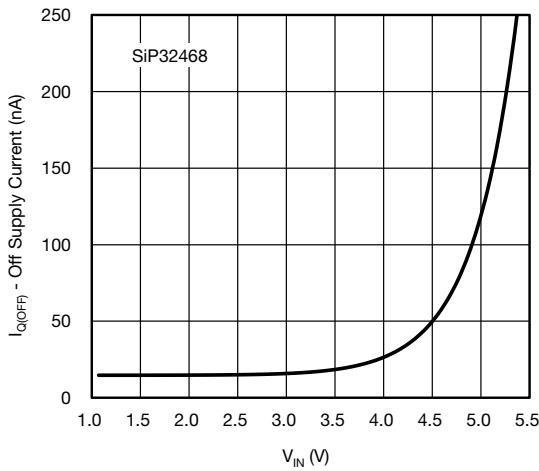


Fig. 5 - Off Supply Current vs. Input Voltage

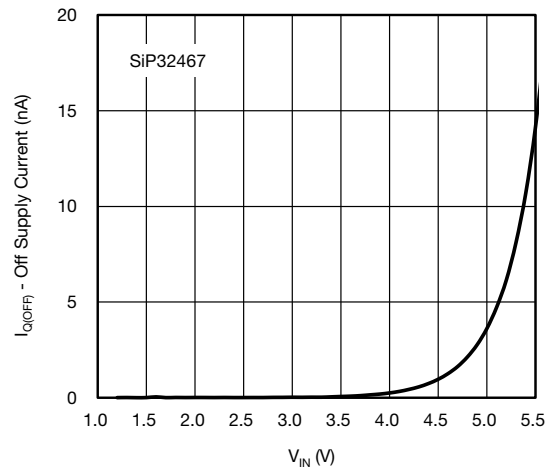


Fig. 8 - Off Supply Current vs. Input Voltage

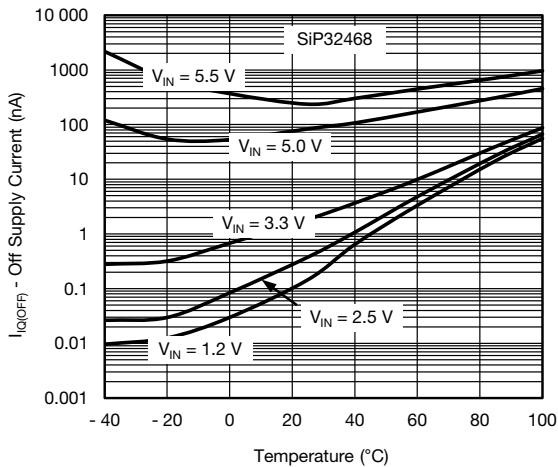


Fig. 6 - Off Supply Current vs. Temperature

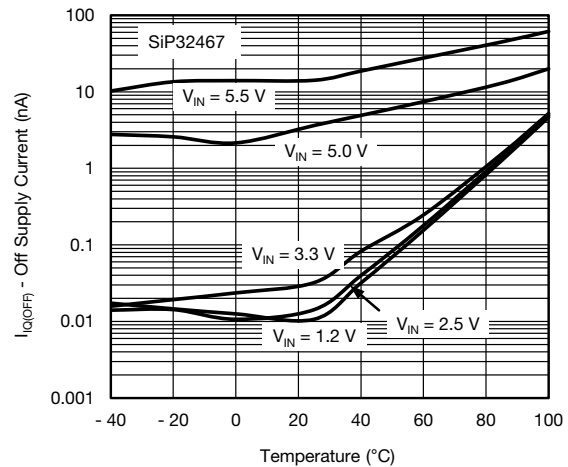


Fig. 9 - Off Supply Current vs. Temperature



TYPICAL CHARACTERISTICS (T<sub>J</sub> = 25 °C, unless otherwise noted)

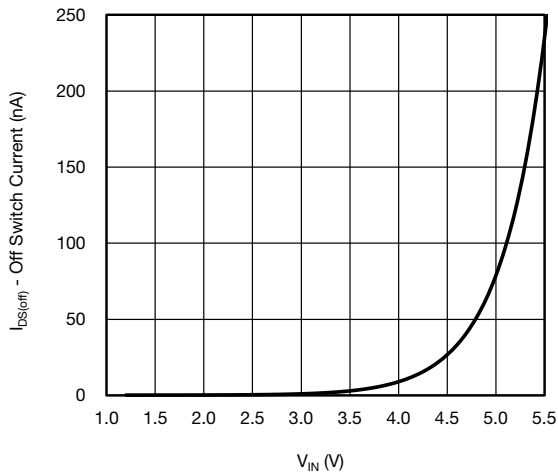


Fig. 10 - Off Switch Current vs. Input Voltage

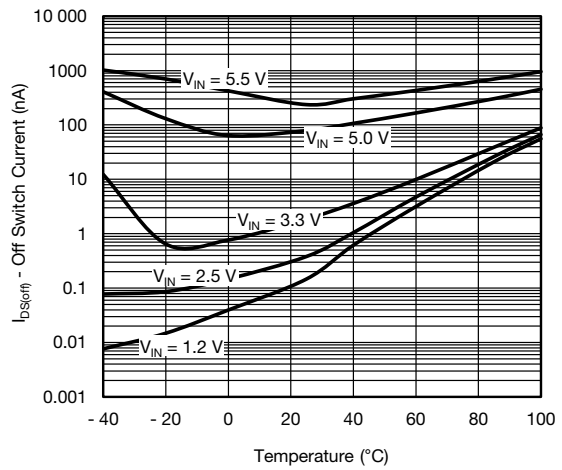


Fig. 13 - Off Switch Current vs. Temperature

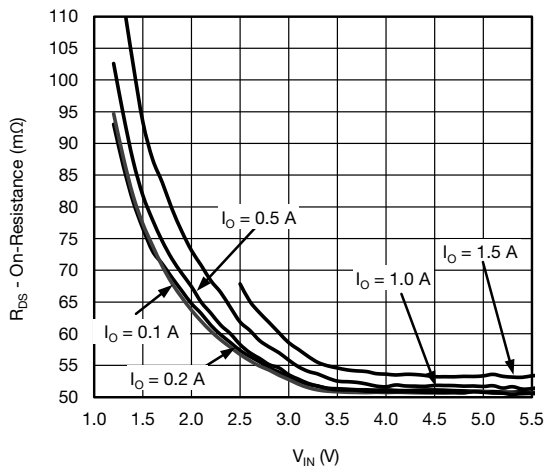


Fig. 11 - R<sub>DS(on)</sub> vs. Input Voltage

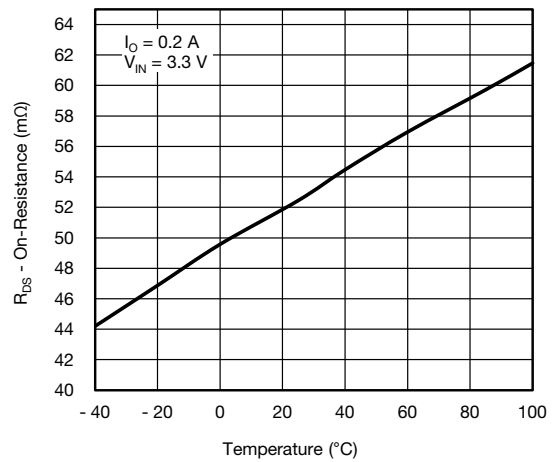


Fig. 14 - R<sub>DS(on)</sub> vs. Temperature

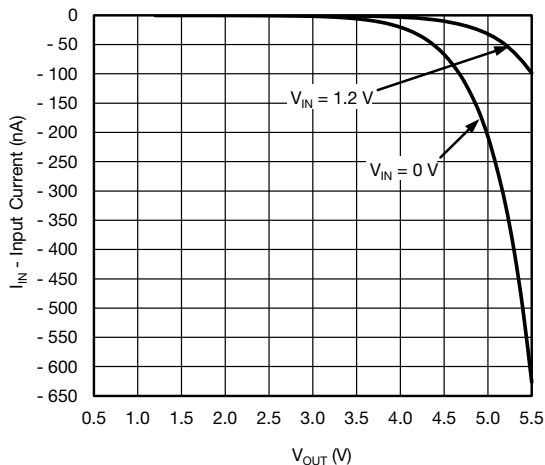


Fig. 12 - Reverse Blocking Current vs. Output Voltage

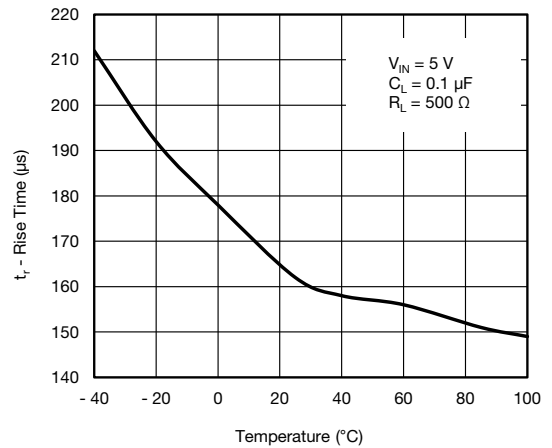


Fig. 15 - Rise Time vs. Temperature



TYPICAL CHARACTERISTICS (T<sub>J</sub> = 25 °C, unless otherwise noted)

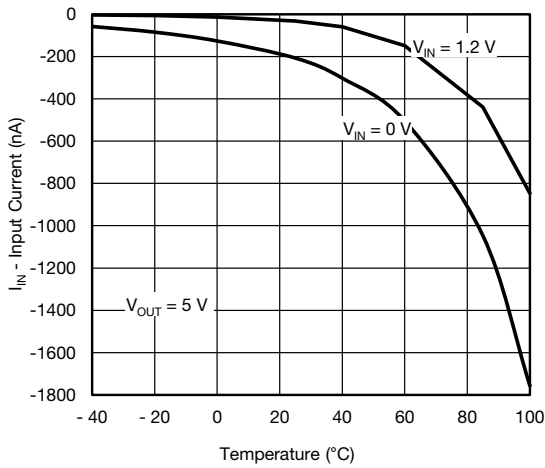


Fig. 16 - Reverse Blocking Current vs. Temperature

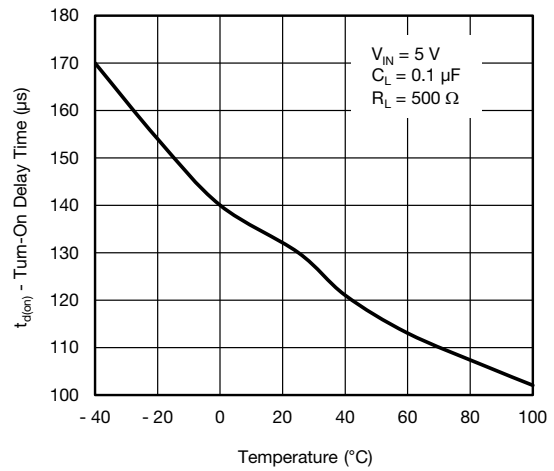


Fig. 19 - Turn-on Delay Time vs. Temperature

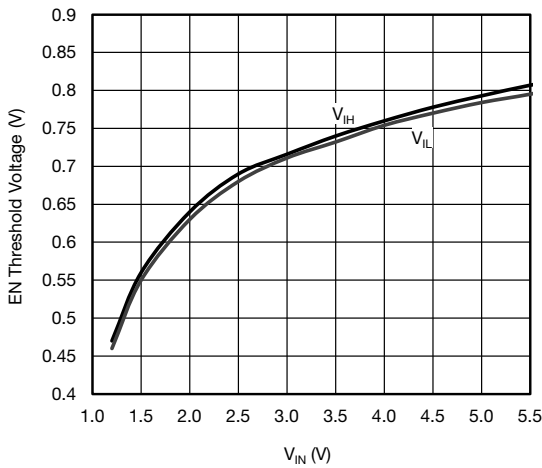


Fig. 17 - EN Threshold Voltage vs. Input Voltage

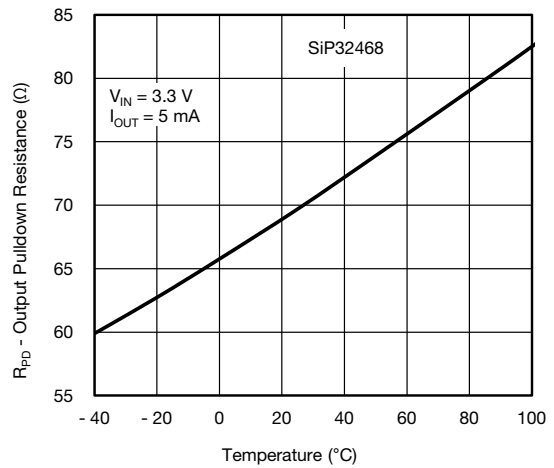


Fig. 20 - Output Pulldown Resistance vs. Temperature

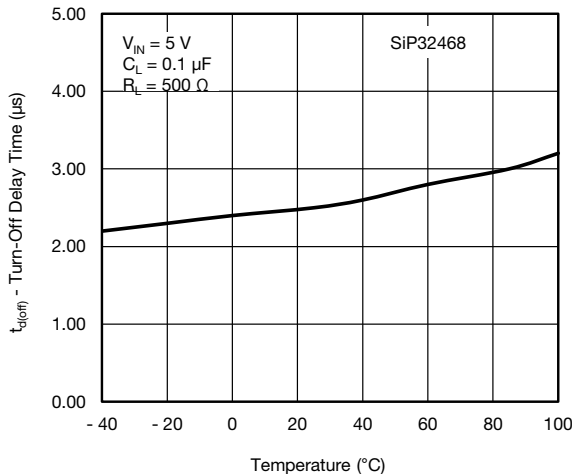


Fig. 18 - Turn-off Delay Time vs. Temperature

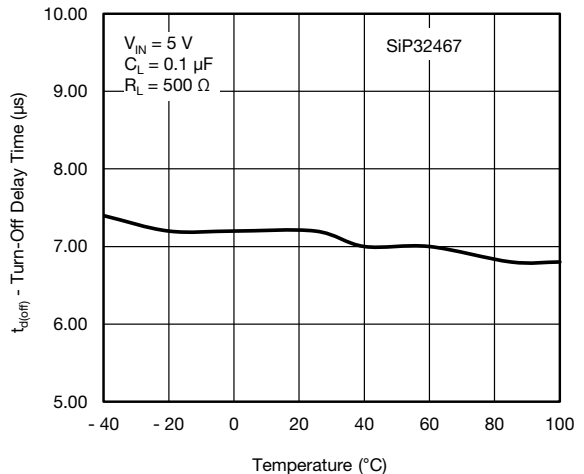
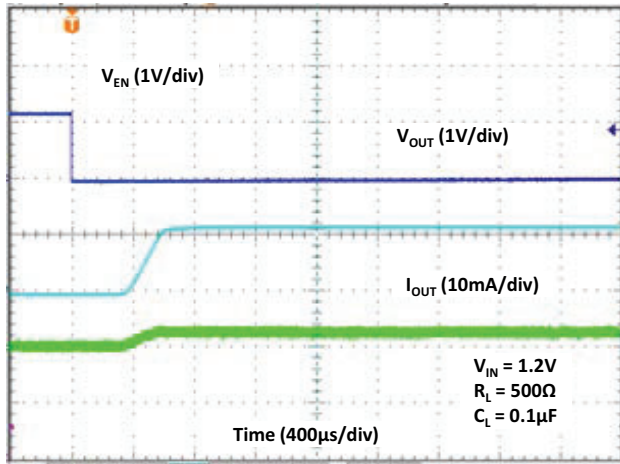
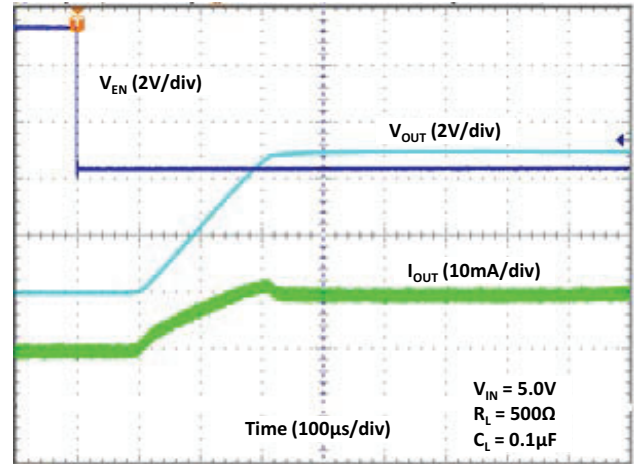
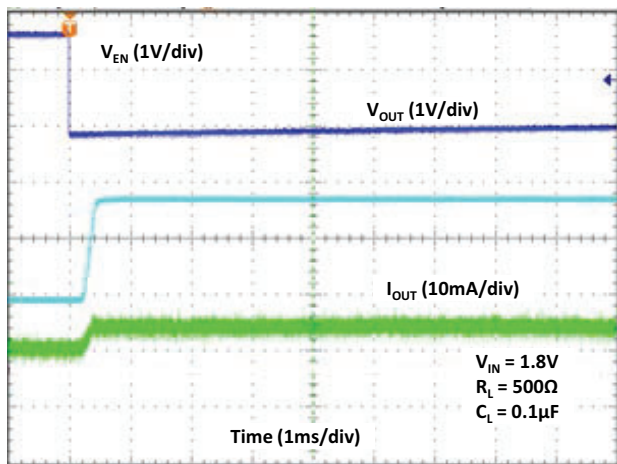
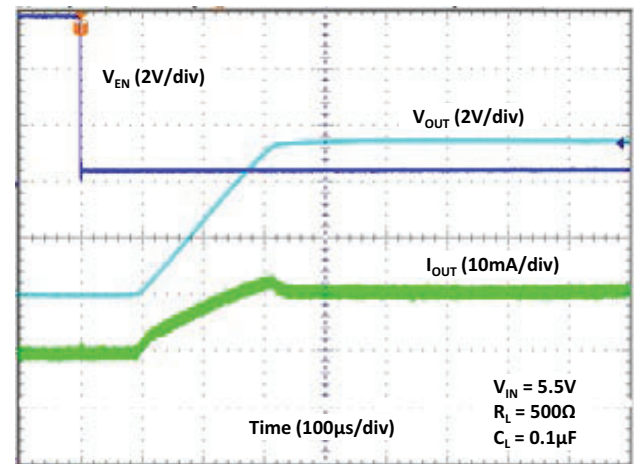
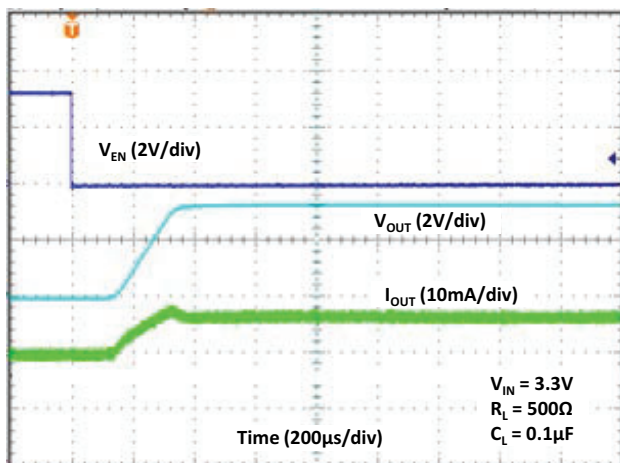
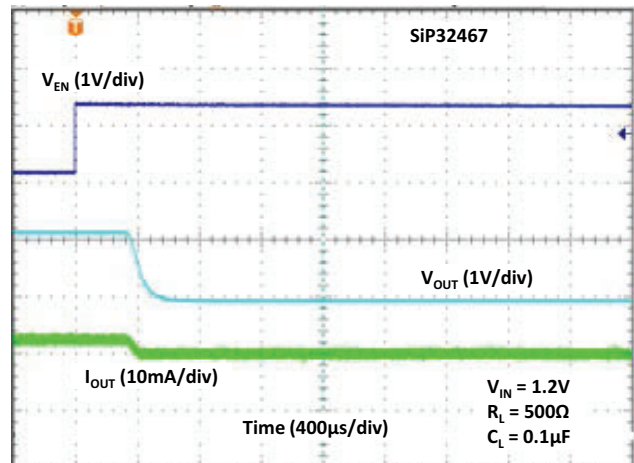
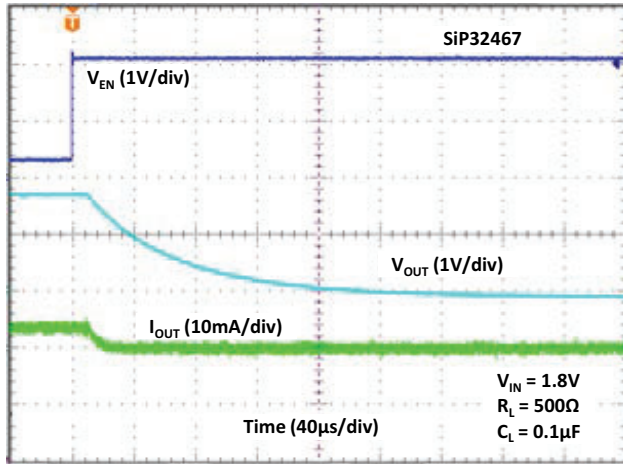
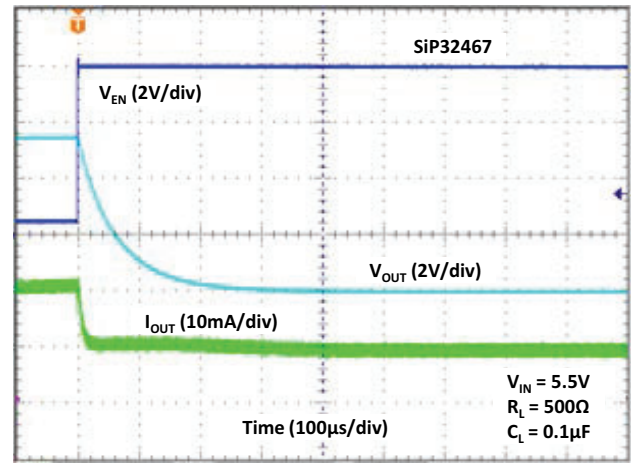
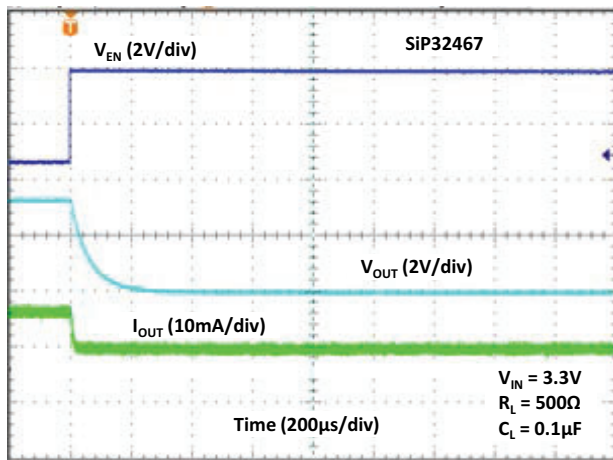
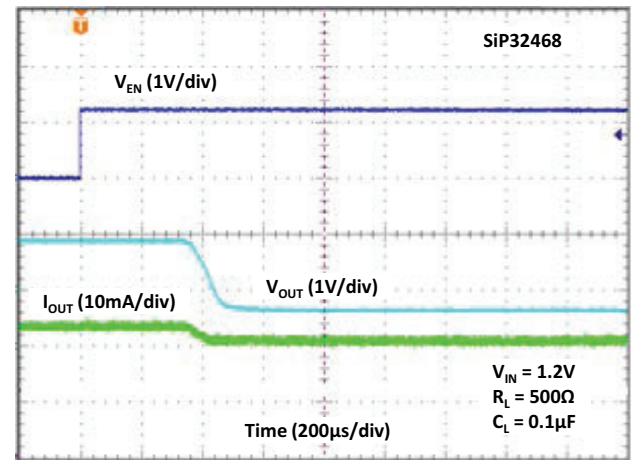
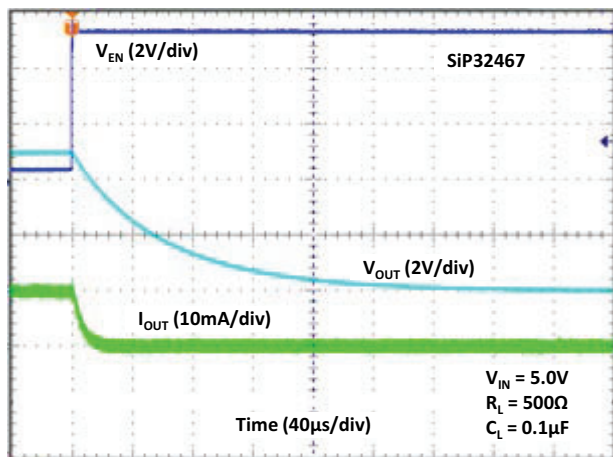
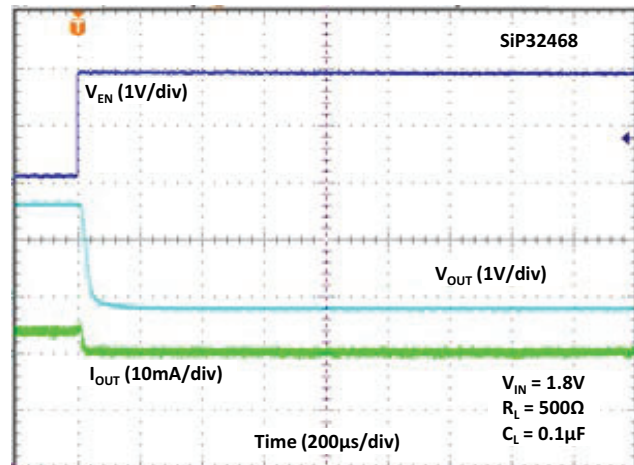
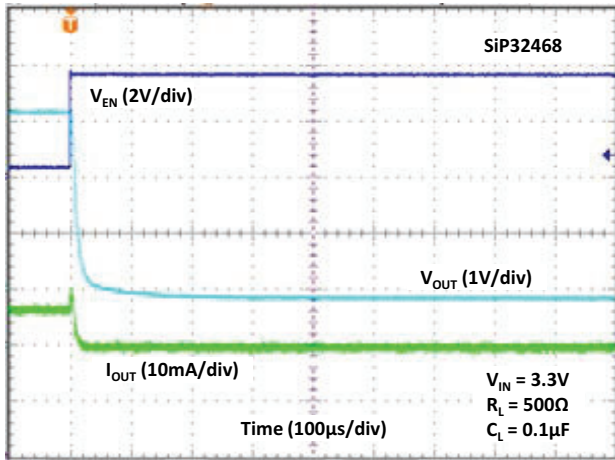
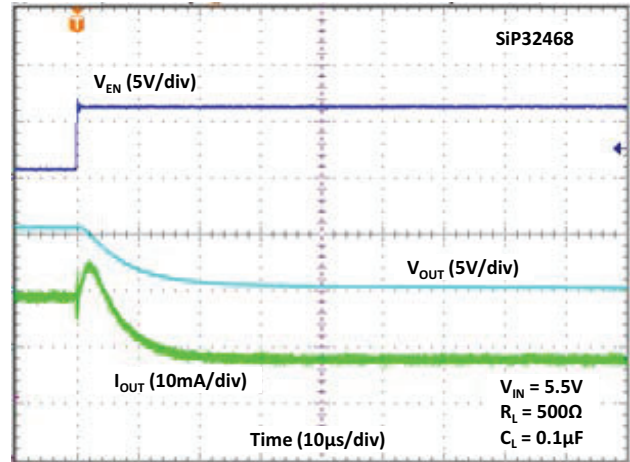
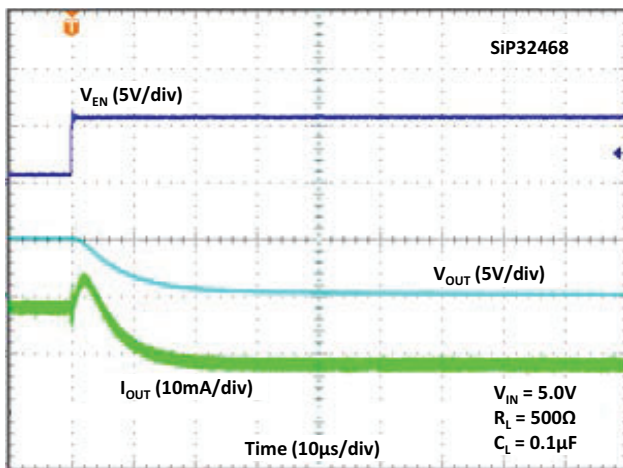


Fig. 21 - Turn-off Delay Time vs. Temperature

**TYPICAL WAVEFORMS**

**Fig. 22 - Turn-on Time**

**Fig. 25 - Turn-on Time**

**Fig. 23 - Turn-on Time**

**Fig. 26 - Turn-on Time**

**Fig. 24 - Turn-on Time**

**Fig. 27 - Turn-off Time**

**TYPICAL WAVEFORMS**

**Fig. 28 - Turn-off Time**

**Fig. 31 - Turn-off Time**

**Fig. 29 - Turn-off Time**

**Fig. 32 - Turn-off Time**

**Fig. 30 - Turn-off Time**

**Fig. 33 - Turn-off Time**



**TYPICAL WAVEFORMS**

**Fig. 34 - Turn-off Time**

**Fig. 36 - Turn-off Time**

**Fig. 35 - Turn-off Time**



## DETAILED DESCRIPTION

SiP32467 and SiP32468 are high side, slew rate controlled, load switches. They incorporate a negative charge pump at the gate to keep the gate to source voltage high when turned on. This keeps the on resistance low at lower input voltages. SiP32467 and SiP32468 are designed with slow slew rate to minimize the inrush current during turn on. These devices have a reverse blocking circuit, when disabled, to prevent the current from going back to the input when the output voltage is higher than the input voltage. The SiP32467 can be used as a bidirectional switch and can be turned on and off when power is at either in or out. The SiP32468 has an output pull down resistor to discharge the output capacitance when the device is off.

## APPLICATION INFORMATION

### Input Capacitor

While a bypass capacitor on the input is not required, a 4.7 μF or larger capacitor for C<sub>IN</sub> is recommended in almost all applications. The bypass capacitor should be placed as physically close as possible to the input pin to be effective in minimizing transients on the input. Ceramic capacitors are recommended over tantalum because of their ability to withstand input current surges from low impedance sources such as batteries in portable devices.

### Output Capacitor

A 0.1 μF capacitor across V<sub>OUT</sub> and GND is recommended to insure proper slew operation. There is inrush current through the output MOSFET and the magnitude of the inrush current depends on the output capacitor, the bigger the C<sub>OUT</sub> the higher the inrush current. There are no ESR or capacitor type requirement.

### Enable

The EN pin is compatible with CMOS logic voltage levels. It requires at least 1 V or above to fully shut down the device and 0.4 V or below to fully turn on the device. There is a 2.6 MΩ resistor connected between EN pin and IN pin.

### Protection Against Reverse Voltage Condition

This device contains a reverse blocking circuit. When disabled (V<sub>EN</sub> greater than 1 V) this circuit keeps the output current from flowing back to the input when the output voltage is higher than the input voltage.

### Thermal Considerations

Due to physical limitations of the layout and assembly of the device the maximum switch current is 1.2 A as stated in the Absolute Maximum Ratings table. However, another limiting

characteristic for the safe operating load current is the thermal power dissipation of the package.

The maximum power dissipation in any application is dependent on the maximum junction temperature, T<sub>J(max.)</sub> = 125 °C, the junction-to-ambient thermal resistance, θ<sub>J-A</sub> = 205 °C/W, and the ambient temperature, T<sub>A</sub>, which may be expressed as:

$$P \text{ (max.)} = \frac{T_{J(max.)} - T_A}{\theta_{JA}} = \frac{125 - T_A}{205}$$

It then follows that, assuming an ambient temperature of 70 °C, the maximum power dissipation will be limited to about 268 mW.

So long as the load current is below the 1.2 A limit, the maximum continuous switch current becomes a function two things: the package power dissipation and the R<sub>DS(on)</sub> at the ambient temperature.

As an example let us calculate the worst case maximum load current at T<sub>A</sub> = 70 °C. The worst case R<sub>DS(on)</sub> at 25 °C is 120 mΩ at V<sub>IN</sub> = 1.5 V. The R<sub>DS(on)</sub> at 70 °C can be extrapolated from this data using the following formula:

$$R_{DS(on)} \text{ (at 70 °C)} = R_{DS(on)} \text{ (at 25 °C)} \times (1 + T_C \times \Delta T)$$

Where T<sub>C</sub> is 2800 ppm/°C. Continuing with the calculation we have

$$R_{DS(on)} \text{ (at 70 °C)} = 120 \text{ m}\Omega \times (1 + 0.0028 \times (70 \text{ °C} - 25 \text{ °C})) = 135 \text{ m}\Omega$$

The maximum current limit is then determined by

$$I_{LOAD(max.)} < \sqrt{\frac{P \text{ (max.)}}{R_{DS(on)}}}$$

which in this case is 1.99 A. Under the stated input voltage condition, if the 1.99 A current limit is exceeded the internal die temperature will rise and eventually, possibly damage the device.

To avoid possible permanent damage to the device and keep a reasonable design margin, it is recommended to operate the device maximum up to 1.2 A only as listed in the Absolute Maximum Ratings table.

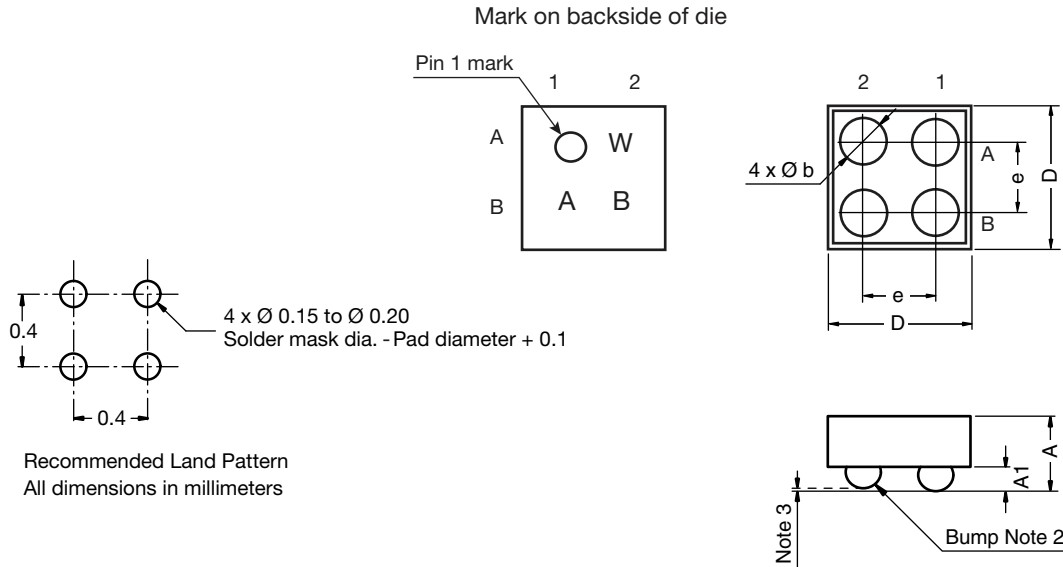


<b>PRODUCT SUMMARY</b>		
Part number	SiP32467	SiP32468
Description	1.2 V to 5.5 V, 50 mΩ, EN active low, bidirectional off isolation	1.2 V to 5.5 V, 50 mΩ, EN active low, bidirectional off isolation, output discharge
Configuration	Single	Single
Slew rate time (μs)	170	170
On delay time (μs)	130	130
Input voltage min. (V)	1.2	1.2
Input voltage max. (V)	5.5	5.5
On-resistance at input voltage min. (mΩ)	95	95
On-resistance at input voltage max. (mΩ)	50	50
Quiescent current at input voltage min. (μA)	1.8	1.8
Quiescent current at input voltage max. (μA)	7.8	7.8
Output discharge (yes / no)	No	Yes
Reverse blocking (yes / no)	Yes	Yes
Continuous current (A)	1.2	1.2
Package type	WCSP4	WCSP4
Package size (W, L, H) (mm)	0.8 x 0.8 x 0.5	0.8 x 0.8 x 0.5
Status code	2	2
Product type	Slew rate	Slew rate
Applications	Computers, consumer, industrial, healthcare, networking, portable	Computers, consumer, industrial, healthcare, networking, portable

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## WCSP4: 4 Bumps

(2 x 2, 0.4 mm pitch, 208 μm bump height, 0.8 mm x 0.8 mm die size)



DWG-No: 6004

### Notes

- (1) Laser mark on the backside surface of die
- (2) Bumps are SAC396
- (3) 0.05 max. coplanarity

DIM.	MILLIMETERS <sup>a</sup>			INCHES		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	0.515	0.530	0.545	0.0203	0.0209	0.0215
A1	0.208			0.0082		
b	0.250	0.260	0.270	0.0098	0.0102	0.0106
e	0.400			0.0157		
D	0.720	0.760	0.800	0.0283	0.0299	0.0315

### Note

- a. Use millimeters as the primary measurement



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