

Features

- High Density Cell Desihn for Ultra Low R_{DS(on)}
- Fully Characterized Avalanche Voltage and Current
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 5°C/W Junction to Case^(Note 2)

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V _{DS}	30	V
Gate-Source Volltage		V _{GS}	±20	V
Continuous Drain Current	T _C =25°C	I _D	30	Α
	T _C =100°C	'D	21	Α
Pulsed Drain Current ^(Note 3)		I _{DM}	60	А
Single Pulse Avalanche Energy ^(Note 4)		E _{AS}	70	mJ
Total Power Dissipation		P _D	25	W

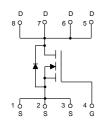
Note:

1. Halogen free "Green" products are defined as those which contain <900ppm bromine,

<900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

- 2. Surface Mounted on FR4 Board, t≤10 sec.
- 3. Pulse Width Limited by Maximum Junction Temperature.
- 4. EAS Condition: T_J =25°C, V_{DD} =15V, V_G =10V, L=0.1mH, Rg=25 Ω .

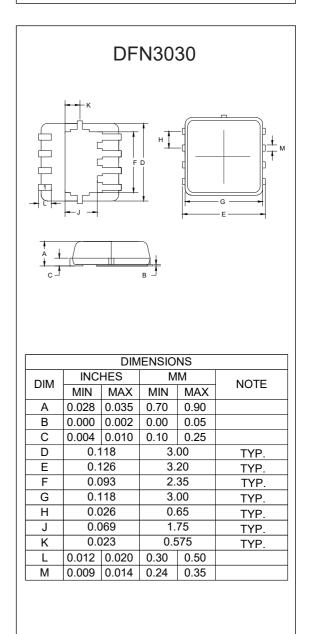
Internal Structure and Marking Code





Pin1







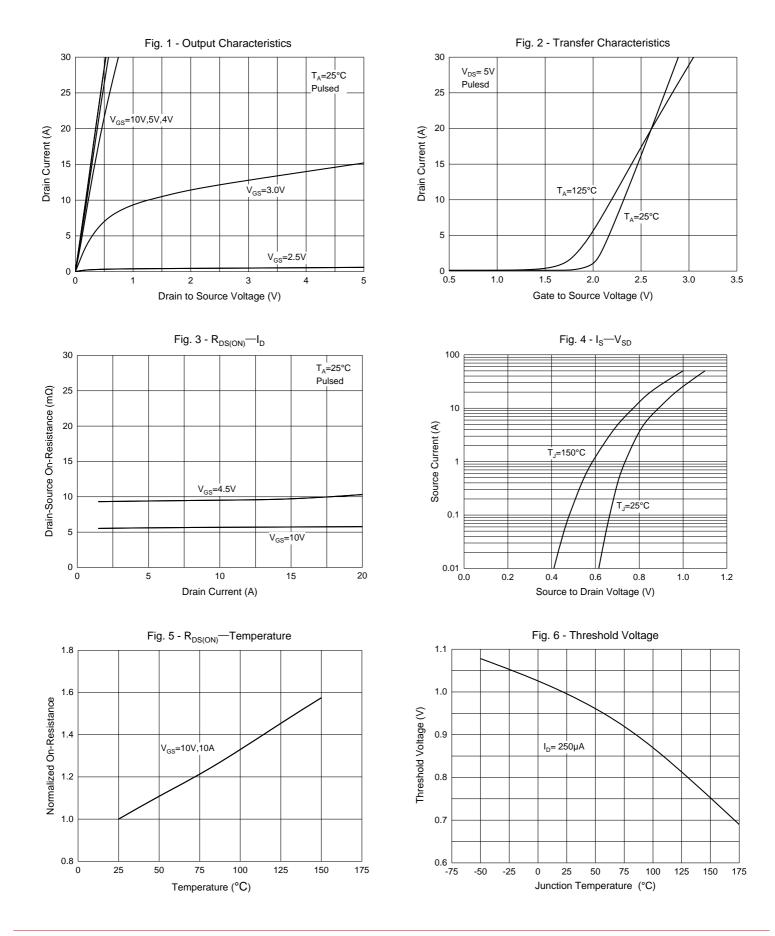
Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Тур	Мах	Unit	
Static Characteristics	1			1	I	1	
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250µA	30	33		V	
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V			1	μA	
Gate-Threshold Voltage ^(Note 5)	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1	1.5	2.3	V	
Drain-Source On-Resistance ^(Note 5)	R _{DS(on)}	V _{GS} =10V, I _D =10A		6.3	9	— mΩ	
		V _{GS} =4.5V, I _D =10A		9.2	13		
Forward Tranconductance ^(Note 5)	g fs	V _{DS} =5V, I _D =20A	15			S	
Dynamic Characteristics ^(Note 6)							
Input Capacitance	C _{iss}			1490			
Output Capacitance	C _{oss}	V _{DS} =15V,V _{GS} =0V,f=1MHz		220		pF	
Reverse Transfer Capacitance	C _{rss}			135			
Total Gate Charge	Qg			15			
Gate-Source Charge	Q _{gs}	V_{DS} =15V, V_{GS} =10V, I_{D} =9A		3		nC	
Gate-Drain Charge	Q _{gd}			4.5			
Turn-On Delay Time	t _{d(on)}			10			
Turn-On Rise Time	t _r	V _{DD} =15V,I _D =10A		8		- ns	
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10V, R_{GEN} =1.8 Ω		30			
Turn-Off Fall Time	t _f			5			
Drain-Source Body Diode Cha	racteristi	cs					
Continuous Body Diode Current	I _S				25	Α	
Body Diode Voltage	V _{SD}	I _{SD} =10A, V _{GS} =0V		0.85	1.2	V	
Reverse Recovery Time	t _{rr}	T _J =25°C, I _F =10A,di/dt=100A/µs		22	35	ns	
Reverse Recovery Charge	Q _{rr}	r_{J} = 20 C, r_{F} = 10A, al/at = 100A/µS		12	20	nC	
Forward Turn-On Time	t _{on}	Intrinsic Turn-On Time is Negligibl	e (Turn-On	is Domina	ated by LS	+LD)	

5. Pulse Test : Pulse Width≤300µs, Duty Cycle ≤2%.
6. Guaranteed by Design, Not Subject to Production Testing.



Curve Characteristics







Ordering Information

Device	Packing		
Part Number-TP	Tape&Reel: 3Kpcs/Reel		

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