

#### **Features**

- Split Gate Trench MOSFET Technology
- Excellent Package for Heat Dissipation
- High Density Cell Design for Low R<sub>DS(ON)</sub>
- · Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- · Moisture Sensitivity Level 1

# **Maximum Ratings**

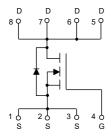
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 20°C/W Junction to Ambient(t<10s)<sup>(2)</sup>
- Thermal Resistance: 50°C/W Junction to Ambient(Steady-State)<sup>(2)</sup>
- Thermal Resistance: 1.42°C/W Junction to Case(Steady-State)

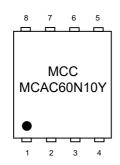
Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DS</sub>	100	V
Gate-Source Volltage	V <sub>GS</sub>	±20	V
Continuous Drain Current	I <sub>D</sub>	60	Α
Pulsed Drain Current <sup>(3)</sup>	I <sub>DM</sub>	240	Α
Total Power Dissipation <sup>(4)</sup>	P <sub>D</sub>	88	W
Single Pulsed Avalanche Energy <sup>(5)</sup>	E <sub>AS</sub>	200	mJ

#### Note:

- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. The value of  $R_{\theta JA}$  is measured with the device mounted on  $1\text{in}^2$  FR-4 board with 2oz. Copper, in a still air environment with  $T_A$ =25°C. The Power dissipation  $P_{DSM}$  is based on  $R_{\theta JA}$  t≤10s and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- 3. Repetitive rating; pulse width limited by max. junction temperature.
- 4.  $P_D$  is based on max. junction temperature, using junction-case thermal resistance.
- 5.  $V_{DD}$ =50V,  $R_{G}$ =25 $\Omega$ , L=0.5mH.

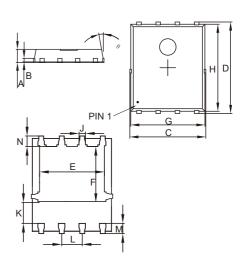
# **Internal Structure and Marking Code**





# N-CHANNEL MOSFET

# **DFN5060**



DIMENSIONS						
DIM	INCHES		MM		NOTE	
DIIVI	MIN	MAX	MIN	MAX	NOTE	
Α	0.031	0.047	0.80	1.20		
В	0.010		0.254		TYP.	
С	0.193	0.222	4.90	5.64		
D	0.232	0.250	5.90	6.35		
Е	0.148	0.167	3.75	4.25		
F	0.126	0.154	3.20	3.92		
G	0.189	0.213	4.80	5.40		
Н	0.222	0.239	5.65	6.06		
K	0.045	0.059	1.15	1.50		
J	0.012	0.020	0.30	0.50		
L	0.046	0.054	1.17	1.37		
М	0.012	0.028	0.30	0.71		
N	0.016	0.028	0.40	0.71		

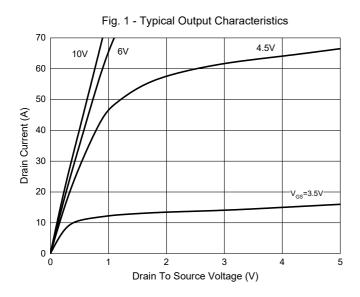


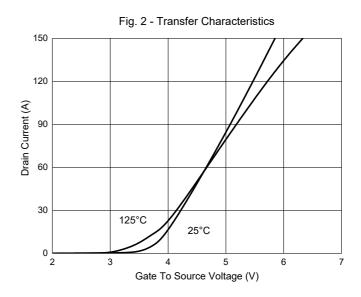
# Electrical Characteristics @ 25°C (Unless Otherwise Specified)

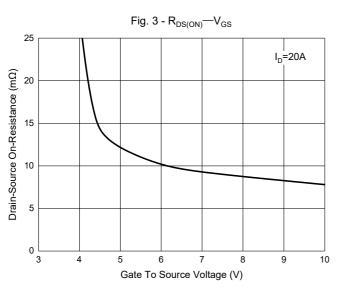
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics			'				
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	100			V	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V			1	μA	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	2.2	2.8	3.4	V	
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A		7.2	8.6	mΩ	
Gate Resistance	$R_G$	f=1MHz, Open drain		0.68		Ω	
Diode Characteristics			•	•			
Continuous Body Diode Current	Is				60	Α	
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =20A			1.3	V	
Reverse Recovery Time	t <sub>rr</sub>	L 004 II / II 4004 /		51.8		ns	
Reverse Recovery Charge	Q <sub>rr</sub>	l <sub>F</sub> =20A, dl <sub>F</sub> /dt=100A/μs		84		nC	
Dynamic Characteristics			•	•			
Input Capacitance	C <sub>iss</sub>			2431			
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =50V,V <sub>GS</sub> =0V,f=1MHz		715		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			32			
Total Gate Charge	$Q_g$			32			
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =50V,V <sub>GS</sub> =10V,I <sub>D</sub> =25A		11.1		nC	
Gate-Drain Charge	$Q_{gd}$			4.8			
Turn-On Delay Time	t <sub>d(on)</sub>			51			
Turn-On Rise Time	t <sub>r</sub>	V <sub>DS</sub> =50V, V <sub>GEN</sub> =10V,		14.5		<b>F</b> -	
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_G=2.2\Omega$ , $I_{DS}=25A$		69		ns	
Turn-Off Fall Time	t <sub>f</sub>			20.7			

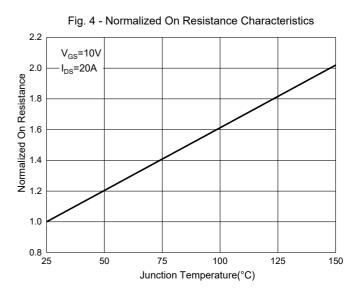


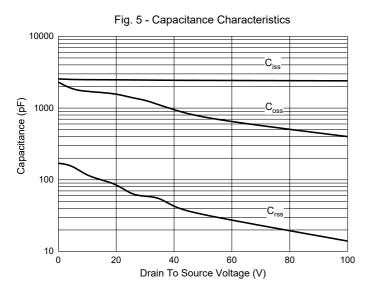
### **Curve Characteristics**

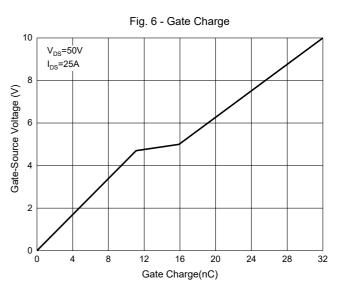














### **Curve Characteristics**

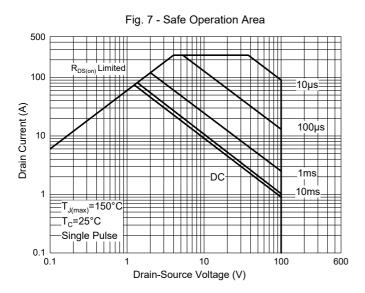
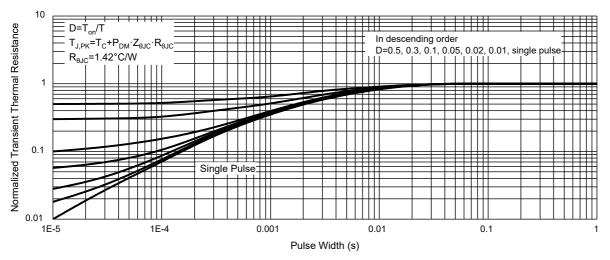


Fig. 8 - Normalized Maximum Transient Thermal Impedance



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# **Ordering Information**

Device	Packing	
Part Number-TP	Tape&Reel: 5Kpcs/Reel	

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