ALLEGRO microsystems

High Precision Hall-Effect AC-Coupled Differential Sensor IC with Integrated Filter Capacitor

FEATURES AND BENEFITS

- Integrated tracking capacitor
- Used for sensing motion of ring magnet or ferrous targets
- Wide operating temperature range
- Operation with magnetic input signal frequency from 20 Hz to 30 kHz
- EMI/ESD-resistant
- Large effective air gaps
- 4.0 to 26.5 V supply operating range
- Output compatible with CMOS logic families
- Reverse battery protection
- Resistant to mechanical and thermal stress
- Accurate true zero crossing switch point (A1421 only)
- High vibration immunity, in running mode (A1423 only)

PACKAGE: 4-pin SIP (suffix K)

Not to scale

DESCRIPTION

The A1421, A1422, and A1423 are AC-coupled Hall-effect sensor ICs which include monolithic integrated circuits that switch in response to changing differential magnetic fields created by rotating ring magnets or, when coupled with a magnet, by ferrous targets. This family of devices also includes an integrated capacitor that provides the high accuracy of analog sensing without an external filter capacitor. This reduces cost and components, while improving the reliability of the final sensor solution.

Magnetic field changes affect the two integrated Hall transducers and then are differentially amplified on the chip. Differential design provides immunity to radial vibration, within the device operating air gap range, by rejection of this common-mode signal change. Steady-state system offsets are eliminated using an on-chip differential bandpass filter with integrated capacitor. This filter also provides relative immunity to interference from electromagnetic sources. The device utilizes advanced temperature compensation for the high-pass filter, sensitivity, and Schmitt trigger switch points to guarantee optimal operation to low frequencies over a wide range of air gaps and temperatures.

Each device includes: a voltage regulator, two Hall transducers, temperature compensating circuitry, a low-level amplifier, bandpass filter, Schmitt trigger, and an output driver. The on-board regulator permits operation with supply

Functional Block Diagram
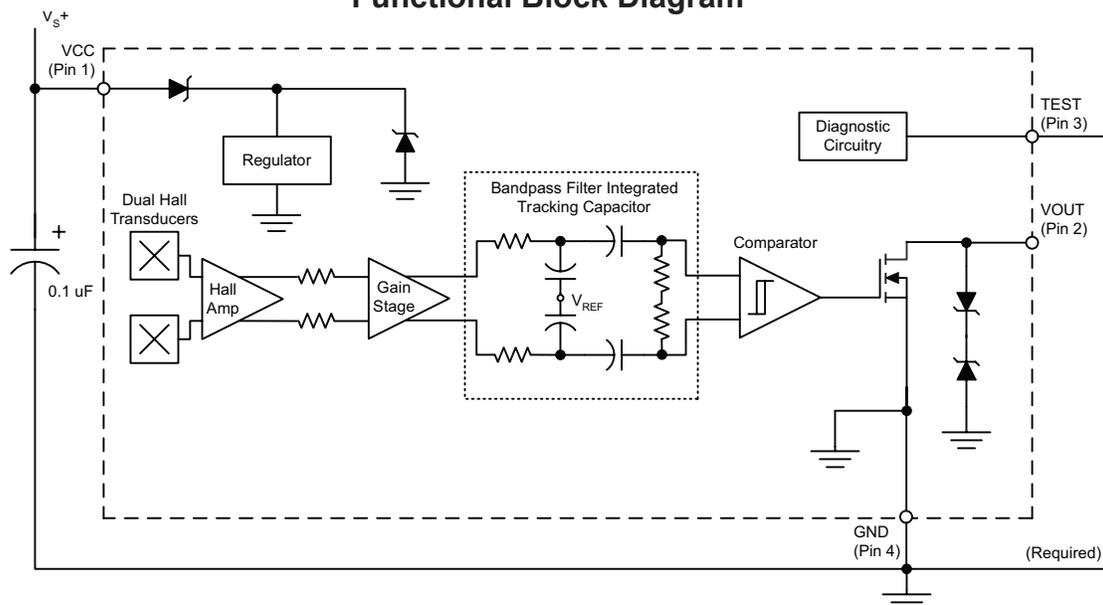
DESCRIPTION (continued)

voltages from 4.0 to 26.5 V. The output stage can switch 20 mA over the full frequency response range of the device, and is compatible with CMOS logic circuits.

The devices in this family differ from each other in their switch point specifications and their switching polarity. The A1421 has a small hysteresis and asymmetrical switch points, with one switch point at the zero-crossing. The A1422 has a small hysteresis and symmetrical switch points, both near the zero-crossing. The A1423 offers high vibration immunity, by means of its larger hysteresis that establishes symmetrical switch points further from

the zero-crossing. The output polarities are shown in the Selection Guide table.

This variety of options provides flexibility for achieving solutions for a wide range of applications, including automotive transmission and crankshaft speed sensing.

The device package has an operating ambient temperature range –40°C to 150°C , and is provided in a 4-pin plastic SIP. Each package is available in a lead (Pb) free version (suffix, –T) , with a 100% matte tin plated leadframe.

SELECTION GUIDE

| Part Number	Output Switching at B_{DIFF} = 0		Switch Points		Symmetry			Packing [1]
	B_{Diff} Increasing	B_{Diff} Decreasing	$B_{OP(typ)}$ (G)	$B_{RP(typ)}$ (G)	$B_{OP(max)}$+ $B_{RP(min)}$ (G)	$B_{OP(typ)}$+ $B_{RP(typ)}$ (G)	$B_{OP(min)}$+ $B_{RP(max)}$ (G)	
A1421LK-T [2]	Low (On) to High (Off)	High (Off) to Low (On)	15	0	15	15	7.5	Bulk, 500 pieces/bag
A1422LK-T	High (Off) to Low (On)	Low (On) to High (Off)	15	−15	0	0	0	
A1423LK-T	High (Off) to Low (On)	Low (On) to High (Off)	65	−65	0	0	0	

[1] Contact Allegro for additional packing options.
[2] A1421LK-T is DISCONTINUED. This classification indicates that the product is obsolete and is no longer in production. Samples are no longer available. Date of status change: July 2, 2018. For existing customer transition, and for new customers or new applications, contact Allegro Sales.

ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	Notes	Rating	Units
Supply Voltage	V_{CC}	Refer to Power Derating section	28	V
Reverse Supply Voltage	V_{RCC}		−18	V
Output Current	I_{OUT}		25	mA
Reverse-Output Current	I_{ROUT}		−50	mA
Operating Ambient Temperature	T_A	Range L	−40 to 150	°C
Maximum Junction Temperature	$T_{J(max)}$		165	°C
Storage Temperature	T_{stg}		−65 to 170	°C

Pinout Diagram
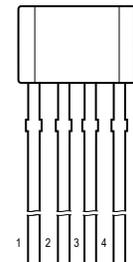
Terminal List Table

Number	Name
1	VCC
2	VOUT
3	TEST
4	GND

2

OPERATING CHARACTERISTICS Valid at $T_A = -40°C$ to $150°C$, $T_J \leq 165°C$; over operational air gap range and V_{CC} within operating range, unless otherwise noted. Typical operating parameters: $V_{CC} = 12$ V and $T_A = 25°C$.

Characteristic	Symbol	Test Conditions	Min.	Typ.	Max.	Units
ELECTRICAL CHARACTERISTICS						
Supply Voltage	V_{CC}	Operating; $T_J < T_{J(max)}$	4.0	12	26.5	V
Supply Current	I_{CC}		–	4.2	7.0	mA
Output Saturation Voltage	$V_{OUT(SAT)}$	$I_{SINK} = 20$ mA	–	140	400	mV
Output Leakage Current	I_{OFF}	$V_{OUT} = 24$ V, $B_{diff} = 0$	–	–	5	µA
PROTECTION COMPONENT CHARACTERISTICS						
Reverse Supply Current	I_{RCC}	$V_{CC} = -18$ V	–	–	–1	mA
Supply Zener Current	$I_{ZSupply}$	$V_S = 28$ V	–	–	10	mA
Supply Zener Clamp Voltage [1]	$V_{ZSupply}$	$I_{CC} = 10$ mA, $T_A = 25°C$	28	33	37	V
Output Zener Current	$I_{ZOutput}$	$V_{OUT} = 28$ V	–	–	3	mA
Output Zener Clamp Voltage	$V_{ZOutput}$	$I_{OUT} = 3$ mA, $T_A = 25°C$	28	–	–	V
Output Short Circuit Current Limit	$I_{OUTS(lim)}$		–	–	50	mA
RESPONSE CHARACTERISTICS						
Power-On State	POS	$t < t_{Response}$	–	High	–	V
Power-On Time [2][6]	t_{PO}	$V_{CC} > V_{CC(min)}$	–	4.5	9	ms
Settling Time [3][6]	$t_{Settling}$	$f_{Bdiff} \geq 100$ Hz	0	–	50	ms
Response Time [6]	$t_{Response}$	Equal to $t_{PO} + t_{Settling}$; $f_{Bdiff} \geq 100$ Hz	4.5	–	59	ms
Upper Corner Frequency	f_{CU}	–3 dB, single pole	20	–	–	kHz
Lower Corner Frequency	f_{CL}	–3 dB, single pole	–	–	20	Hz
OUTPUT CHARACTERISTICS						
Output Rise Time [4]	t_r	$R_{PU} = 1$ kΩ, $C_{OUTC2} = 10$ pF	–	–	200	ns
Output Fall Time	t_f	$R_{PU} = 1$ kΩ, $I_{SINK} = 20$ mA, $C_{OUTC2} = 10$ pF	–	–	200	ns

Continued on next page.

OPERATING CHARACTERISTICS, continued Valid at $T_A = -40°C$ to $150°C$, $T_J \le 165°C$; over operational air gap range and V_{CC} within operating range, unless otherwise noted. Typical operating parameters: $V_{CC} = 12$ V and $T_A = 25°C$.

Characteristic	Symbol	Test Conditions	Min.	Typ.	Max.	Units
MAGNETIC CHARACTERISTICS [5][6]						
Operate Point	B_{OP}	1421, B_{diff} increasing, f_{Bdiff} = 200 Hz, B_{diff} = 50 Gp-p V_{OUT} switches from low (on) to high (off)	0.0	15.0	27.5	G
		1422, B_{diff} increasing, f_{Bdiff} = 200 Hz, B_{diff} = 50 Gp-p V_{OUT} switches from high (off) to low (on)	5.0	15.0	35.0	G
		1423, B_{diff} increasing, f_{Bdiff} = 200 Hz, B_{diff} = 200 Gp-p V_{OUT} switches from high (off) to low (on)	10.0	65.0	100.0	G
Release Point	B_{RP}	1421, B_{diff} decreasing, f_{Bdiff} = 200 Hz, B_{diff} = 50 Gp-p V_{OUT} switches from high (off) to low (on)	−12.5	0.0	7.5	G
		1422, B_{diff} decreasing, f_{Bdiff} = 200 Hz, B_{diff} = 50 Gp-p V_{OUT} switches from low (on) to high (off)	−35.0	−15.0	−5.0	G
		1423, B_{diff} decreasing, f_{Bdiff} = 200 Hz, B_{diff} = 200 Gp-p V_{OUT} switches from low (on) to high (off)	−100	−65.0	−10.0	G
Hysteresis	B_{HYS}	1421, f_{Bdiff} = 200 Hz, B_{diff} = 50 Gp-p	5	15	35	G
		1422, f_{Bdiff} = 200 Hz, B_{diff} = 50 Gp-p	–	30	–	G
		1423, f_{Bdiff} = 200 Hz, B_{diff} = 200 Gp-p	–	130	–	G
Applied Magnetic Field [7]	B_{diff}	Differential p-p magnetic field	–	–	1250	G

[1] I_{CC} is equivalent to $I_{CC(max)}$ + 3 mA.
[2] Time required to initialize device.
[3] Time required for the output switch points to be within specification.
[4] Output Rise Time will be dominated by the RC time constant.
[5] For lower frequencies, the absolute values of B_{OP}, B_{RP}, and B_{HYS} may decrease due to delay induced by the high-pass filter.
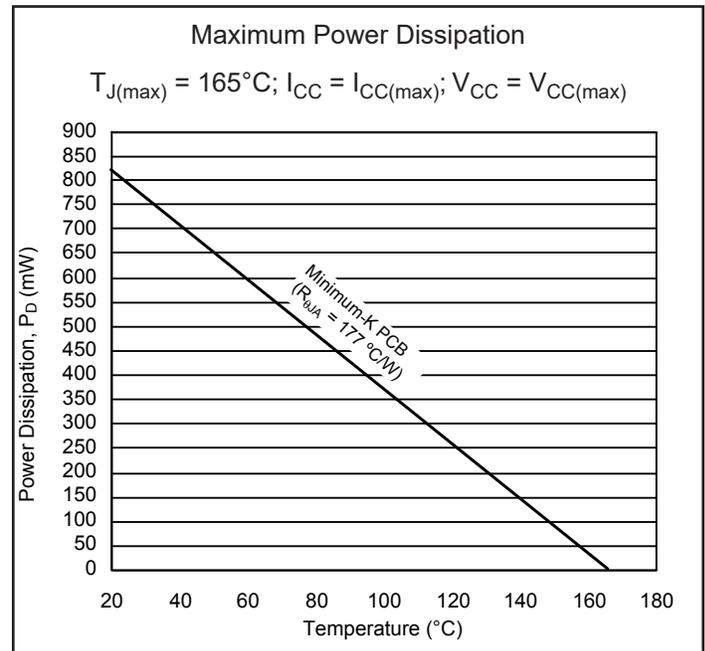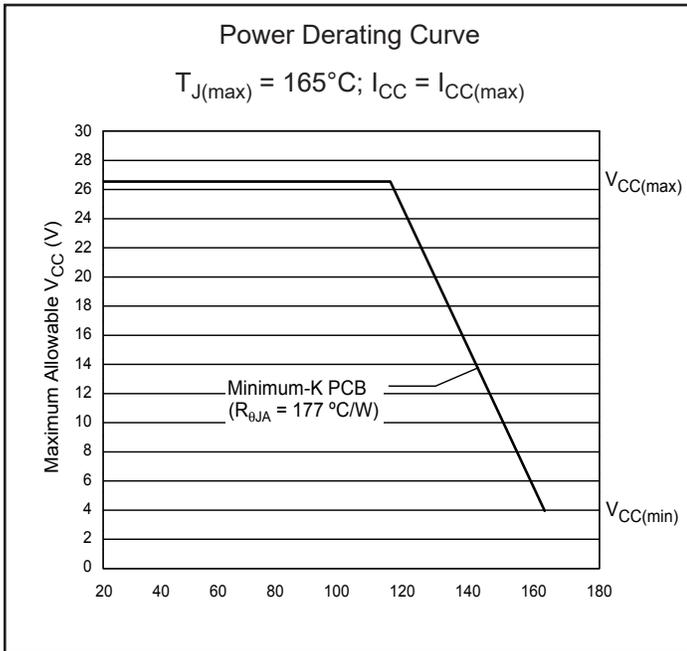[6] See Definitions of Terms section.
[7] Exceeding the maximum magnetic field may result in compromised absolute accuracy.

THERMAL CHARACTERISTICS may require derating at maximum conditions, see application information

Characteristic	Symbol	Test Conditions*	Value	Units
Package Thermal Resistance	$R_{\theta JA}$	Minimum-K PCB (single-sided with copper limited to solder pads)	177	°C/W

*In still air. Additional thermal information available on Allegro website.

Power Derating Curve

$T_{J(max)} = 165°C$; $I_{CC} = I_{CC(max)}$

Maximum Power Dissipation

$T_{J(max)} = 165°C$; $I_{CC} = I_{CC(max)}$; $V_{CC} = V_{CC(max)}$

Definitions of Terms

The following provide additional information about some of the parameters cited. For additional information, visit the Allegro Web site at *www.allegromicro.com*.

Applied Magnetic Field, B_{diff} – The differential magnetic flux density, which is calculated as the arithmetic difference of the flux densities observed by each of the two Hall elements. f_{Bdiff} is the input signal frequency.

Output Off Switch Point (Operate Point), B_{OP} – The value of increasing differential magnetic flux density at which the device output switches from low to high (A1421) or high to low (A1422 and A1423).

Output On Switch Point (Release Point), B_{RP} – The value of decreasing differential magnetic flux density at which the device output switches from high to low (A1421) or from low to high (A1422 and A1423).

Power-On Time, t_{PO} – The time needed by the device, after power is applied, to initialize all circuitry necessary for proper operation.
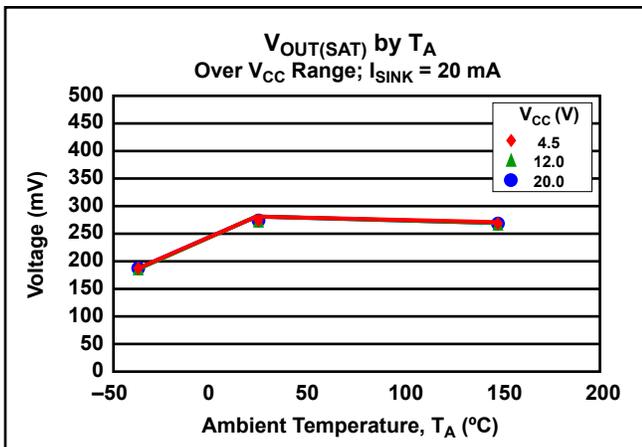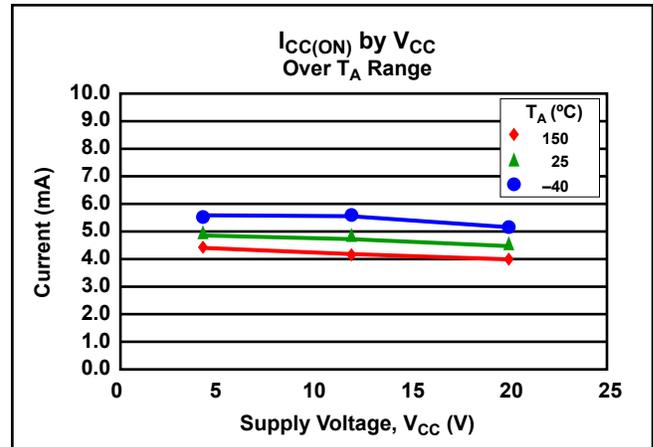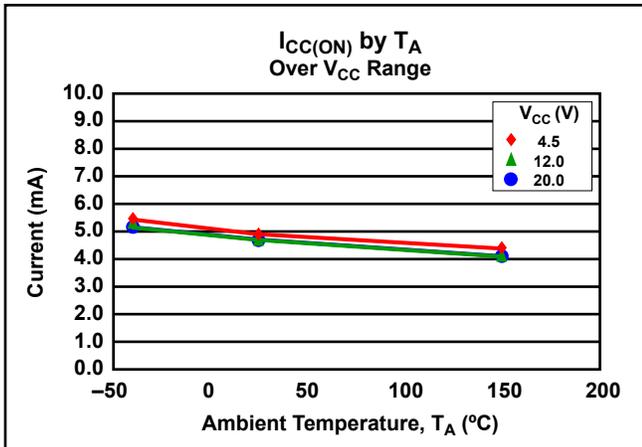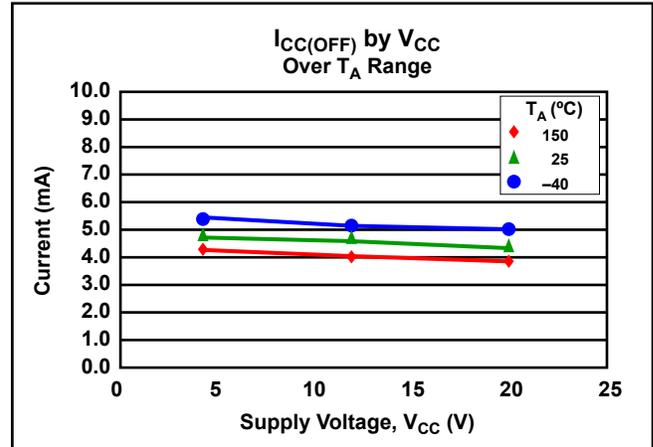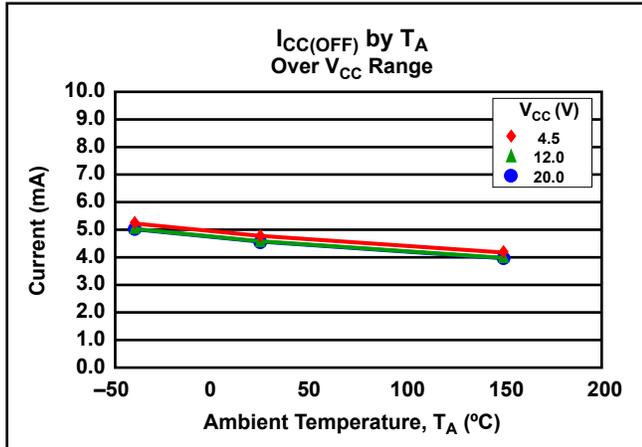
Settling Time, $t_{Settling}$ – The time required by the device, after t_{PO}, and after a valid magnetic signal has been applied, to provide proper output transitions. Settling time is a function of magnetic offset, offset polarity, signal phase, signal frequency, and signal amplitude.

Supply Current (on), $I_{CC(on)}$ – The current draw of the device with the output transitor is turned on.

Supply Current (off), $I_{CC(off)}$ – The current draw of the device with the output transitor is turned off.
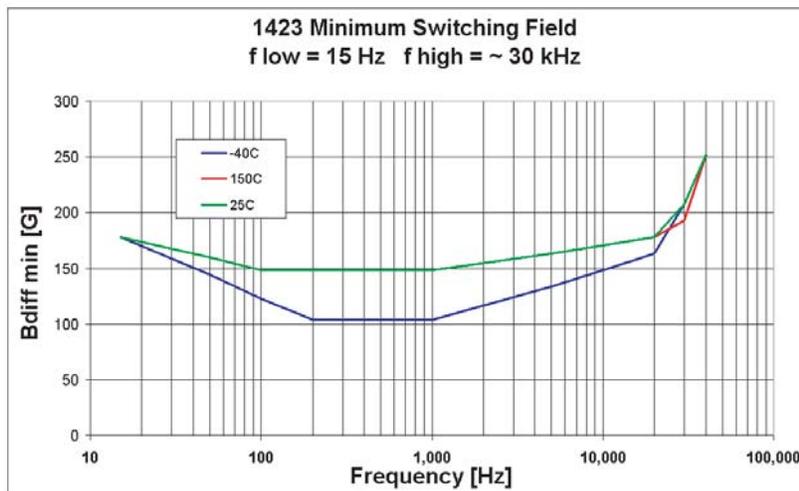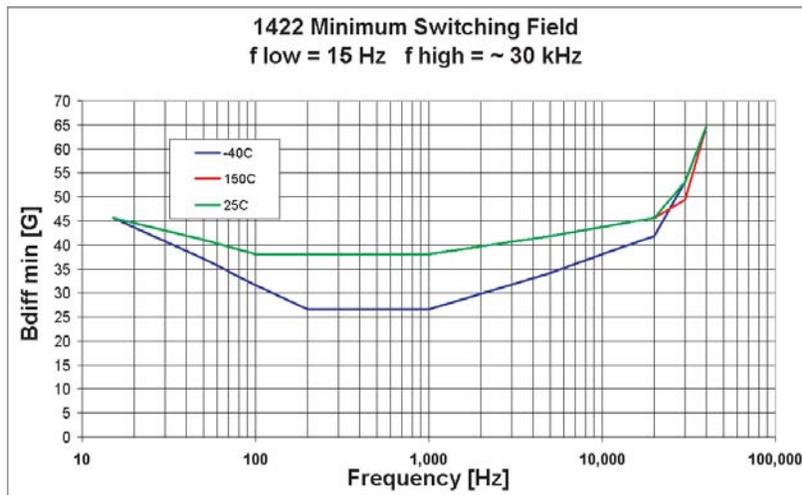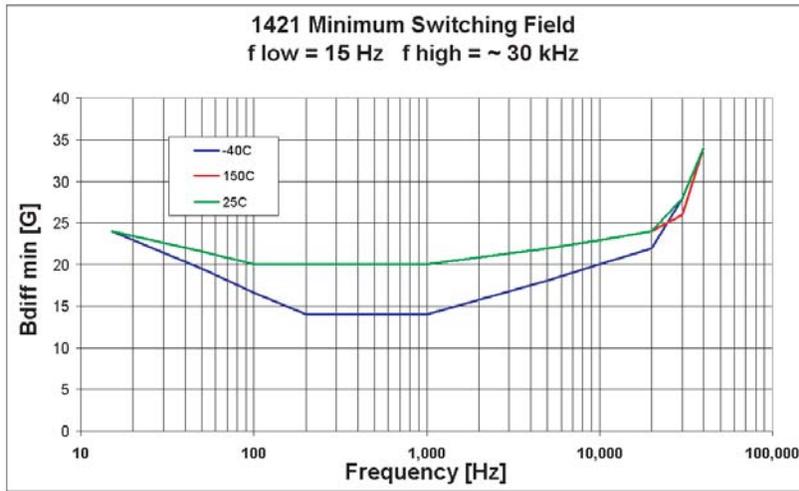
Response Time, $t_{Response}$ – The total time required for generating zero-crossing output transitions after initialization (the sum of Power-on Time and Settling Time).
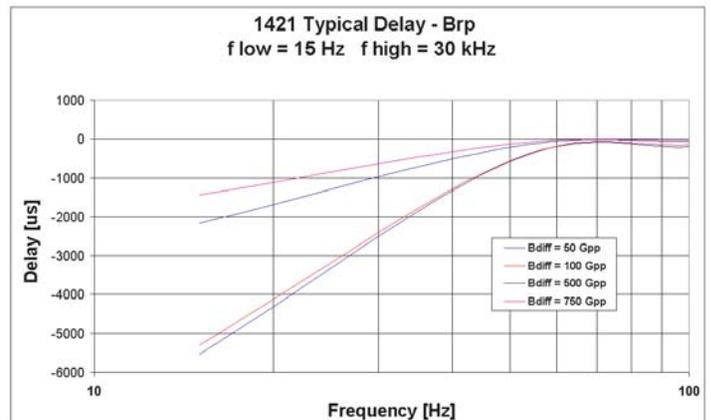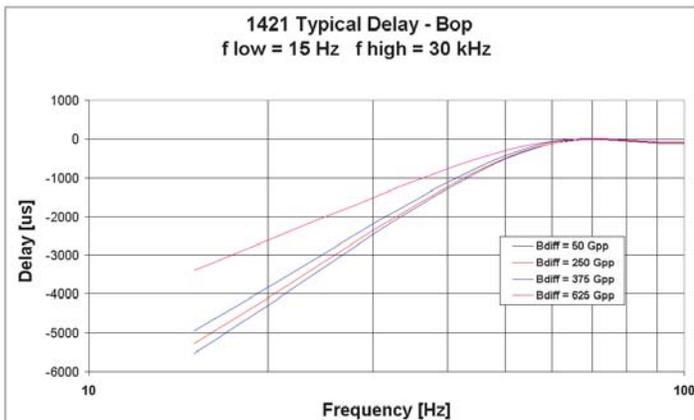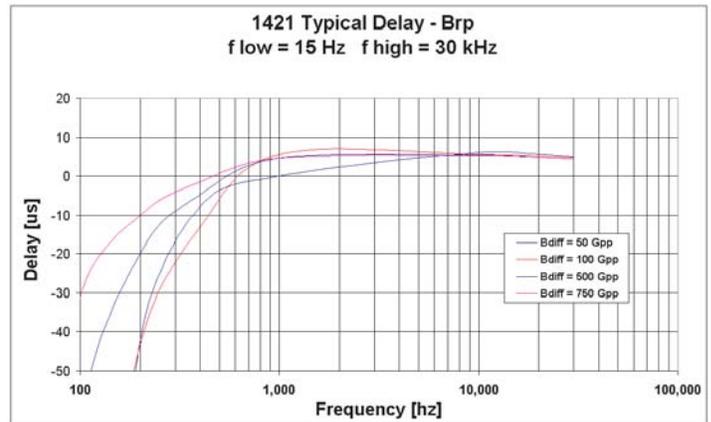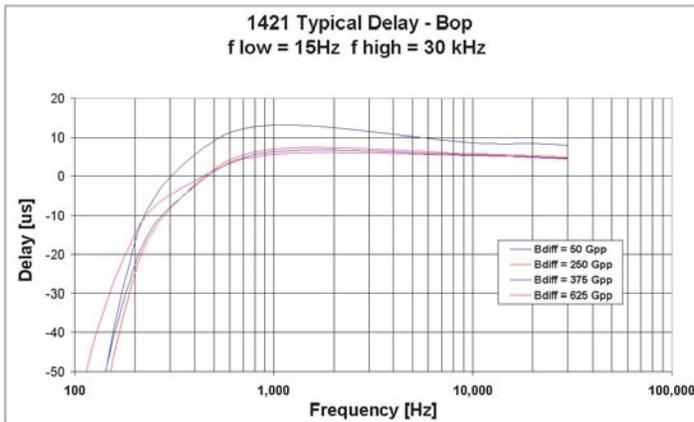
EMPIRICAL RESULTS
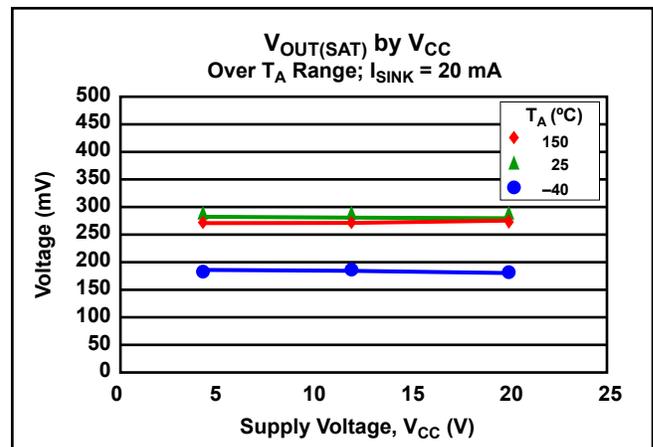
$I_{CC(OFF)}$ by T_A Over V_{CC} Range

$I_{CC(OFF)}$ by V_{CC} Over T_A Range

$I_{CC(ON)}$ by T_A Over V_{CC} Range

$I_{CC(ON)}$ by V_{CC} Over T_A Range

$V_{OUT(SAT)}$ by T_A Over V_{CC} Range; I_{SINK} = 20 mA

$V_{OUT(SAT)}$ by V_{CC} Over T_A Range; I_{SINK} = 20 mA

Continued on next page.

SIMULATION RESULTS

1421 Minimum Switching Field
f low = 15 Hz   f high = ~ 30 kHz

1422 Minimum Switching Field
f low = 15 Hz   f high = ~ 30 kHz

1423 Minimum Switching Field
f low = 15 Hz   f high = ~ 30 kHz

Continued on next page.

SIMULATION RESULTS, continued

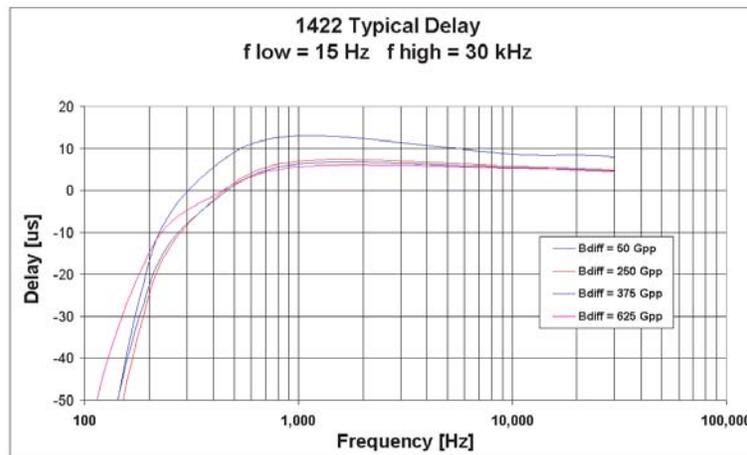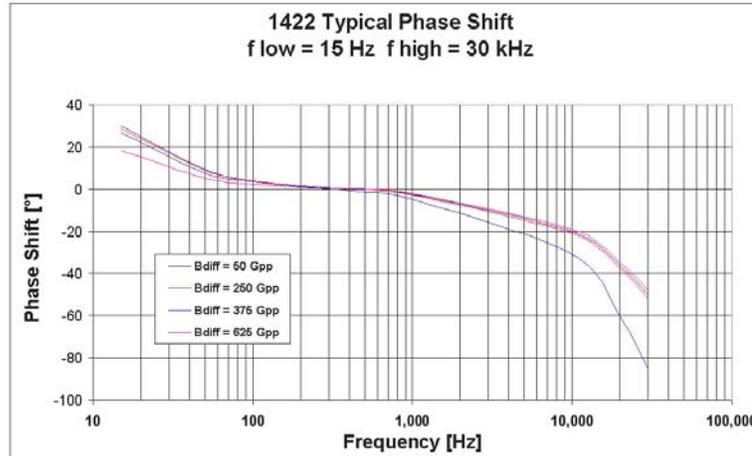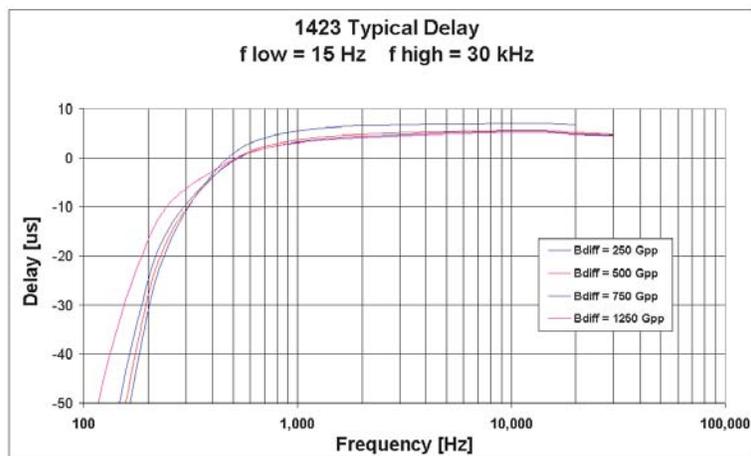*Continued on next page.*

SIMULATION RESULTS, continued
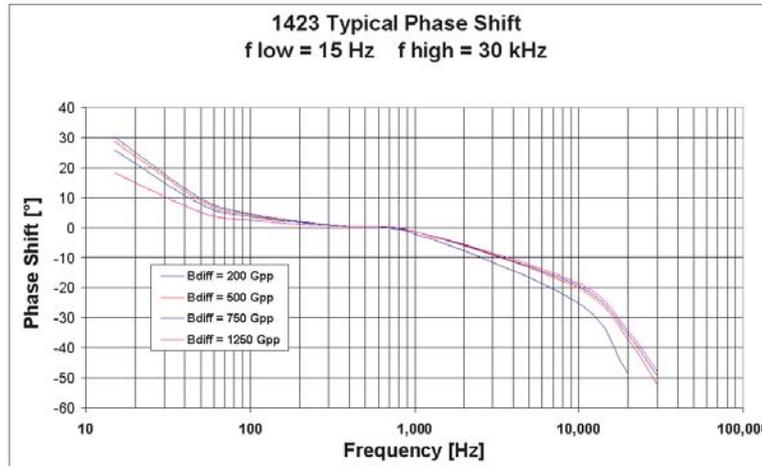

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SIMULATION RESULTS, continued

1423 Typical Phase Shift
f low = 15 Hz   f high = 30 kHz

1423 Typical Delay
f low = 15 Hz   f high = 30 kHz

1423 Typical Delay
f low = 15 Hz   f high = 30 kHz
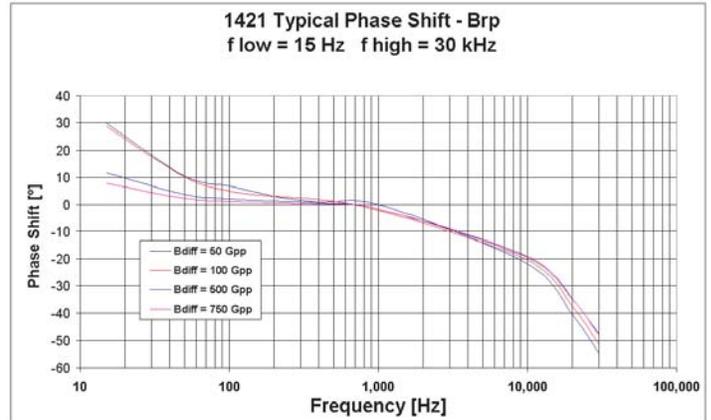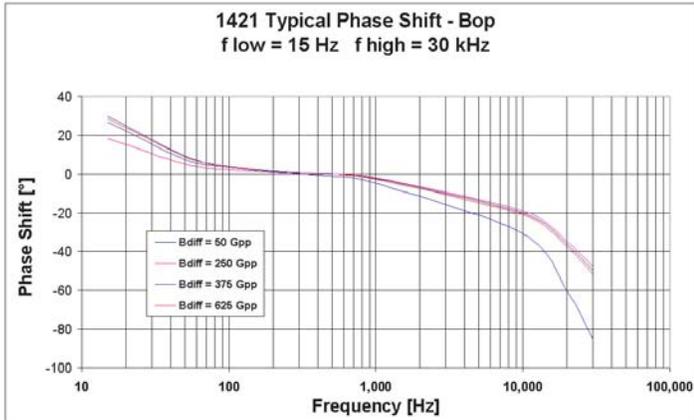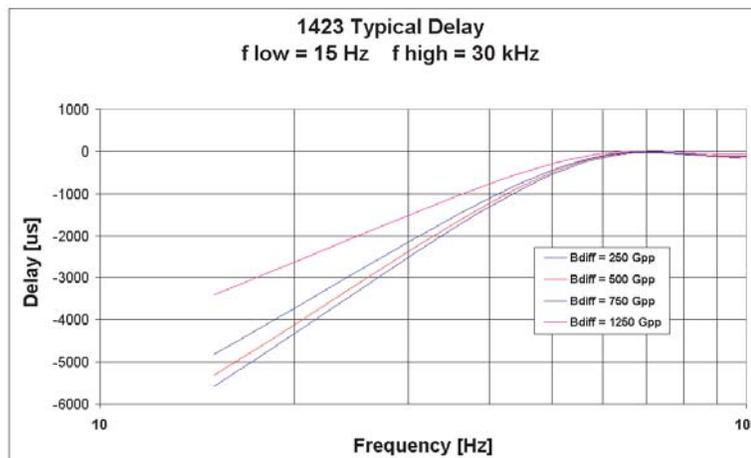
DEVICE EVALUATION: EMC CHARACTERIZATION

Please contact Allegro MicroSystems for EMC performance information.

Test Name	Reference Specification
ESD – Human Body Model[*]	AEC-Q100-002
ESD – Machine Model	AEC-Q100-003
Conducted Transients	ISO 7637-1
Direct RF Injection	ISO 11452-7
Bulk Current Injection	ISO 11452-4
TEM Cell	ISO 11452-3

[*]ESD test is done with no external components.
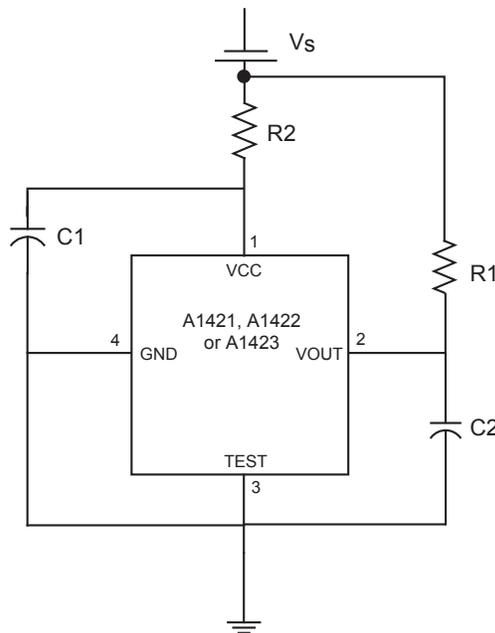
Component	Value	Units
R1[*]	1	kΩ
R2	100	Ω
C1	0.1	μF
C2	0.1	ηF

[*]Pull-up resistor not required for protection but for normal operation.

Recommended EMC test circuit. Test circuit recommended configuration may change after evaluation of first silicon.

APPLICATIONS INFORMATION

The A1421, A1422, and A1423 are versatile high-precision differential sensor ICs that can be used in a wide range of applications. Proper choice of the target material and shape, magnet material and shape, and assembly techniques enables large working air gaps and high switch point accuracy over the device operating temperature range.

Device Operation

The device IC contains two integrated Hall transducers that are used to differentially respond to a magnetic field across the surface of the IC. Referring to figure 1, which shows curves for the A1421 as an example, the trigger switches the output when the differential magnetic field crosses the B_{OP} level while increasing in strength (referred to as the positive direction). In the example, the A1421 output voltage switches high (off), and switches the output low (on) when the differential magnetic field crosses B_{RP} while decreasing (the negative direction).

The operation is achieved through the use of two separate comparators. One comparator has a positive hysteresis, B_{HYS1}, and the other a negative hysteresis, B_{HYS2}. Therefore, one comparator switches at the B_{OP} crossing on an increasing differential signal and the other switches at the B_{RP} crossing on a decreasing differential signal. The hysteresis on each comparator precludes false switching on noise or target jitter.

The behavior is similar for the A1422 and the A1423. The switch points are as shown in the magnetic charactersitics table, and the output polarity is inverted. This is illustrated in figure 2, on the next page.

Start-up

During power-on time, t_{PO}, the output signal, V_{OUT}, is high. Beyond this time, if the applied magnetic field, B_{diff}, is smaller than B_{HYS}, the switching state and V_{OUT} polarity are indeterminate. V_{OUT} will be valid for $B_{diff} > B_{HYS}$, after the additional settling time, $t_{Settling}$, has also elapsed.

Delay

The bandpass filter induces delay in the output signal, V_{OUT}, relative to the applied magnetic field, B_{diff}. Simulation data shown
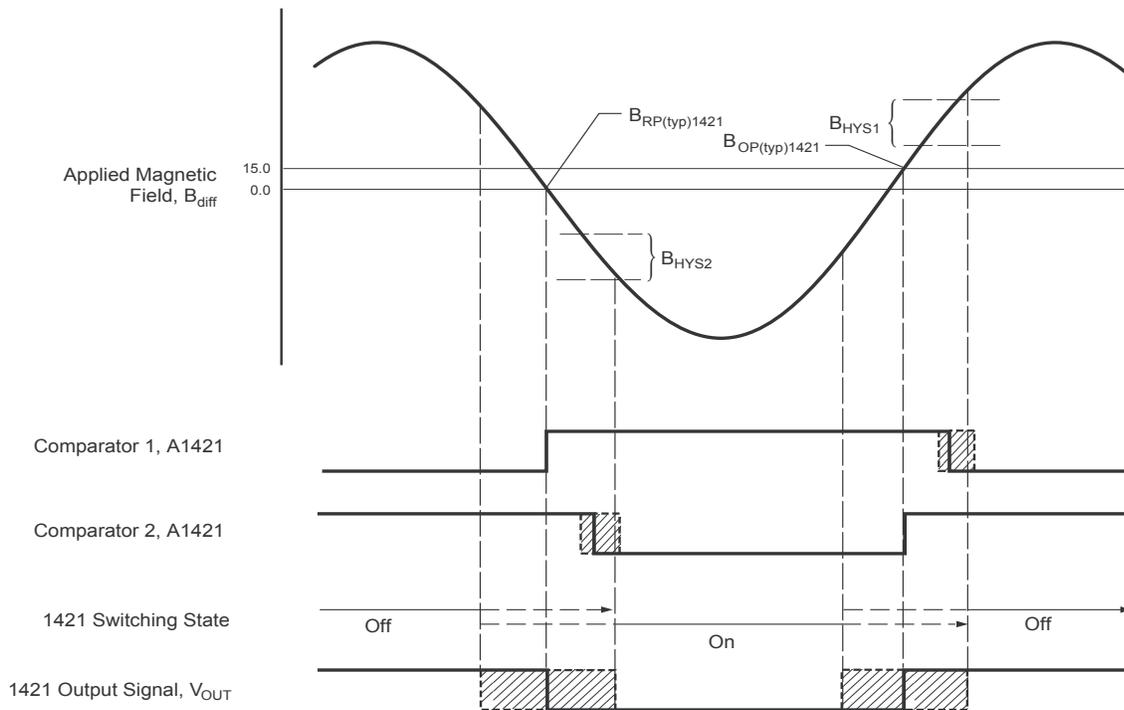

Figure 1. Typical output characteristics with dual comparator operation. The example shown is for the A1421. Characteristics shown without delay, see characteristic data charts for delay and phase shift contributions.

in the Characteristic Data section quantify the effect of the input signal amplitude on the phase shift of the output. Positive values of delay indicate a lagging output, while negative values indicate a leading output.

AC-Coupled Operation

Steady-state magnet and system offsets are eliminated using an on-chip differential bandpass filter. The upper and lower cut-off frequencies of this patented filter are set using an internal integrated capacitor. The differential structure of this filter improves the ability of the IC to reject single-ended noise on the GND or VCC lines and, as a result, makes the device more resistant to EMI (electromagnetic interference) typically seen in hostile remote-sensing environments.

Power Supply Protection

The device contains an on-chip voltage regulator and can operate over a wide supply voltage range. In applications that operate the device from an unregulated power supply, transient protection must be added externally. For applications using a regulated line, EMI/RFI protection may still be required. The circuit shown in figure 3 is the most basic configuration required for proper device operation.

Typical Circuit

A pull-up resistor, R_{PU}, is required between the supply and output terminals, as shown in figure 3. Also, the auxilliary terminal, TEST, must be connected externally to the GND terminal.
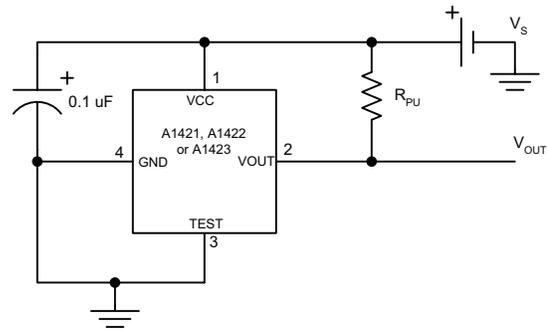

Figure 3. Basic application circuit. A pull-up resistor, R_{PU}, is required with the output driver.
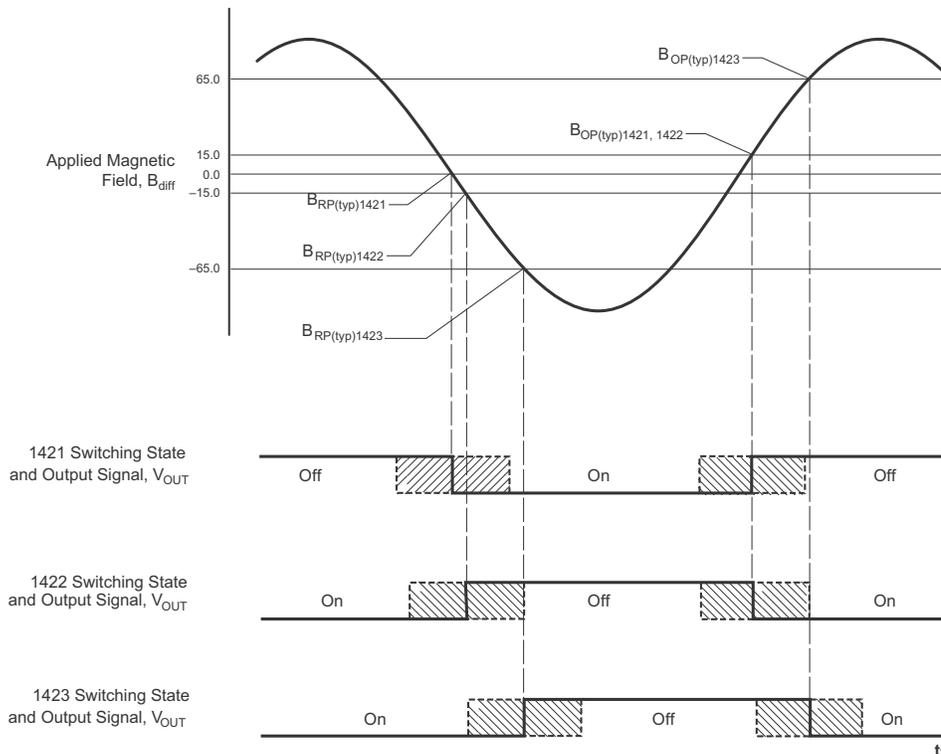

Figure 2. Comparative typical output characteristics. This chart illustrates the switch points and the output polarities of the A1421, A1422, and the A 1423. Characteristics shown without delay, see characteristic data charts for delay and phase shift contributions.

Power Derating

The device must be operated below the maximum junction temperature of the device, $T_{J(max)}$. Under certain combinations of peak conditions, reliable operation may require derating supplied power or improving the heat dissipation properties of the application. This section presents a procedure for correlating factors affecting operating T_J. (Thermal data is also available on the Allegro MicroSystems Web site.)

The Package Thermal Resistance, $R_{\theta JA}$, is a figure of merit summarizing the ability of the application and the device to dissipate heat from the junction (die), through all paths to the ambient air. Its primary component is the Effective Thermal Conductivity, K, of the printed circuit board, including adjacent devices and traces. Radiation from the die through the device case, $R_{\theta JC}$, is relatively small component of $R_{\theta JA}$. Ambient air temperature, T_A, and air motion are significant external factors, damped by overmolding.

The effect of varying power levels (Power Dissipation, P_D), can be estimated. The following formulas represent the fundamental relationships used to estimate T_J, at P_D.

$$P_D = V_{IN} \times I_{IN}$$
$$(1)$$
$$\Delta T = P_D \times R_{\theta JA}$$
$$(2)$$
$$T_J = T_A + \Delta T$$
$$(3)$$

For example, given common conditions such as: $T_A = 25°C$, $V_{CC} = 12$ V, $I_{CC} = 4.2$ mA, and $R_{\theta JA} = 177$ °C/W, then:

$$P_D = V_{CC} \times I_{CC} = 12\ V \times 4.2\ mA = 50\ mW$$
$$\Delta T = P_D \times R_{\theta JA} = 50\ mW \times 177\ °C/W = 9°C$$
$$T_J = T_A + \Delta T = 25°C + 9°C = 34°C$$

A worst-case estimate, $P_{D(max)}$, represents the maximum allowable power level ($V_{CC(max)}$, $I_{CC(max)}$), without exceeding $T_{J(max)}$, at a selected $R_{\theta JA}$ and T_A.

Example

Reliability for V_{CC} at $T_A = 150°C$, package L-I1, using minimum-K PCB

Observe the worst-case ratings for the device, specifically: $R_{\theta JA} = 177°C/W$, $T_{J(max)} = 165°C$, $V_{CC(max)} = 26.5$ V, and $I_{CC(max)} = 7.0$ mA.

Calculate the maximum allowable power level, $P_{D(max)}$. First, invert equation 3:

$$\Delta T_{max} = T_{J(max)} - T_A = 165°C - 150°C = 15°C$$

This provides the allowable increase to T_J resulting from internal power dissipation. Then, invert equation 2:

$$P_{D(max)} = \Delta T_{max} \div R_{\theta JA} = 15°C \div 177\ °C/W = 91\ mW$$

Finally, invert equation 1 with respect to voltage:

$$V_{CC(est)} = P_{D(max)} \div I_{CC(max)} = 91\ mW \div 7.0\ mA = 13\ V$$

The result indicates that, at T_A, the application and device can dissipate adequate amounts of heat at voltages $\leq V_{CC(est)}$.

Compare $V_{CC(est)}$ to $V_{CC(max)}$. If $V_{CC(est)} \leq V_{CC(max)}$, then reliable operation between $V_{CC(est)}$ and $V_{CC(max)}$ requires enhanced $R_{\theta JA}$. If $V_{CC(est)} \geq V_{CC(max)}$, then operation between $V_{CC(est)}$ and $V_{CC(max)}$ is reliable under these conditions.
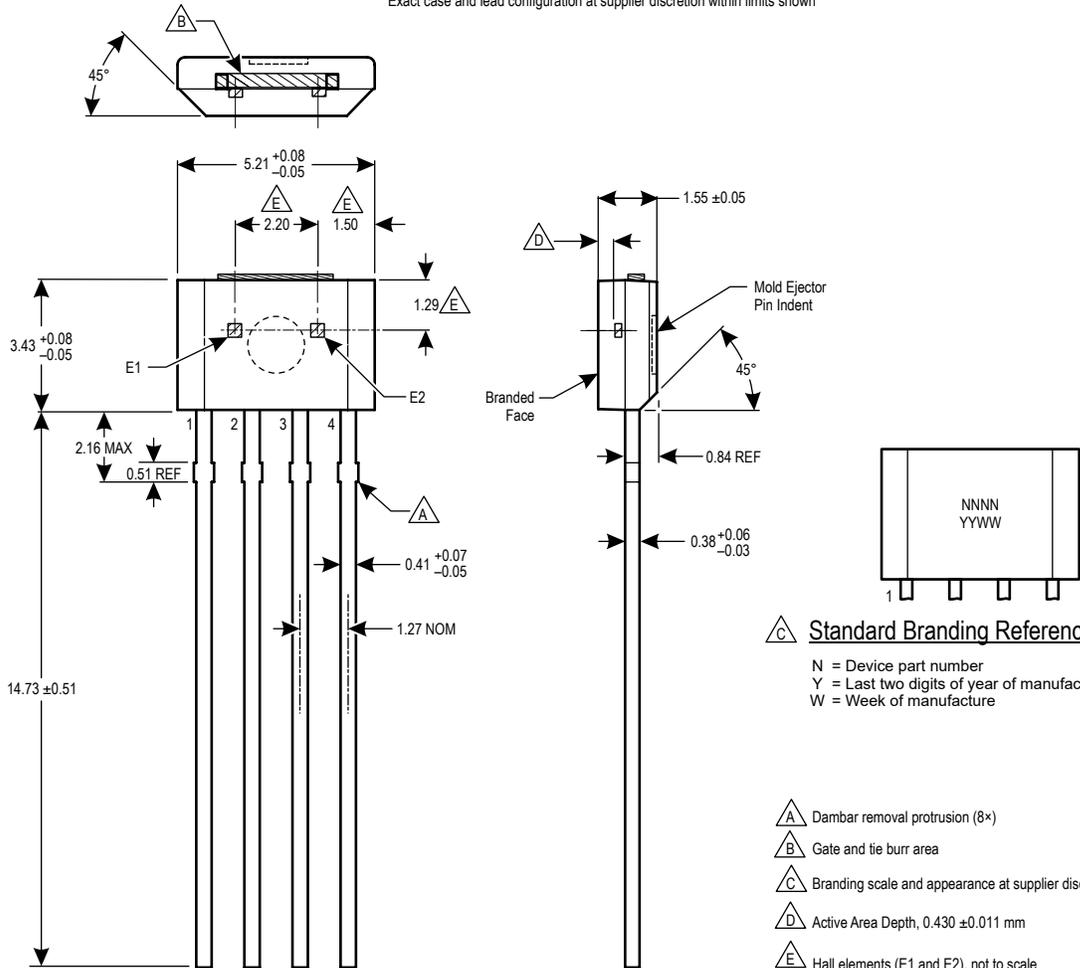
PACKAGE K, 4-PIN SIP

For Reference Only - Not for Tooling Use
(Reference DWG-0000395)
Dimensions in millimeters - NOT TO SCALE
Dimensions exclusive of mold flash, gate burs, and dambar protrusions
Exact case and lead configuration at supplier discretion within limits shown

B
45°

5.21 +0.08 −0.05

E 2.20

E 1.50

1.55 ±0.05

D

1.29 E

3.43 +0.08 −0.05

E1

E2

Mold Ejector Pin Indent

45°

Branded Face

1 2 3 4

2.16 MAX

0.51 REF

A

0.84 REF

0.41 +0.07 −0.05

0.38 +0.06 −0.03

1.27 NOM

14.73 ±0.51

NNNN
YYWW

1

C Standard Branding Reference View

N = Device part number
Y = Last two digits of year of manufacture
W = Week of manufacture

A Dambar removal protrusion (8×)

B Gate and tie burr area

C Branding scale and appearance at supplier discretion

D Active Area Depth, 0.430 ±0.011 mm

E Hall elements (E1 and E2), not to scale

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Revision Table

Number	Date	Description
7	July 2, 2018	Updated A1421LK-T status to discontinued; minor editorial updates
8	July 3, 2019	Minor editorial updates
9	July 13, 2022	Updated package drawing (page 15)

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