M.2 to MiniPCIe Adapter:
SKU: OP3008250

The adapter converts a 3042 B-Key M.2 module into a Mini Card form factor with MiniPCIe interface.

The adapter includes a MicroSD slot for extra memory. The MicroSD is visible as a USB device on the PCIx interface. There is a USB hub on the interposer card that connects to two USB devices: the modem and the MicroSD card.

Revision 2

Image: Front side and back side of the M.2 to MiniPCIe adapter

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Pinouts:

PCIe Minicard Edge connector

Pin nr	Signal	Connects to
1	WAKE#	WAKE#
3	COEX1	+3.3V uSD card
5	COEX2	
7	CLKREQ#	
9	GND	GND
11	REFCLK-	
13	REFCLK+	
15	GND	GND
17	UIM_IC_DM	STOOK_EN
19	UIM_IC_DP	+3.3V uSD card
21	GND	GND
23	PERn0	
25	PERp0	
27	GND	GND
29	GND	GND
31	PETn0	
33	PETp0	
35	GND	GND
37	GND	GND
39	+3.3Vaux	+3.3V module
41	-3.3Vaux	+3.3V module
43	GND	GND
45	Reserved	
47	47 Reserved	
49	Reserved	
51	Reserved	

Pin nr	Signal	Connects to
2	+3.3V	+3.3V module
4	GND	GND
6	+1.5V	
8	UIM_PWR	UIM_PWR
10	UIM_DATA	UIM_DATA
12	UIM_CLK	UIM_CLK
14	UIM_RST	UIM_RST
16	UIM_VPP	
18	GND	GND
20	W_DISABLE#	W_DISABLE
22	PERST#	PERST#
24	+3.3Vaux	+3.3V module
26	GND	GND
28	+1.5V	
30	SMB_CLK	
32	SMB_DATA	
34	GND	GND
36	USB_D-	USBDU_DM
38	USB_D+	USBDU_DP
40	GND	GND
42	LED_WWAN#	LED_WWAN#
44	LED_WLAN#	
46	LED_WPAN#	
48	+1.5V	
50	GND	GND
52	+3.3Vaux	+3.3V module

	Signal	Function
Power	GND	Ground
	+3.3Vaux	3.3V source
	+1.5V	1.5V source
User Identity Module Signals (SIM Card)	UIM_PWR	SIM card power
	UIM_DATA	SIM card data
	UIM_CLK	SIM card clock
	UIM_RST	SIM card reset
PCI Express	REFCLK- REFCLK+	PCI Express differential reference clock
	PERn0/PERp0	PCI Express receive signals
	PETn0/PETp0	PCI Express transmit signals
Auxiliary Signals	CLKREQ#	Clock request
	PERST#	Reset for the Mini Card
	W_DISABLE#	Wireless disable
	WAKE#	Wake signal
USB 2.0	USB_D-	USB line negative
	USB_D+	USB line positive

Ordering Information:

- PN OP3008250
- Custom versions available

M2 Edge connector

Pin nr	Signal	Connects to
75	CONFIG_2	
73	GND	GND
71	GND	GND
69	CONFIG_1	
67	RST#1	PERST#
65	ANTCTL3	
63	ANTCTL2	
61	ANTCTL2	
59	ANTCTL0	
57	GND	GND
55	REFCLK+	
53	REFCLK-	
51	GND	GND
49	PERP0	
47	PERN0	
45	GND	GND
43	PETP0	
41	PETN0	
39	GND	GND
37	SSRX+	
35	SSRX-	
33	GND	GND
31	SSTX+	
29	SSTX-	
27	GND	GND
25	GPIO12	
23	GPIO11	WAKE#
21	CONFIG_0	
11	GND	GND
9	USB_D-	USBDN_DM
7	USB_D+	USBDN_DP
5	GND	GND
3	GND	GND
1	CONFIG_3	

Pin nr	Signal	Connects to
74	+3.3V	+3.3V module
72	+3.3V	+3.3V module
70	+3.3V	+3.3V module
68	SUSCLK (32KHz)	
66	SIM_DETECT	
64	COEX_1	
62	COEX_2	
60	COEX_3	
58		
56		
54		
52		
50		
48	GPIO_4	
46	GPIO_3	
44	GPIO_2	
42	GPIO_1	
40	GPIO_0	
38	DEVSLP	
36	UIM_PWR	UIM_PWR
34	UIM_DATA	UIM_DATA
32	UIM_CLK	UIM_CLK
30	UIM_RST	UIM_RST
28	GPIO_8	
26	W_DISABLE2#	
24	GPIO_7	
22	GPIO_6	
20	GPIO_5	
10	GPIO_9	LED_WWAN#
8	W_DISABLE1#	W_DISABLE
6	CARD_PWR_OFF	
4	+3.3V	+3.3V module
2	+3.3V	+3.3V module

	Signal	Function
Power	GND	Ground
	+3.3V	3.3V source
User Identity Module Signals (SIM Card)	UIM_PWR	SIM card power
	UIM_DATA	SIM card data
	UIM_CLK	SIM card clock
	UIM_RST	SIM card reset
Communication-specific signals	W_DISABLE#	Wireless disable
	WoWWAN#	Wake-On-WWAN
Supplemental communication-specific signals	CARD_PWR_OFF	Turn off power
	RST#	Reset
	GPIO[0:11]	General Purpose I/O
USB 2.0	USB_D-	USB line negative
	USB_D+	USB line positive
USB 3.0	SSTX+	SuperSpeed serial data transmit positive
	SSTX-	SuperSpeed serial data transmit negative
	SSRX+	SuperSpeed serial data receive positive
	SSRX-	SuperSpeed serial data receive negative
Module Configuration Pins	CONFIG[0:3]	Module configuration
SATA Interface signals	DEVSLP	Device Sleep