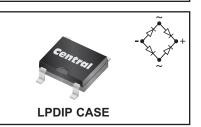
CBRLDSH1-40

SURFACE MOUNT LOW PROFILE 1 AMP SILICON SCHOTTKY BRIDGE RECTIFIER





www.centralsemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CBRLDSH1-40 is a full wave Schottky bridge rectifier mounted in a low profile epoxy surface mount molded case, utilizing glass passivated chips.

MARKING CODE: CLP1

APPLICATIONS:

- Solid state lighting (SSL)
- DC-DC converters
- Polarity protection

FEATURES:

- Low leakage current (16µA TYP @ V_{RRM})
- Low profile case (1.45mm MAX)
- Low V_F Schottky diodes (450mV TYP @ I_F=1.0A)

MAXIMUM RATINGS: (T _A =25°C unless otherwise noted)	SYMBOL		UNITS
Peak Repetitive Reverse Voltage	V_{RRM}	40	V
DC Blocking Voltage	V_{R}	40	V
RMS Reverse Voltage	V _{R(RMS)}	28	V
Average Forward Current	IO	1.0	Α
Peak Forward Surge Current (tp=8.3ms)	IFSM	30	Α
Operating & Storage Junction Temperature	T _J , T _{stg}	-55 to +150	°C
Thermal Resistance	ΘιΔ	95	°C/W

ELECTRICAL CHARACTERISTICS PER DIODE: (T_A=25°C unless otherwise noted)

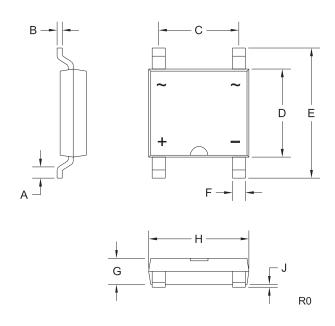
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SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I_R	V _R =40V		16	100	μΑ
BV_R	I _R =0.1mA	40	50		V
V_{F}	I _F =1.0A		450	500	mV
C _{.1}	V _R =4.0V, f=1.0MHz		110		pF

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LPDIP CASE - MECHANICAL OUTLINE



DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
Α	0.019	0.028	0.50	0.70			
В	0.007	0.012	0.20	0.30			
С	0.153	0.162	3.90	4.10			
D	0.167	0.179	4.25	4.55			
E	0.248	0.264	6.30	6.70			
F	0.023	0.028	0.60	0.70			
G	0.045	0.058	1.15	1.45			
Н	0.190	0.203	4.85	5.15			
J	0.002	0.006	0.05	0.15			
		LPDIP (REV: R0)					

MARKING CODE: CLP1

R1 (17-January 2012)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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