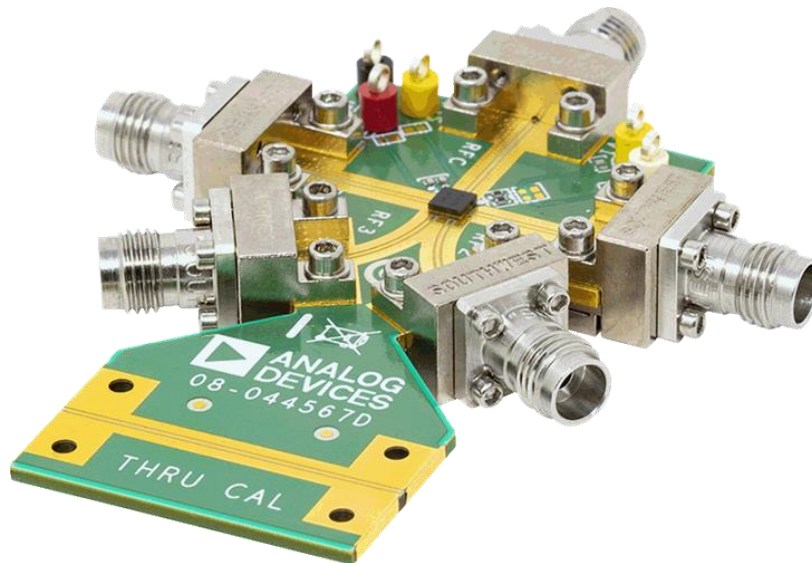


EVAL-ADRF5047



Overview

Product Details

ADRF5047-EVALZ is a fully populated, connectorized evaluation board using 4-layer, 62 mil thick, RO4003 PCB material.

For normal operation, two supply voltages +3.3V and -3.3V should be applied to the VDD and VSS test points respectively, while their ground voltages are connected to the GND test point. Two control input voltages, 0/+3.3V, should be applied to V1 and V2 test points to select the switch states; RFC to RF1, RF2, RF3 or RF4.

The RF input signal can be applied at the edge-launch, 2.4 mm RF connector labeled RFC and the RF output signals can be measured at the edge-launch, 2.4 mm connectors labeled RF1, RF2, RF3 and RF4.

Evaluation Documentation

ADRF5046/ADRF5047 Gerber Files

https://www.analog.com/media/en/technical-documentation/evaluation-documentation/gerber-files/ADRF5046_ADRF5047_Gerber.zip

