

## **Features**

- High Density Cell Design for Ultra Low R<sub>DS(ON)</sub>
- · Fully Characterized Avalanche Voltage and Current
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Halogen Free Available Upon Request By Adding Suffix "-HF"
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

# **P-Channel MOSFET**

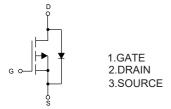
# **Maximum Ratings**

- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 100°C/W Junction to Ambient

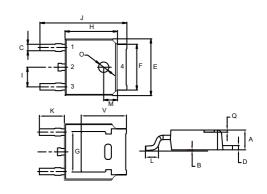
Parameter	Symbol	Rating	Unit
Drain -Source Voltage	V <sub>DS</sub>	-40	V
Gate -Source Volltage	V <sub>GS</sub>	±20	V
Drain Current-Continuous	I <sub>D</sub>	-40	А
Drain Current-Pulsed	I <sub>DM</sub>	-160	Α
Single Pulsed Avalanche Energy <sup>(Note1)</sup>	E <sub>AS</sub>	544	mJ
Power Dissipation	P <sub>D</sub>	1.25	W

Note: 1.E<sub>AS</sub> condition:  $V_{DD}$ =-20V,L=1mH,  $R_G$ =25 $\Omega$ , Starting  $T_J$  = 25 $^{\circ}$ C

## **Internal Structure**



# **DPAK**



DIMENSIONS					
DIM	INCHES		S MM		NOTE
DIIVI	MIN	MAX	MIN	MAX	NOTE
Α	0.087	0.094	2.20	2.40	
В	0.000	0.005	0.00	0.13	
С	0.026	0.034	0.66	0.86	
D	0.018	0.023	0.46	0.58	
Е	0.256	0.264	6.50	6.70	
F	0.201	0.215	5.10	5.46	
G	0.190		4.83		TYP.
Н	0.236	0.244	6.00	6.20	
I	0.086	0.094	2.18	2.39	
J	0.386	0.409	9.80	10.40	
K	0.114		2.90		TYP.
L	0.055	0.067	1.40	1.70	
М	0.063		1.60		TYP.
0	0.043	0.051	1.10	1.30	
Q	0.000	0.012	0.00	0.30	
V	0.211		5.3	35	TYP.



# **ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)**

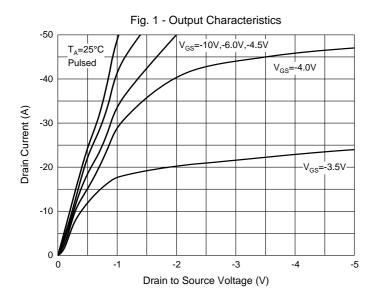
Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-40			V
Gate-Threshold Voltage <sup>(Note2)</sup>	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1.5	-1.85	-3.0	V
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =± 20V, V <sub>DS</sub> =0V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-40V, V <sub>GS</sub> =0V			-1	μA
Drain-Source On-Resistance <sup>(Note2)</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-12A		12	14	mΩ
Forward Transconductance <sup>(Note2)</sup>	g <sub>FS</sub>	V <sub>DS</sub> =-5V, I <sub>D</sub> =-12A	34			S
Dynamic Characteristics(Note3)						
Input Capacitance	C <sub>iss</sub>			2960		
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =-20V, $V_{GS}$ =0V, f=1MHz		370		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			310		l
Switching Characteristics(Note:	3)					
Total Gate Charge	Qg			72		
Gate-Source Charge	$Q_{gs}$	V <sub>DS</sub> =-20V,V <sub>GS</sub> =-10V,I <sub>D</sub> =-12A		14		Nc
Gate-Drain Charge	$Q_{gd}$			15		
Turn-on Delay Time	t <sub>d(on)</sub>			10		
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =-20V, $V_{GS}$ =-10V, $R_{G}$ =3 $\Omega$ ,		18		
Turn-off Delay Time	t <sub>d(off)</sub>	I <sub>D</sub> =-20A		38		ns
Turn-off Fall Time	t <sub>f</sub>			24		
Drain-Source Diode Characteristics	1		-			
Drain-Source Diode Forward Voltage <sup>(Note 2)</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =-20A			-1.2	V
Continuous Drain-Source Diode Forward Current (Note 4)	Is				-40	А
Pulsed Drain-Source Diode Forward Current	I <sub>SM</sub>				-160	А

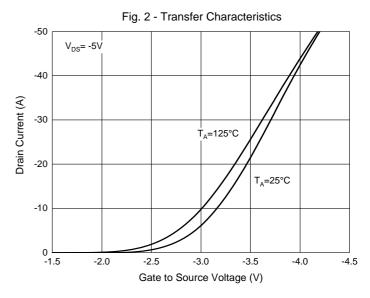
### Notes:

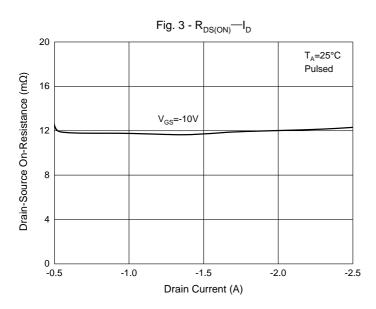
- 1.  $E_{AS}$  Condition:  $V_{DD}$ =-20V,L=1mH,  $R_{G}$ =25 $\Omega$ , Starting  $T_{J}$  = 25 $^{\circ}$ C
- 2. Pulse Test : Pulse Width≤300µs, Duty Cycle ≤2%.
- 3. Guaranteed by Design, Not Subject to Production.
- 4. Surface Mounted on FR4 Board, t≤10 sec.

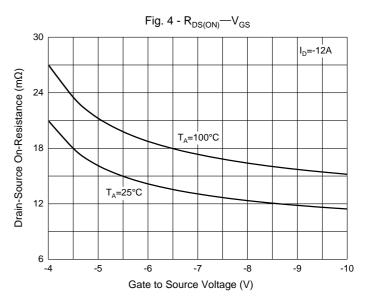


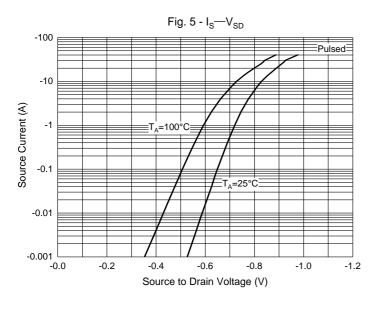
## **Curve Characteristics**

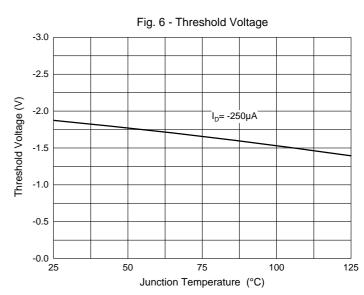














# **Ordering Information**

Device	Packing
Part Number-TP	Tape&Reel:2.5Kpcs/Reel

Note: Adding "-HF" Suffix For Halogen Free, eg. Part Number-TP-HF

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