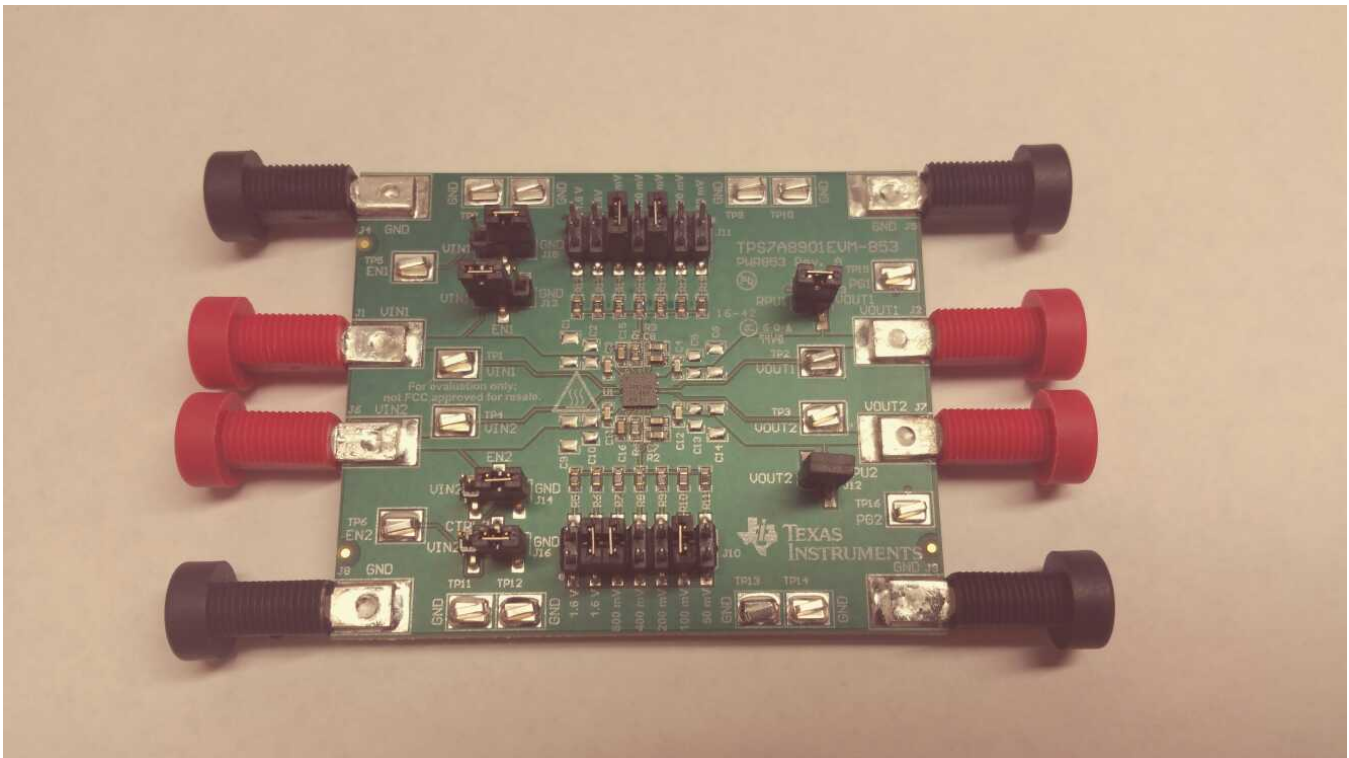


TPS7A89 Evaluation Module



This user's guide describes the operational use of the TPS7A8901EVM-853 evaluation module (EVM) as a reference design for engineering demonstration and evaluation of the TPS7A8901RTJ, dual low-noise, low-dropout voltage regulator (LDO). Included in this user's guide are setup and operating instructions, thermal and layout guidelines, a printed circuit board (PCB) layout, a schematic diagram, and a bill of materials (BOM).

Throughout this document, the terms demonstration kit, evaluation board, evaluation module are synonymous with the TPS7A8901EVM-853.

The following related documents are available through the Texas Instruments web site at <http://www.ti.com>.

Related Documentation

Device	Literature Number
TPS7A89	SBVS280

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Trademarks

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1 Introduction

The Texas Instruments TPS7A8901EVM-853 EVM helps design engineers evaluate the operation and performance of the TPS7A89 linear regulator for possible use in their own circuit application. This particular EVM configuration contains a dual low-noise, high-PSRR linear regulator, ideal for high-performance signal chain. The regulator is capable of delivering up to 2 A per channel to each load with low dropout ($V_{IN} - V_{OUT}$) voltage for high-efficiency operation. For stability, each channel requires a 10- μ F input capacitor and 10- μ F output capacitor for the TPS7A89.

1.1 Before You Begin

The following warnings and cautions are noted for the safety of anyone using or working close to the TPS7A8901EVM-853. Observe all safety precautions.

WARNING



Hot surface. Contact may cause burns. Do not touch.

CAUTION

The circuit module may be damaged by overtemperature. To avoid damage, monitor the temperature during evaluation and provide cooling, as needed, for your system environment.

CAUTION

Some power supplies can be damaged by application of external voltages. If using more than one power supply, check your equipment requirements and use blocking diodes or other isolation techniques, as needed, to prevent damage to your equipment.

CAUTION

The circuit module is not a finished product or electrical appliance. The module does not contain current or voltage thresholds for circuit protection. It must be used by qualified personnel with additional equipment for evaluation only.

2 EVM Setup

This section describes how to properly connect and setup the TPS7A8901EVM-853, including the jumpers and connectors on the EVM board.

2.1 Input/Output Connectors and Jumper Descriptions

2.1.1 J1 – VIN1

Channel-1, input power-supply voltage connector. Twist together the positive input lead and ground return lead from the input power supply; keep the leads as short as possible to minimize input wire inductance. Add additional bulk capacitance between J1 and J4 (use the C1 footprint) if the supply leads are longer than six inches. For example, an additional 47- μ F electrolytic capacitor connected from J1 to ground improves the transient response of the TPS7A89, while eliminating unwanted ringing on the input because of long wire connections.

2.1.2 J2 – VOUT1

Channel-1, regulated, output voltage connector.

2.1.3 J3 – VPU1

Channel-1, pullup-voltage selector for PG1. This EVM is designed so that PG1 can be pulled up to either VOUT1 by shorting J3, or pulled up to another voltage by applying an external voltage to the RPU1 post.

2.1.4 J4 – GND

Return connector for the channel-1, input power supply.

2.1.5 J5 – GND

Channel-1, output, ground return connector.

2.1.6 J6 – VIN2

Channel-2, input, power-supply voltage connector. Twist together the positive input lead and ground return lead from the input power supply; keep the leads as short as possible to minimize input wire inductance. Add additional bulk capacitance between J6 and J8 (use the C9 footprint) if the supply leads are longer than six inches. For example, an additional 47- μ F electrolytic capacitor connected from J6 to ground improves the transient response of the TPS7A89, while eliminating unwanted ringing on the input because of long wire connections.

2.1.7 J7 – VOUT2

Channel-2, regulated, output voltage connector.

2.1.8 J8 – GND

Return connector for the channel-2, input power supply.

2.1.9 J9 – GND

Channel-2, output, ground return connector.

2.1.10 J10 – ANYOUT2

The channel-2 output voltage of the TPS7A89 is selectable in accordance with the names given to the output voltage setting pins: 50 mV, 100 mV, 200 mV, 400 mV, 800 mV, 1.6 V, and 1.6 V. For each pin connected to the ground, the output voltage setting increases by the value associated with that pin name, starting from the value of the reference voltage of 0.8 V; floating the pins has no effect on the output voltage.

2.1.11 J11 – ANYOUT1

The channel-1 output voltage of the TPS7A89 is selectable in accordance with the names given to the output voltage setting pins: 50 mV, 100 mV, 200 mV, 400 mV, 800 mV, 1.6 V, and 1.6 V. For each pin connected to the ground, the output voltage setting increases by the value associated with that pin name, starting from the value of the reference voltage of 0.8 V; floating the pins has no effect on the output voltage.

2.1.12 J12 – VPU2

Channel-2 pullup voltage selector for PG2. This EVM is designed so that PG2 can be pulled up to either VOUT1 by shorting J12, or pulled up to another voltage by applying an external voltage to the RPU2 post.

2.1.13 J13 – EN1

Channel-1 output enable. To enable the output for channel 1, connect a jumper to short VIN1 to EN1. To disable the output, connect a jumper to short EN1 to GND.

2.1.14 J14 – EN2

Channel-2 output enable. To enable the output for channel 2, connect a jumper to short VIN2 to EN2. To disable the output, connect a jumper to short EN2 to GND.

2.1.15 J15 – CTRL1

Channel-1, soft-start control. Connect a jumper to short VIN1 to CTRL1 for a fast charge time. Connect a jumper to short CTRL1 to GND for a slower startup.

2.1.16 J16 – CTRL2

Channel-2, soft-start control. Connect a jumper to short VIN2 to CTRL2 for a fast charge time. Connect a jumper to short CTRL2 to GND for a slower startup.

2.1.17 TP1 – VIN1

Channel-1, input, sense test point.

2.1.18 TP2 – VOUT1

Channel-1, output, sense test point.

2.1.19 TP3 – VOUT2

Channel-2, output, sense test point.

2.1.20 TP4 – VIN2

Channel-2, input, sense test point.

2.1.21 TP5 – EN1

Channel-1, enable, sense test point.

2.1.22 TP6 – EN2

Channel-2, enable, sense test point.

2.1.23 TP7 – GND

Ground.

2.1.24 TP8 – GND

Ground.

2.1.25 TP9 – GND

Ground.

2.1.26 TP10 – GND

Ground.

2.1.27 TP11 – GND

Ground.

2.1.28 TP12 – GND

Ground.

2.1.29 TP13 – GND

Ground.

2.1.30 TP14 – GND

Ground.

2.2 Soldering Guidelines

To avoid damaging the integrated circuit (IC), use a hot-air system for any solder rework to modify the EVM for the purpose of repair or other application reasons.

2.3 Equipment Connection

1. Set the input power supplies for each channel to 6.5 V (max), and turn the power supplies off.
2. Connect the positive voltage lead from input power supply 1 to VIN1, at the J1 connector of the EVM.
3. Connect the ground lead from input power supply 1 to GND at the J4 connector of the EVM.
4. Connect the positive voltage lead from input power supply 2 to VIN2, at the J6 connector of the EVM.
5. Connect the ground lead from input power supply 2 to GND at the J7 connector of the EVM.
6. Connect a 0-A to 2-A load between OUT1 and GND.
7. Connect a 0-A to 2-A load between OUT2 and GND.
8. Disable the output for channel 1 by shorting EN1 to GND on J13.
9. Disable the output for channel 2 by shorting EN2 to GND on J14.

3 Operation

1. Turn on the power supplies.
2. Enable the channel 1 output by shorting the EN1 pin to VIN1 on J13.
3. Enable the channel 2 output by shorting the EN2 pin to VIN2 on J14.
4. Vary the respective load and input voltage, as necessary, for test purposes.

4 PCB Layout

Figure 1 to Figure 5 illustrate the PCB layout for this EVM.

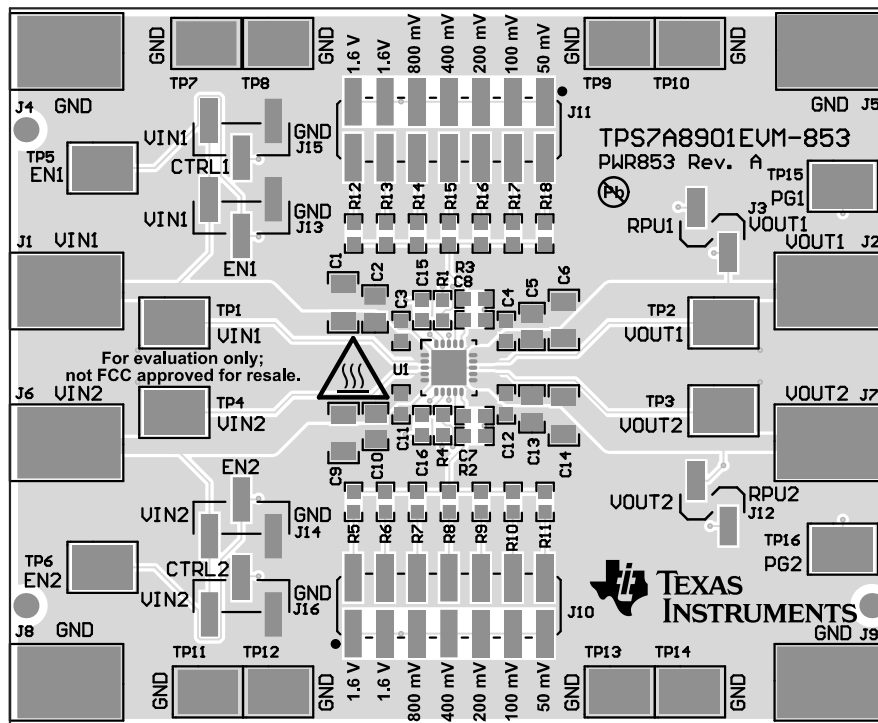


Figure 1. Assembly Layer

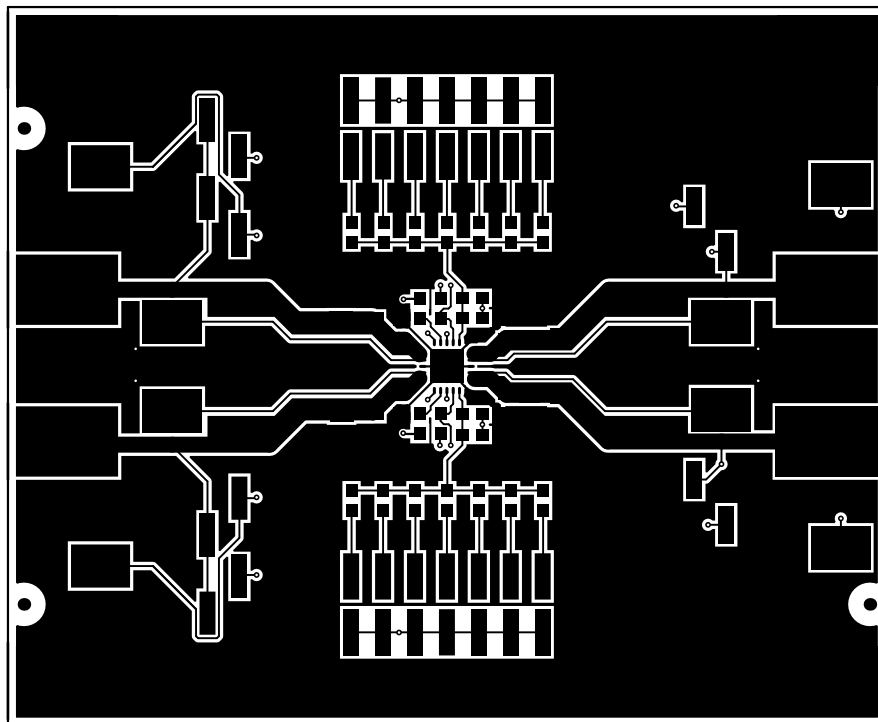


Figure 2. Top Layer Routing

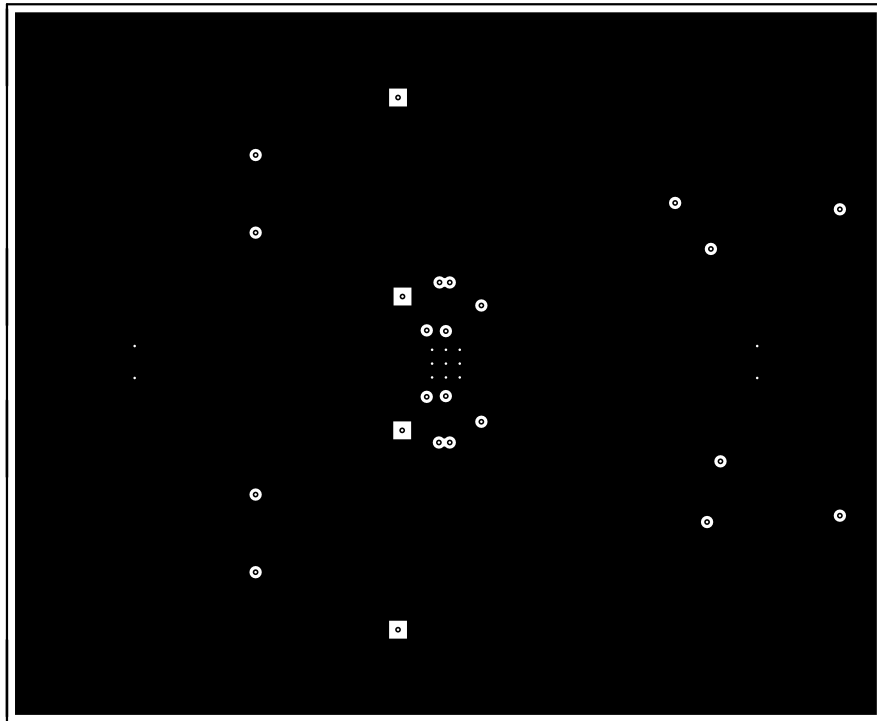


Figure 3. Layer 2 Routing

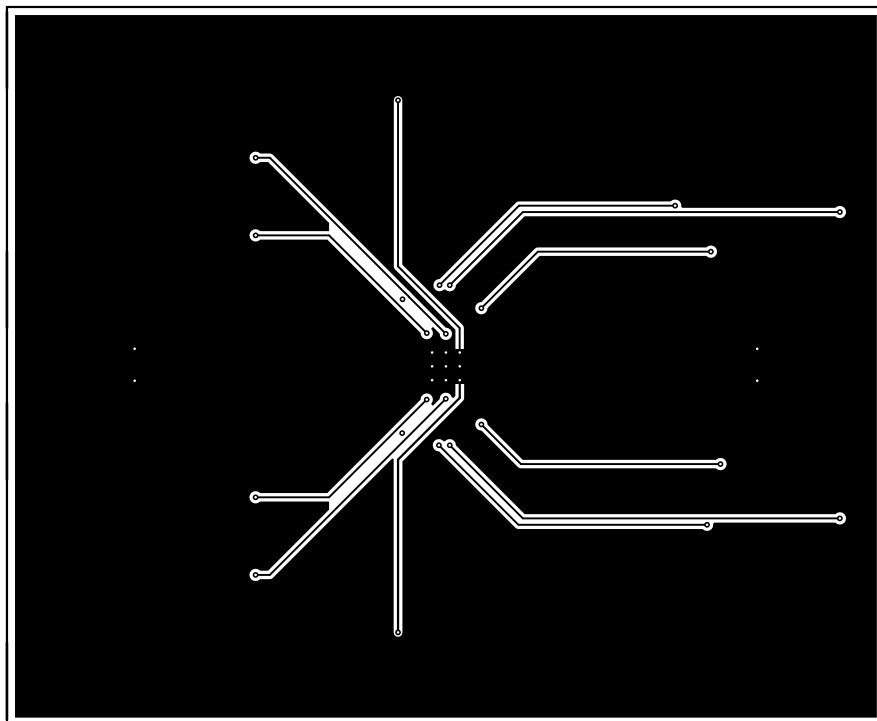


Figure 4. Layer 3 Routing

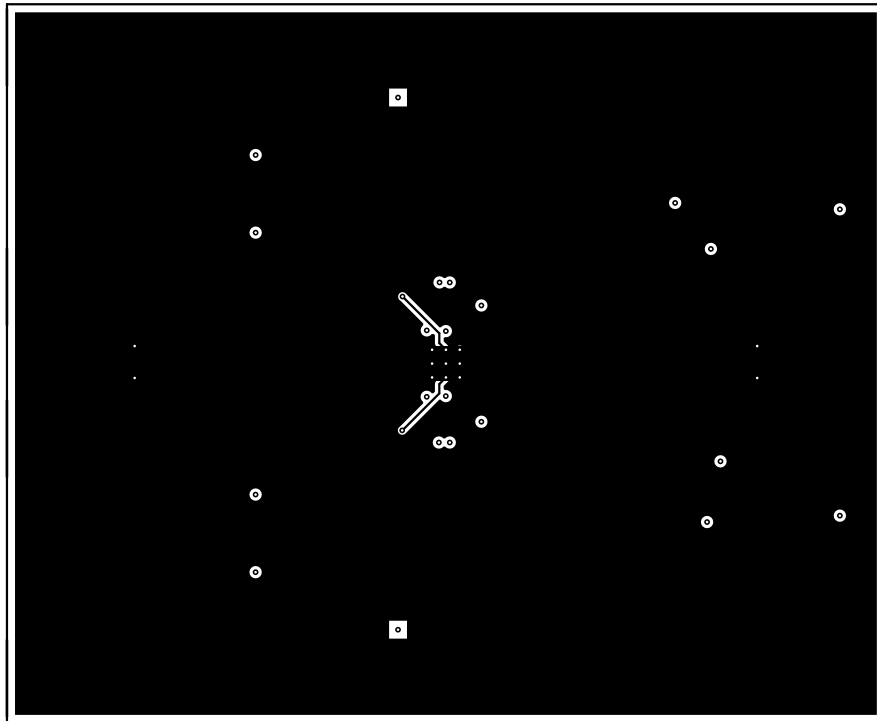
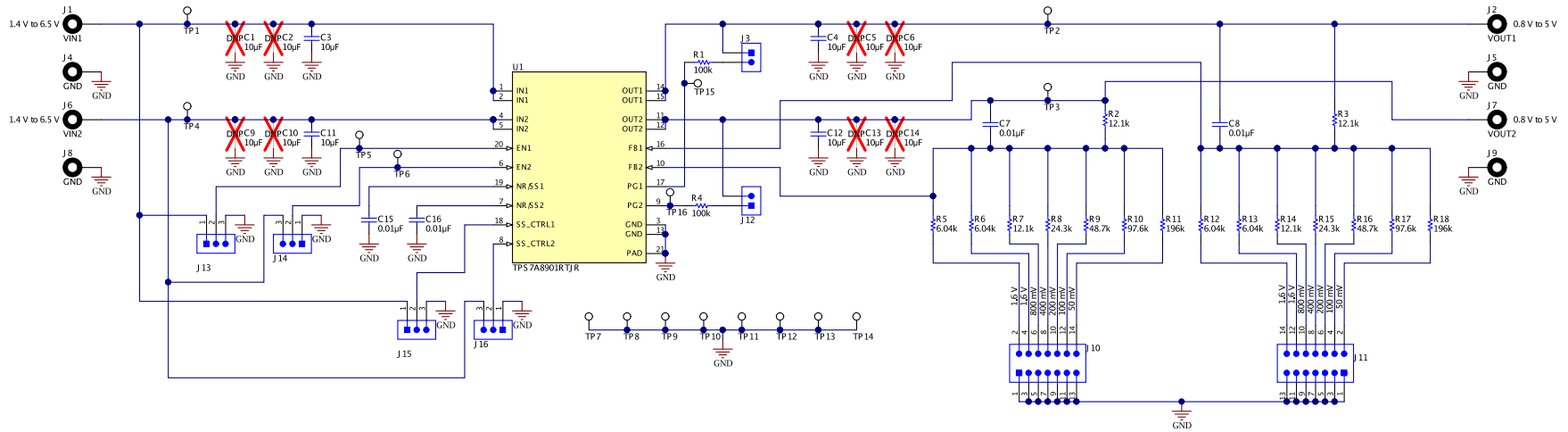


Figure 5. Bottom Layer Routing

5 Schematic

Figure 6 is the schematic for this EVM.



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Figure 6. Schematic

6 Bill of Materials

The bill of materials (BOM) for this EVM is shown in [Table 1](#).

Table 1. Bill of Materials⁽¹⁾

Designator	Qty	Value	Description	Package Reference	Part Number	Manufacturer	Alternate Part Number	Alternate Manufacturer
!PCB1	1		Printed Circuit Board		PWR853	Any	-	-
C3, C4, C11, C12	4	10uF	CAP, CERM, 10 μ F, 10 V, +/- 20%, X5R, 0603	0603	GRM188R61A106ME69	Murata		
C7, C8, C15, C16	4	0.01uF	CAP, CERM, 0.01 μ F, 16 V, +/- 10%, X7R, 0603	0603	GRM188R71C103KA01D	Murata		
J1, J2, J6, J7	4		Standard Banana Jack, Insulated, Red	6091	6091	Keystone		
J3, J12	2		Header, 2.54 mm, 2x1, Gold, R/A, SMT	Header, 2.54 mm, 2x1, R/A, SMT	87898-0204	Molex		
J4, J5, J8, J9	4		Standard Banana Jack, Insulated, Black	6092	6092	Keystone		
J10, J11	2		Header, 100mil, 7x2, SMT	Header, 100 mil, 7x2, SMT	0015912140	Molex		
J13, J14, J15, J16	4		Header, 100mil, 3x1, Gold, SMT	Samtec_TSM-103-01-X-SV	TSM-103-01-L-SV	Samtec		
R1, R4	2	100k	RES, 100 k, 1%, 0.1 W, 0603	0603	CRCW0603100KFKEA	Vishay-Dale		
R2, R3, R7, R14	4	12.1k	RES, 12.1 k, 1%, 0.1 W, 0603	0603	CRCW060312K1FKEA	Vishay-Dale		
R5, R6, R12, R13	4	6.04k	RES, 6.04 k, 1%, 0.1 W, 0603	0603	CRCW06036K04FKEA	Vishay-Dale		
R8, R15	2	24.3k	RES, 24.3 k, 1%, 0.1 W, 0603	0603	CRCW060324K3FKEA	Vishay-Dale		
R9, R16	2	48.7k	RES, 48.7 k, 1%, 0.1 W, 0603	0603	CRCW060348K7FKEA	Vishay-Dale		
R10, R17	2	97.6k	RES, 97.6 k, 1%, 0.1 W, 0603	0603	CRCW060397K6FKEA	Vishay-Dale		
R11, R18	2	196k	RES, 196 k, 1%, 0.1 W, 0603	0603	CRCW0603196KFKEA	Vishay-Dale		
SH-J1, SH-J2, SH-J3, SH-J4, SH-J5, SH-J6, SH-J7, SH-J8, SH-J9, SH-J10, SH-J11	11	1x2	Shunt, 100mil, Gold plated, Black	Shunt	969102-0000-DA	3M	SNT-100-BK-G	Samtec
TP1, TP2, TP3, TP4, TP5, TP6, TP7, TP8, TP9, TP10, TP11, TP12, TP13, TP14, TP15, TP16	16	SMT	Test Point, Compact, SMT	Testpoint_Keystone_Compact	5016	Keystone		
U1	1		Small, Dual, 2-A, Low-Noise (3.8 μ VRMS), LDO Voltage Regulator, RTJ0020D	RTJ0020D	TPS7A8901RTJR	Texas Instruments	TPS7A8901RTJT	Texas Instruments
C1, C6, C9, C14	0	10uF	CAP, CERM, 10 μ F, 10 V, +/- 10%, X5R, 1206	1206	GRM31CR71A106KA01L	Murata		
C2, C5, C10, C13	0	10uF	CAP, CERM, 10 μ F, 10 V, +/- 10%, X5R, 0805	0805	GRM21BR61A106KE19L	Murata		
FID1, FID2, FID3	0		Fiducial mark. There is nothing to buy or mount.	Fiducial	N/A	N/A		

⁽¹⁾ Unless otherwise noted in the Alternate Part Number or Alternate Manufacturer columns, all parts may be substituted with equivalents.

STANDARD TERMS AND CONDITIONS FOR EVALUATION MODULES

1. *Delivery:* TI delivers TI evaluation boards, kits, or modules, including demonstration software, components, and/or documentation which may be provided together or separately (collectively, an "EVM" or "EVMs") to the User ("User") in accordance with the terms and conditions set forth herein. Acceptance of the EVM is expressly subject to the following terms and conditions.
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 - 1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.
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 - 2.1 These terms and conditions do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.
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 - 2.3 If any EVM fails to conform to the warranty set forth above, TI's sole liability shall be at its option to repair or replace such EVM, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.
3. *Regulatory Notices:*
 - 3.1 *United States*
 - 3.1.1 *Notice applicable to EVMs not FCC-Approved:*

This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.
 - 3.1.2 *For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:*

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

3.3 Japan

3.3.1 *Notice for EVMs delivered in Japan:* Please see http://www.tij.co.jp/lstds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。
http://www.tij.co.jp/lstds/ti_ja/general/eStore/notice_01.page

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required by Radio Law of Japan to follow the instructions below with respect to EVMs:

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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3. 技術基準適合証明を取得後ご使用いただく。

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4 *EVM Use Restrictions and Warnings:*

- 4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.
- 4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.
- 4.3 *Safety-Related Warnings and Restrictions:*
 - 4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.
 - 4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.
- 4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User's handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.

5. *Accuracy of Information:* To the extent TI provides information on the availability and function of EVMs, TI attempts to be as accurate as possible. However, TI does not warrant the accuracy of EVM descriptions, EVM availability or other information on its websites as accurate, complete, reliable, current, or error-free.

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